

Digital VCXO

- 9.6 x 11.4 SMD
- * CMOS Output
- * 1 MHz - 200 MHz
- * 3.3 Volt

Applications

- * Digital PLL
- * Digital tuning
- * Test Equipment

Series **CDVP**



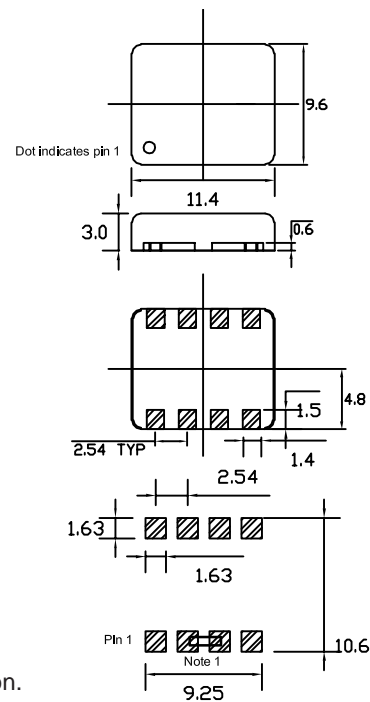
Part Numbering Example: CDVP Z 100.0

CDVP	Z	100.0
SERIES	ADDED FEATURES	FREQUENCY
Blank = Bulk		1.0 – 200.0
T = Tube		
Z = Tape/Reel		

Specifications:	Min	Typ	Max	Unit
Frequency Range:	1.0		200.0	MHz
Stability Options	-50		50	ppm
Programmable Input Voltage	3.135	3.3	3.465	V
Operating Temperature Range Options:	-40		85	°C
Storage Temperature:	-55		125	°C
Aging (PPM/1st Year): Ta=25C, Vdd=3.3V			± 5	
Digital Pull Range	-70		120	ppm
Supply Current		40		mA
Start-Up Time		3	10	ms
Output Level:	CMOS			

Frequency Controlled via I²C interface. Consult Cardinal for configuration information.

CDVP



Recommended solder pad layout
Note 1: Connect pin 2 to pin 3

- PIN FUNCTION**
- PIN 1 OE
 - PIN 2 CONNECT TO PIN 3
 - PIN 3 CONNECT TO PIN 2
 - PIN 4 GND
 - PIN 5 OUTPUT
 - PIN 6 SCLK
 - PIN 7 SDAT
 - PIN 8 VDD

Notes: Recommended .01 μF bypass capacitor from Vcc to GND. Capacitor should be as close to oscillator as possible.

