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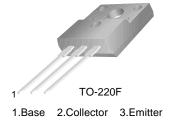
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### KSD2012

# Low Frequency Power Amplifier • Complement to KSB1366



### **NPN Epitaxial Silicon Transistor**

### Absolute Maximum Ratings T<sub>C</sub>=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V <sub>CBO</sub>	Collector-Base Voltage	60	V
V <sub>CEO</sub>	Collector-Emitter Voltage	60	V
V <sub>EBO</sub>	Emitter-Base Voltage	7	V
I <sub>C</sub>	Collector Current	3	А
I <sub>B</sub>	Base Current	0.3	Α
P <sub>C</sub>	Collector Power Dissipation (T <sub>C</sub> =25°C)	25	W
T <sub>J</sub>	Junction Temperature	150	°C
T <sub>STG</sub>	Storage Temperature	- 55 ~ 150	°C

### Electrical Characteristics T<sub>C</sub>=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage	$I_C = 50 \text{mA}, I_B = 0$	60			V
I <sub>CBO</sub>	Collector Cut-off Current	$V_{CB} = 60V, I_{E} = 0$			100	μΑ
I <sub>EBO</sub>	Emitter Cut-off Current	$V_{EB} = 7V, I_{C} = 0$			10	μΑ
h <sub>FE1</sub>	DC Current Gain	$V_{CE} = 5V, I_{C} = 0.5A$	100		320	
h <sub>FE2</sub>		$V_{CE} = 5V, I_{C} = 3A$	20			
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage	$I_C = 2A, I_B = 0.2A$		0.4	1	V
V <sub>BE</sub> (on)	Base-Emitter ON Voltage	$V_{CE} = 5V, I_{C} = 0.5A$		0.7	1	V
f <sub>T</sub>	Current Gain Bandwidth Product	$V_{CE} = 5V, I_{C} = 0.5A$		3		MHz

### $h_{\text{FE}}$ Classification

Classification	Y	G	
h <sub>FE1</sub>	100 ~ 200	150 ~ 320	

# **Typical Characteristics**

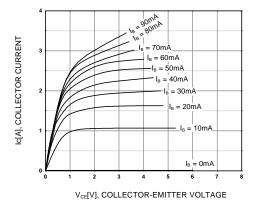


Figure 1. Static Characteristic

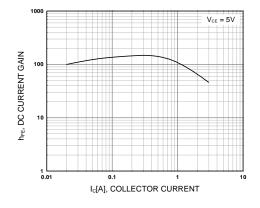


Figure 2. DC current Gain

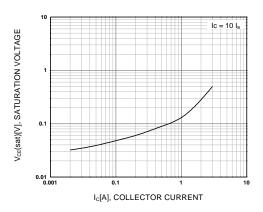


Figure 3. Collector-Emitter Saturation Voltage

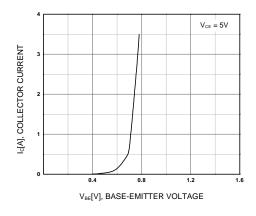


Figure 4. Base-Emitter On Voltage

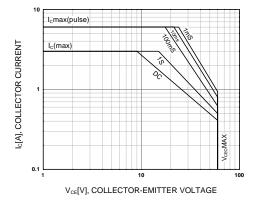


Figure 5. Safe Operating Area

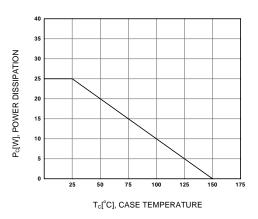
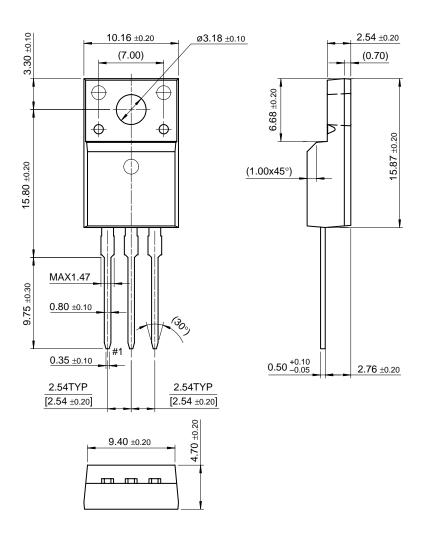


Figure 6. Power Derating

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# **Package Demensions**

## TO-220F



Dimensions in Millimeters

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No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.		
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