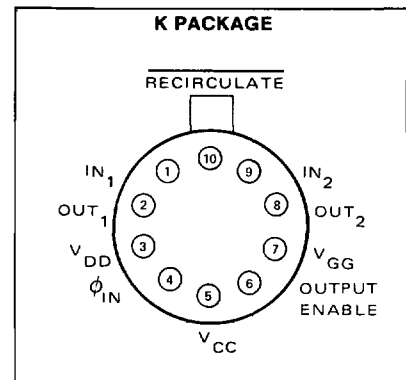
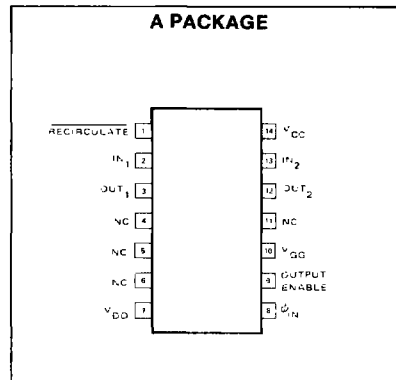


DESCRIPTION

The 2511 200-bit recirculating static shift register consists of enhancement mode P-channel silicon gate MOS devices integrated on a single monolithic chip. Internal recirculation logic plus TTL/DTL level clock signals and tri-state outputs are provided for maximum interfacing ease.

PIN CONFIGURATION



SWITCHING CHARACTERISTICS

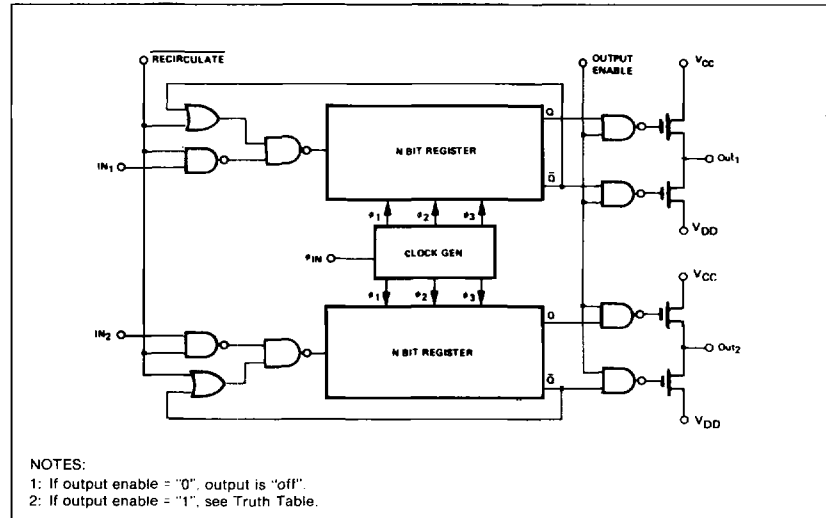
PARAMETER	LIMITS		UNIT
	MIN	MAX	
t_{DE} Disconnect		300	

TRUTH TABLE

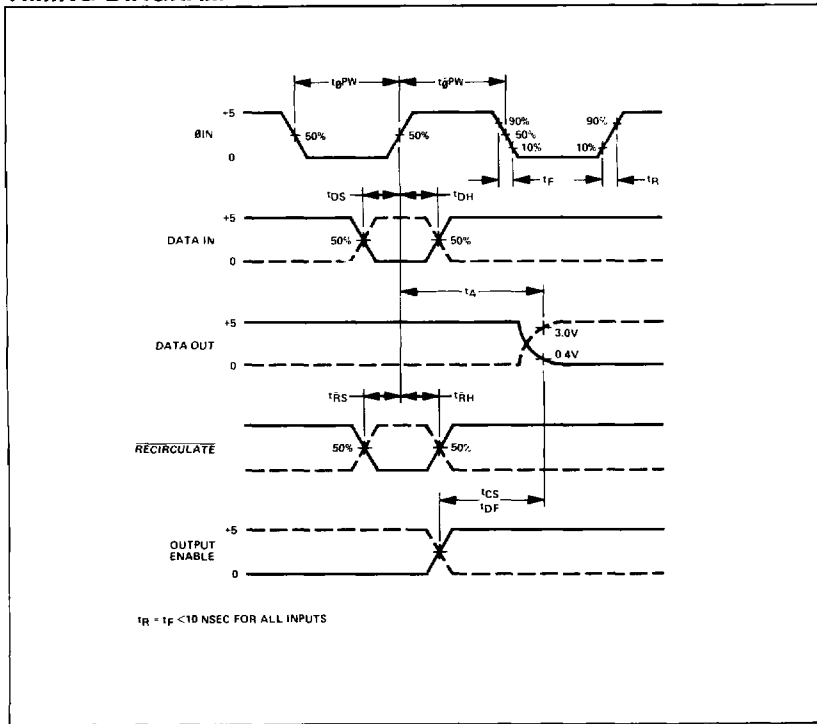
RECIRCULATE	INPUT	FUNCTION
0	0	Recirculate
0	1	Recirculate
1	0	"0" is Written
1	1	"1" is Written

NOTE: "0" = 0V; "1" = +5V.

BLOCK DIAGRAM



TIMING DIAGRAM



MEMORIES