

SCD8651

Dual LDO Adjustable Positive/Negative Voltage Regulators

VRG8651/52

Features

- Manufactured using Linear Technologies Space Qualified RH1086 and RH1185 die
- Radiation performance
 - Total dose ≥ 100 krad(Si),
Dose rate = 50-300 rad(Si)/s
- Two-Independent voltage regulators
- Thermal shutdown
- Adjustable Output Voltages
- **Positive** regulator features (RH1086)
 - Output voltage adjustable: 1.25V to 23V
 - Dropout voltage: 1.3V at 1.0 Amps
 - 3-Terminal
 - Output current: 1.0A, See note 13
 - Voltage reference: 1.25V +2%, -3.2%
 - Load regulation: 0.3% max
 - Line regulation: 0.25% max
 - Ripple rejection: >60dB
- **Packaging**
 - Hermetic metal
 - Thru-hole or Surface mount
 - 8 Leads, .755"L x .415"W x .200"Ht
 - Power package
 - Weight - 6 gm max
- Designed for aerospace and high reliability space applications
- **Negative** regulator features (RH1185)
 - Output voltage adjustable: -2.37 to -25V
 - Dropout voltage: 1.05V at 3Amps
 - 5-Terminal
 - Output current: 3A
 - Voltage reference: -2.370V $\pm 3\%$
 - Load regulation: 0.8% max
 - Line regulation: 0.02% max
 - Ripple rejection: >60dB
- **Radiation Hardness Assurance Plan: DLA Certified to MIL-PRF-38534, Appendix G.**

Description

The VRG8651/8652 consists of one Positive Adjustable (RH1086) and one Negative Adjustable (RH1185) LDO voltage regulator capable of supplying 1.0Amps (RH1086) / 3Amps (RH1185) respectively over the output voltage range as defined under recommended operating conditions. The VRG8651/8652 offers excellent line and load regulation specifications and ripple rejection. There is full electrical isolation between the regulators and each regulator to the package.

The VRG8651/8652 serves a wide variety of applications including SCSI-2 Active Terminator, High Efficiency Linear Regulators, Post Regulators for Switching Supplies, Constant Current Regulators, Battery Chargers and Microprocessor Supply.

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The VRG8651/8652 has been specifically designed to meet exposure to radiation environments. The VRG8651 is configured for a Thru-Hole 8 lead metal power package and the VRG8652 is configured for a Surface Mount 8 lead metal power package. It is guaranteed operational from -55°C to +125°C. Available screened to MIL-STD-883, the VRG8651/8652 is ideal for demanding military and space applications.

Dropout ($V_{IN} - V_{OUT}$) decreases at lower load currents for both regulators.

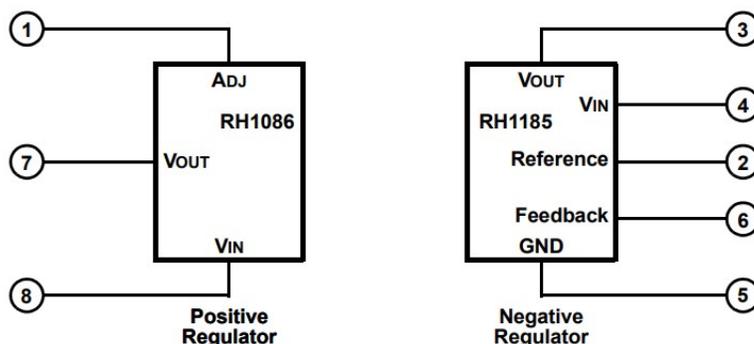


Figure 1 - Block Diagram / Schematic

Absolute Maximum Ratings

Parameter	Range		Units
	RH1086	RH1185	
Input Voltage	25+ V_{REF}	-35	V_{DC}
Lead temperature (soldering 10 Sec)	300		°C
Input Output Differential	25	30	V_{DC}
Feedback & Reference Voltage	-	-7	V_{DC}
Output Voltage	+25	-30	V_{DC}
DC Output Current	1.5	-	A
ESD	2000 $\frac{1}{}$		V
Operating Junction Temperature Range	-55 to +150		°C
Storage Temperature Range	-65 to +150		°C

$\frac{1}{}$ Meets ESD testing per MIL-STD-883, method 3015, Class 1C.

Notice: Stresses above those listed under "Absolute Maximums Rating" may cause permanent damage to the device. These are stress rating only; functional operation beyond the "Operation Conditions" is not recommended and extended exposure beyond the "Operation Conditions" may effect device reliability.

Recommended Operating Conditions

Parameter	Range		Units
	RH1086	RH1185	
Output Voltage Range	1.275 to 23	-2.45 to -25	V_{DC}
Input Output Differential	1.5 to 25	1 to 28	V_{DC}
Case Operating Temperature Range	-55 to +125		°C
Thermal Resistance, Junction to case θ_{JC}	5		°C/W

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Electrical Performance CharacteristicsUnless otherwise specified, these specifications apply for post radiation and $-55^{\circ}\text{C} \leq T_c \leq +125^{\circ}\text{C}$.

Parameter	SYM	Conditions ($P \leq P_{MAX}$)	MIN	MAX	Units
RH1086 Positive LDO section only					
Reference Voltage <u>2/ 3/</u>	V_{REF}	$1.5V \leq V_{IN} - V_{OUT} \leq 15V, I_{LOAD} = 10mA$	1.210	1.275	V
Line Regulation <u>2/ 3/</u>	$\frac{\Delta V_{OUT}}{\Delta V_{IN}}$	$I_{LOAD} = 10mA, 1.5V \leq V_{IN} - V_{OUT} \leq 15V$	-	0.25	%
Load Regulation <u>2/ 3/</u>	$\frac{\Delta V_{OUT}}{\Delta I_{OUT}}$	$10mA \leq I_{OUT} \leq 1.0A, V_{IN} - V_{OUT} = 3V$	-	0.3	%
Dropout Voltage <u>2/ 4/</u>	V_{DROP}	$\Delta V_{REF} = 1\%, I_{OUT} = 1.0A$	-	1.30	V
Adjust Pin Current <u>2/</u>	-		-	120	μA
Adjust Pin Current Change <u>2/</u>	-	$10 mA \leq I_{OUT} \leq 1.0A, 1.5V \leq V_{IN} - V_{OUT} \leq 15V$	-	5	μA
Current Limit <u>2/ 14/</u>	I_{MAX}	$V_{IN} - V_{OUT} = 5V$ $V_{IN} - V_{OUT} \leq 25V$	1.5 0.047	- -	A A
Minimum Load Current <u>5/</u>	I_{MIN}	$V_{IN} - V_{OUT} = 25V$	-	10	mA
Ripple Rejection <u>3/</u>	-	$I_{OUT} = 1.0A, V_{IN} - V_{OUT} = 3V, f = 120Hz,$ $C_{ADJ} = C_{OUT} = 25\mu F$	60	-	dB
Thermal Regulation		30ms pulse, $T_c = +25^{\circ}\text{C}$	-	0.04	%/W
VREF Long-Term Stability <u>5/</u>		Burn In: $T_c = +125^{\circ}\text{C}$ @ 1000hrs min, tested @ 25°C	-	0.3	%

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Unless otherwise specified, these specifications apply for post radiation and $-55^{\circ}\text{C} \leq T_c \leq +125^{\circ}\text{C}$.

Parameter	SYM	Conditions ($P \leq P_{MAX}$)	MIN	MAX	Units
RH1185 Negative LDO section only 1/, 9/					
Reference Voltage (At pin 6) 2/ 9/	V_{REF}	$1\text{mA} \leq I_{OUT} \leq 3\text{A}$, $V_{IN} - V_{OUT} = 1.2\text{V}$ to 28V , $V_{OUT} = -5\text{V}$	-2.29	-2.45	V
Dropout Voltage 2/ 6/	V_{DROP}	$I_{OUT} = 0.5\text{A}$, $V_{OUT} = -5\text{V}$	-	0.425	V
		$I_{OUT} = 3\text{A}$, $V_{OUT} = -5\text{V}$	-	1.05	V
Line Regulation 2/ 10/	$\frac{\Delta V_{OUT}}{\Delta V_{IN}}$	$1.0\text{V} \leq V_{IN} - V_{OUT} \leq 20\text{V}$, $V_{OUT} = -5\text{V}$	-	0.02	%/V
Load Regulation 2/ 10/	$\frac{\Delta V_{OUT}}{\Delta I_{OUT}}$	$5\text{mA} \leq I_{OUT} \leq 3\text{A}$, $V_{IN} - V_{OUT} = 1.5\text{V}$ to 10V , $V_{OUT} = -5\text{V}$	-	0.8	%
Minimum Input Voltage 2/ 7/	$V_{IN\ MIN}$	$I_{OUT} = 3\text{A}$, $V_{OUT} = V_{REF}$	-	-4.50	V
Internal Current Limit (See Graph for Guaranteed Curve- See Figure 6) 2/ 14/	I_{CL}	$1.5\text{V} \leq V_{IN} - V_{OUT} \leq 10\text{V}$	3.3	4.55	A
		$V_{IN} - V_{OUT} = 15\text{V}$	2.0	4.5	A
		$V_{IN} - V_{OUT} = 20\text{V}$	1.0	3.1	A
		$V_{IN} - V_{OUT} = 30\text{V}$ 5/	0.2	1.6	A
External Current Limit 2/	I_{LIM}	$R_{LIM} = 5\text{K}\Omega$ 12/	2.7	3.7	A
		$R_{LIM} = 15\text{K}\Omega$ 12/	0.9	1.6	A
Quiescent Supply Current 2/ 8/	I_Q	$I_{OUT} = 5\text{mA}$, $V_{OUT} = V_{REF}$, $-4\text{V} \leq V_{IN} \leq -25\text{V}$	-	3.5	mA
Supply Current Change with Load 2/	$I_{Q\Delta}$	$V_{IN} - V_{OUT} = \text{VSAT}$ 11/	-	35	mA/A
		$V_{IN} - V_{OUT} \geq 2\text{V}$	-	21	mA/A
Ripple Rejection	-	$I_{OUT} = 1.0\text{A}$, $V_{IN} - V_{OUT} = 3\text{V}$, $f = 120\text{Hz}$,	60	-	dB
Thermal Regulation (See application information LT1185) 5/	-	$V_{IN} - V_{OUT} = 10\text{V}$, $I_{OUT} = 5\text{mA}$ to 2A , $T_C = +25^{\circ}\text{C}$	-	0.014	%/W

Notes

- 1) Meets ESD testing per MIL-STD-883, method 3015, Class 2.
- 2) Specification derated to reflect High Dose Rate (1019 condition A) to 100 krad(Si) @ +25°C.
- 3) Line and load regulation are guaranteed up to the maximum power dissipation of 15W. Power dissipation is determined by the input/output differential voltage and the output current. Guaranteed maximum power dissipation will not be available over the full input/output voltage range.
- 4) Dropout voltage is specified over the full output current range of the device.
- 5) Not tested. Shall be guaranteed by design, characterization, or correlation to other tested parameters.
- 6) Dropout voltage is tested by reducing input voltage until the output drops 1% below its nominal value. Tests are done at 0.5A and 3A. The power transistor looks basically like a pure resistance in this range so that minimum differential at any intermediate current can be calculated by interpolation; $V_{DROPOUT} = 0.25\text{V} + (0.25\Omega \times I_{OUT})$. For load current less than 0.5A, see Figure 4.
- 7) "Minimum input voltage" is limited by base emitter voltage drive of the power transistor section, not saturation as measured in Note 6. For output voltages below 4V, "minimum input voltage" specification may limit dropout voltage before transistor saturation limitation.
- 8) Supply current is measured on the ground pin, and does not include load current, RLIM, or output divider current.

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- 9) The 25W power level is guaranteed for an input-output voltage of 8.3V to 17V. At lower voltages the 3A limit applies, and at higher voltages the internal power limiting may restrict regulator power below 25W.
- 10) Line and load regulation are measured on a pulse basis with a pulse width of 2ms, to minimize heating. DC regulation will be affected by thermal regulation and temperature coefficient of the reference.
- 11) VSAT is the maximum specified dropout voltage; $0.25V + (0.25\Omega \times IOUT)$.
- 12) Current limit is programmed with a resistor from REF pin to GND pin. $RLIM = 15K\Omega/ILIM$.
- 13) For compliance with MIL-STD- 883 revision C current density specification, the RH1086MK is derated to 1 Amp but is capable of 1.5 Amps.
- 14) Pulsed @ <10% duty cycle @ +25°C.

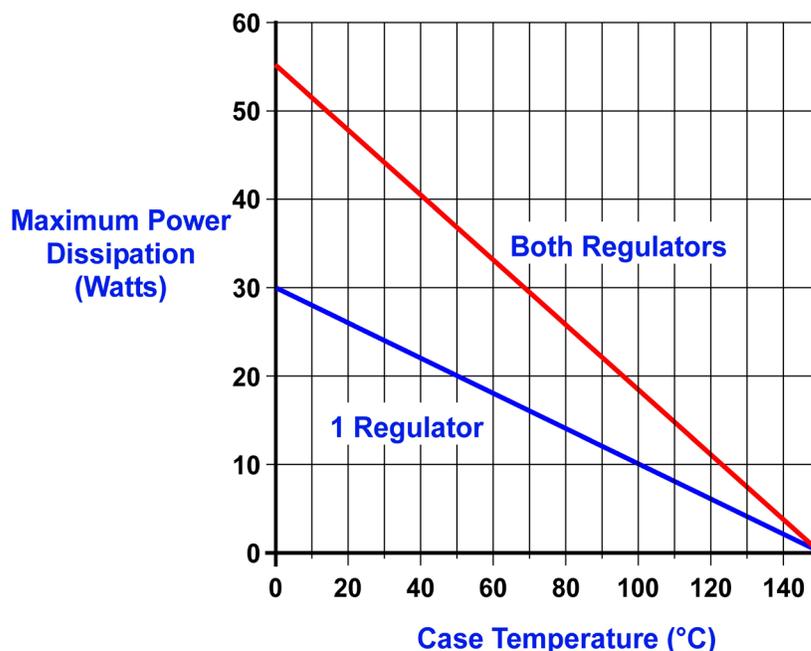


Figure 2 - Maximum Power Vs Case Temperature

The maximum Power dissipation is limited by the thermal shutdown function of each regulator chip in the VRG8651/8652. The graph above represents the achievable power before the chip shuts down. The first line in the graph represents the maximum power dissipation of the VRG8651/8652 with one regulator on (the other off) and the other line represents both regulators on dissipating equal power. If both regulators are on and one regulator is dissipating more power than the other, the maximum power dissipation of the VRG8651/8652 will fall between the two lines. This graph is based on the maximum junction temperature of 150°C and a thermal resistance (θ_{JC}) of 5°C/W.

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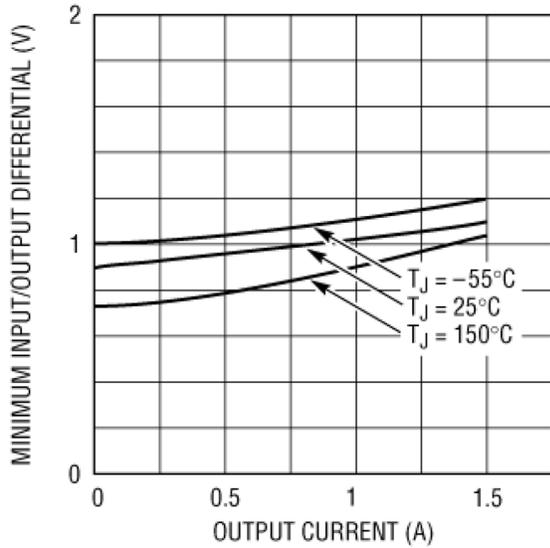


Figure 3 - RH1086 Dropout Voltage Typical Curve

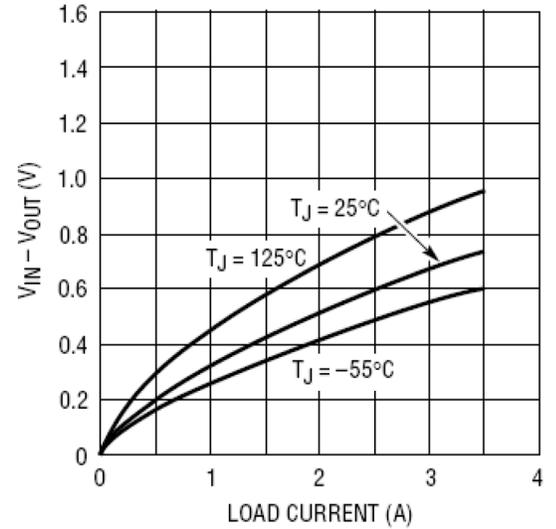


Figure 4 - RH1185 Dropout Voltage Typical Curve

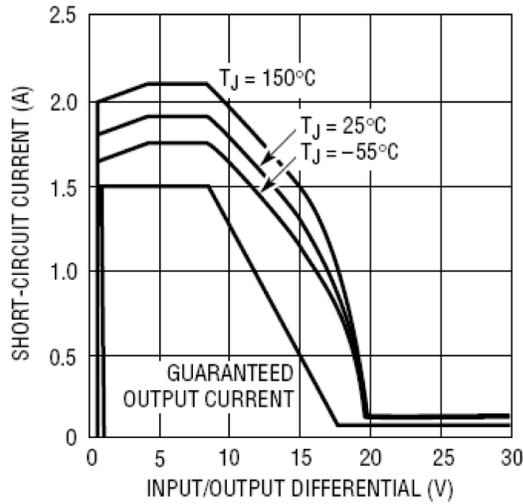


Figure 5 - RH1086 Short Circuit Current

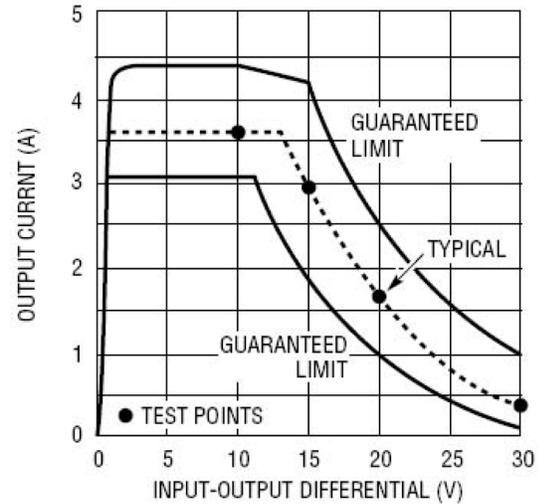


Figure 6 - RH1185 Internal Current Limit

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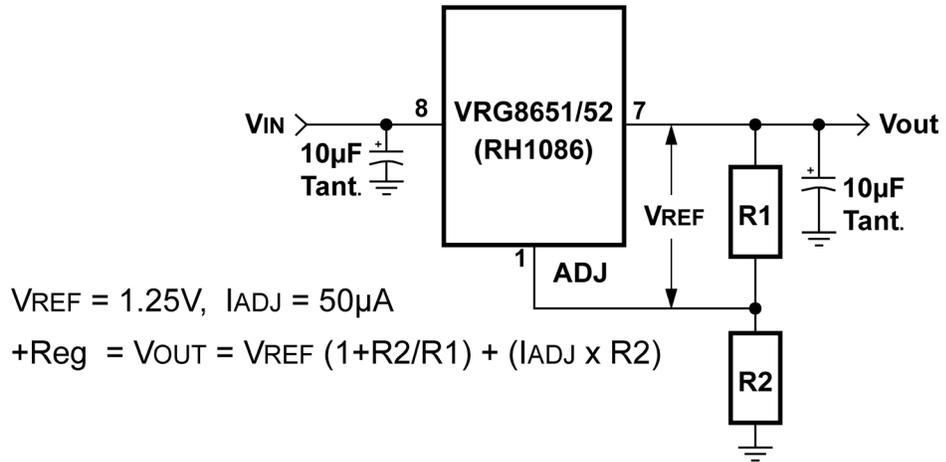


Figure 7 - Basic RH1086 Adjustable Regulator Application

The RH1185 output voltage is set by two external resistors. Internal reference voltage is trimmed to 2.37V so that a standard 1% 2.37k resistor (R1) can be used to set divider current at 1mA. R2 is then selected from:

$$R2 = \frac{(V_{OUT} - 2.37)R1}{V_{REF}}$$

for R1 = 2.37k and VREF = 2.37V, this reduces to:

$$R2 = \frac{V_{OUT} - 2.37}{10^{-3}}$$

suggested values of 1% resistors are shown.

V _{OUT}	R2 WHEN R1 = 2.37k
2.5V	130Ω
3.3V	930Ω
5V	2.67k
12V	9.76k
15V	12.7k

Setting Output Voltage

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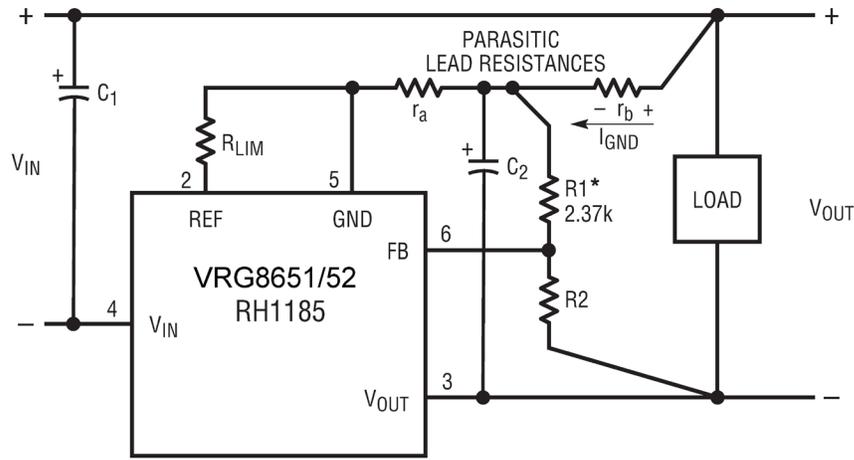


Figure 8 - Basic RH1185 Adjustable Regulator Application

* R1 should be connected directly to ground lead, not to the load, so that $r_a \approx 0\Omega$. This limits the output voltage error to $(I_{GND})(r_b)$. Errors created by r_a are multiplied by $(1 + R2/R1)$.

Note that V_{OUT} increases with increasing ground pin current. R2 should be connected directly to load for remote sensing. $C1 = C2 \geq 2\mu\text{F}$ Tantalum.

R1 & R2 Location & proper connection of positive sense lead

Table I - Pin Numbers vs Function

Pin	Function
1	POS_ADJ_1
2	NEG_REF_2
3	NEG_VOUT_2
4	NEG_VIN_2
5	NEG_GND_2
6	NEG_FB_2
7	POS_VOUT_1
8	POS_VIN_1

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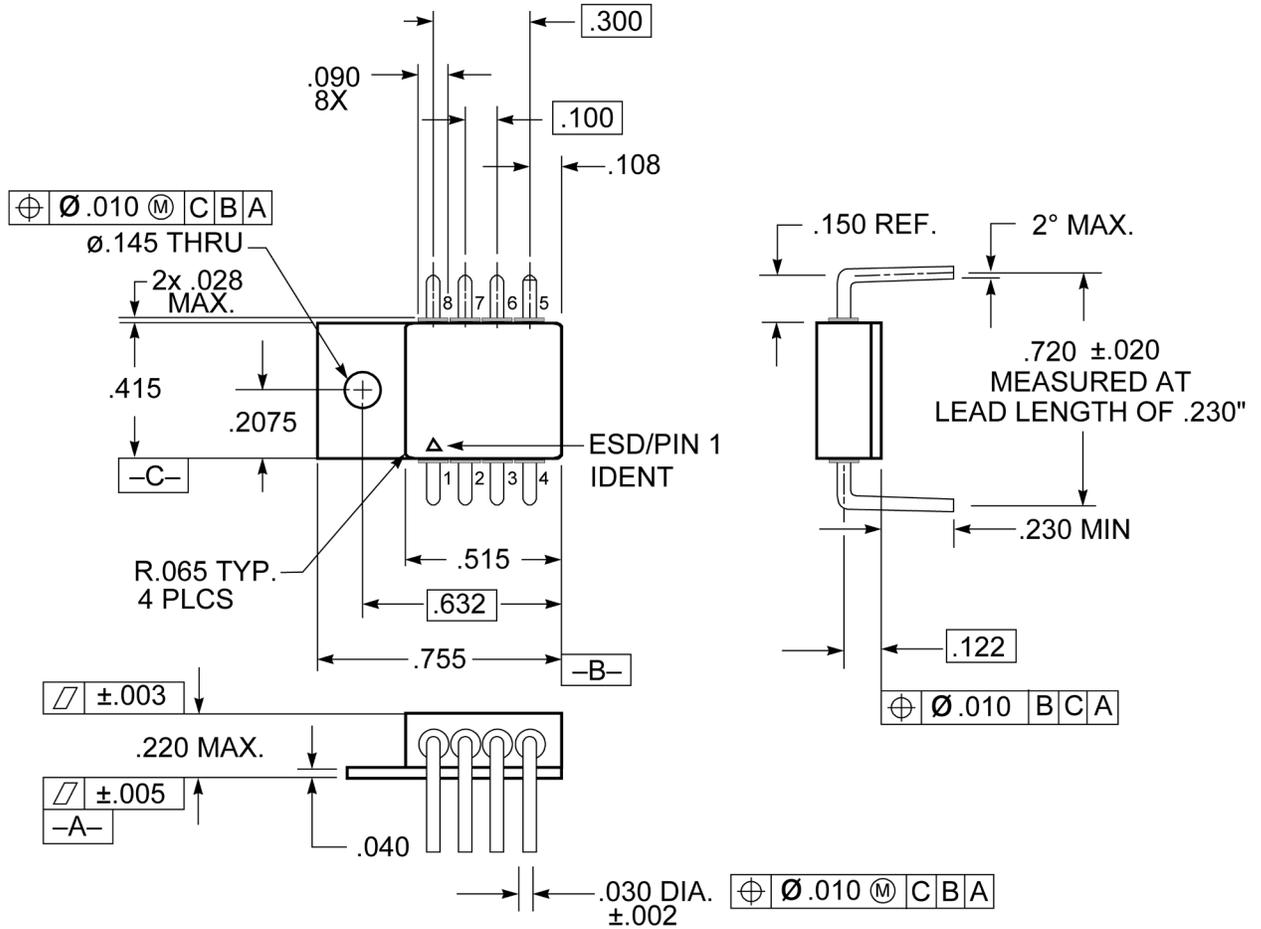


Figure 9 - Package Outline — Thru-Hole Power Package

Notes:

- 1) Dimension Tolerance: ±.005 inches
- 2) Package contains BeO substrate
- 3) Case electrically isolated

Table II - Pin Numbers vs Function

Pin	Function
1	POS_ADJ_1
2	NEG_REF_2
3	NEG_V _{OUT_2}
4	NEG_V _{IN_2}
5	NEG_GND_2
6	NEG_FB_2
7	POS_V _{OUT_1}
8	POS_V _{IN_1}

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Ordering Information

Model	DLA SMD #	Screening	Package
VRG8651-7	-	Commercial Flow, +25°C testing only	8 Lead Thru-Hole Power Pkg
VRG8651-S	-	Military Temperature, -55°C to +125°C Screened in accordance with the individual Test Methods of MIL-STD-883 for Space Applications	
VRG8651-201-1S	5962-0920101KUC	In accordance with DLA SMD	
VRG8651-201-2S	5962-0920101KUA		
VRG8651-901-1S	5962R0920101KUC	In accordance with DLA Certified RHA Program Plan to RHA Level "R", 100 krad(Si)	
VRG8651-901-2S	5962R0920101KUA		
VRG8652-7	-	Commercial Flow, +25°C testing only	8 Lead Surface Mount Power Pkg
VRG8652-S	-	Military Temperature, -55°C to +125°C Screened in accordance with the individual Test Methods of MIL-STD-883 for Space Applications	
VRG8652-201-1S	5962-0920101KZC	In accordance with DLA SMD	
VRG8652-201-2S	5962-0920101KZA		
VRG8652-901-1S	5962R0920101KZC	In accordance with DLA Certified RHA Program Plan to RHA Level "R", 100 krad(Si)	
VRG8652-901-2S	5962R0920101KZA		

Revision History

Date	Revision	Change Description
03/24/2016	L	Import into CAES format
10/6/2016	M	Correct Figure 8 pinouts
12/14/2020	N	Revised per ECN 23408
03/26/2021	P	Revised per ECN 23566



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Datasheet Definitions

	DEFINITION
Advanced Datasheet	CAES reserves the right to make changes to any products and services described herein at any time without notice. The product is still in the development stage and the datasheet is subject to change . Specifications can be TBD and the part package and pinout are not final .
Preliminary Datasheet	CAES reserves the right to make changes to any products and services described herein at any time without notice. The product is in the characterization stage and prototypes are available.
Datasheet	Product is in production and any changes to the product and services described herein will follow a formal customer notification process for form, fit or function changes.

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