

ITE40F06/ITE40C06

POWERLINE N-CHANNEL IGBT WITH OPTIONAL ULTRAFAST DIODE

The ITE40X06 is a robust n-channel, enhancement mode insulated gate bipolar transistor (IGBT) designed for low power dissipation in a wide range of high voltage applications such as power supplies and motor drives. The high impedance gate simplifies gate drive considerations, allowing operation directly from low power control circuitry.

Fast rise and fall times allow very high frequency switching making the device suitable for modern systems employing ultrasonic switching.

Low saturation voltages minimise power dissipation, thereby reducing the cost of the overall system in which they are used.

Each device in the Powerline range is available with or without an integral anti-parallel ultrafast soft recovery diode, see Ordering Information.

Typical applications include high frequency inverters for motor control, welding and heating apparatus. The Powerline range of IGBTs is also applicable to switched mode and uninterruptible power supplies.

FEATURES

- Enhancement Mode n-Channel Device
- High Switching Speed
- Low On-state Saturation Voltage
- High Input Impedance Simplifies Gate Drive
- Latch-Free Operation
- Optionally Available With Integral Fast Recovery Diode

APPLICATIONS

- High Frequency Inverters
- Motor Control
- Switched Mode Power Supplies
- High Frequency Welding
- Heating/Cooking Apparatus

ORDERING INFORMATION

ITE40C06 (TO247 With fast recovery diode)
ITE40F06 (TO247 Without fast recovery diode)

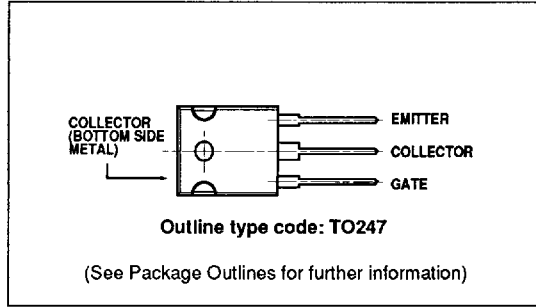


Fig.1 Pin connections - top view (not to scale)

IGBT KEY PARAMETERS

V_{CES}	600V
$V_{CE(sat)}$	2.5V
$I_{C(CONT)}$	40A
$I_{C(PK)}$	80A
t_r	150ns
t_f	200ns

DIODE KEY PARAMETERS

V_{RRM}	600V
I_F	40A
t_{rr}	40ns

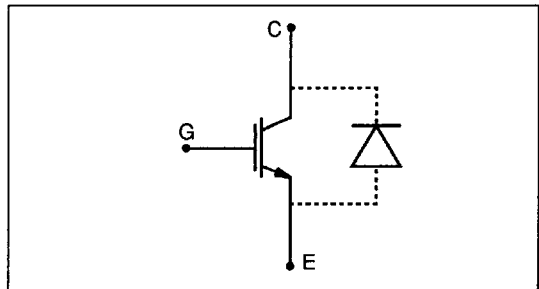


Fig.2 ITE40X06 circuit

IGBT ABSOLUTE MAXIMUM RATINGS

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to Absolute Maximum Ratings for extended periods may affect device reliability.

$T_{case} = 25^{\circ}\text{C}$ unless stated otherwise.

Symbol	Parameter	Test Conditions	Max.	Units
V_{CES}	Collector-emitter voltage	$V_{GE} = 0\text{V}$	600	V
V_{GES}	Gate-emitter voltage	-	± 20	V
I_C	Collector current	DC, $T_{case} = 85^{\circ}\text{C}$	40	A
I_{CM}		1ms, $T_{case} = 85^{\circ}\text{C}$	80	A
P_{tot}	Power dissipation	$T_{case} = 85^{\circ}\text{C}$	100	W

THERMAL AND MECHANICAL RATINGS

Symbol	Parameter	Conditions	Min.	Max.	Units
$R_{\theta(j-c)}$ IGBT	Thermal resistance - IGBT	DC junction to case	-	0.65	$^{\circ}\text{C}/\text{W}$
$R_{\theta(j-c)}$ DIODE	Thermal resistance - Diode	DC junction to case	-	1.0	$^{\circ}\text{C}/\text{W}$
T_J	Junction temperature (IGBT/Diode)	-	-40	150	$^{\circ}\text{C}$
T_{op}/T_{stg}	Operating and storage temp. range (IGBT/Diode)	-	-40	150	$^{\circ}\text{C}$

IGBT DC ELECTRICAL CHARACTERISTICS

$T_{case} = 25^{\circ}\text{C}$ unless stated otherwise.

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Units
I_{CES}	Collector cut-off current	$V_{GE} = 0\text{V}$, $V_{CE} = 600\text{V}$	-	-	1	mA
I_{GES}	Gate leakage current	$V_{GE} = 15\text{V}$, $V_{CE} = 0\text{V}$	-	-	500	nA
$V_{GE(TH)}$	Gate threshold voltage	$I_C = 1\text{mA}$, $V_{GE} = V_{CE}$	3.0	4.5	6.0	V
$V_{CE(SAT)}$	Collector-emitter saturation voltage	$V_{GE} = 15\text{V}$, $I_C = 40\text{A}$	-	2.2	2.5	V

ITE40X06

IGBT AC ELECTRICAL CHARACTERISTICS

$T_{case} = 25^{\circ}\text{C}$ unless stated otherwise.

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Units
C_{ies}	Input capacitance	$V_{CE} = 25\text{V}, V_{GE} = 15\text{V}, f = 1\text{MHz}$	-	3100	-	pF
C_{oes}	Output capacitance	$V_{CE} = 25\text{V}, V_{GE} = 15\text{V}, f = 1\text{MHz}$	-	800	-	pF
C_{res}	Reverse transfer capacitance	$V_{CE} = 25\text{V}, V_{GE} = 15\text{V}, f = 1\text{MHz}$	-	700	-	pF

IGBT RESISTIVE SWITCHING CHARACTERISTICS

$T_{case} = 25^{\circ}\text{C}$ unless stated otherwise.

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Units
$t_{d(ON)}$	Turn-on delay time	$I_C = 40\text{A},$ $V_{GE} = \pm 15\text{V},$ $V_{CE} = 50\%V_{CES}$ $R_{G(ON)} = R_{G(OFF)} = 5\Omega$	-	150	-	ns
t_r	Rise time		-	200	-	ns
E_{ON}	Turn-on energy loss - per cycle		-	0.75	-	mWs
$t_{d(OFF)}$	Turn-off delay time		-	200	-	ns
t_f	Fall time		-	200	300	ns
E_{OFF}	Turn-off energy loss - per cycle		-	1.0	-	mWs

IGBT INDUCTIVE SWITCHING CHARACTERISTICS

$T_{case} = 25^{\circ}\text{C}$ unless stated otherwise.

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Units
$t_{d(off)}$	Turn-off delay time	$I_C = 40\text{A}, V_{GE} = \pm 15\text{V}$ $V_{CE} = 50\% V_{CES}$ $R_{G(ON)} = R_{G(OFF)} = 5\Omega$	-	300	-	ns
t_f	Fall time		-	170	200	ns
E_{OFF}	Turn-off energy loss - per cycle		-	0.9	-	mWs

DIODE ABSOLUTE MAXIMUM RATINGS

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Symbol	Parameter	Conditions	Max.	Units
V_{RRM}	Repetitive peak reverse voltage	$T_{case} = 150^{\circ}C$	600	V
I_F	Forward current	Half wave resistive load, $T_{case} = 85^{\circ}C$	40	A

DIODE CHARACTERISTICS

$T_{case} = 25^{\circ}C$ unless stated otherwise.

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Units
V_{FM}	Forward voltage	At $I_F = 40A$ peak	-	-	1.6	V
I_{RM}	Peak reverse current	At V_{RRM} , $T_{case} = 125^{\circ}C$	-	-	4.0	mA
t_r	Reverse recovery time	$I_F = 40A$, $di_{RR}/dt = 200A/\mu s$	-	40	-	ns
I_{RRM}	Reverse recovery current	$V_R = 50\%V_{RRM}$	-	20	-	A

MEASUREMENT OF SWITCHING TIMES AND POWER DISSIPATION

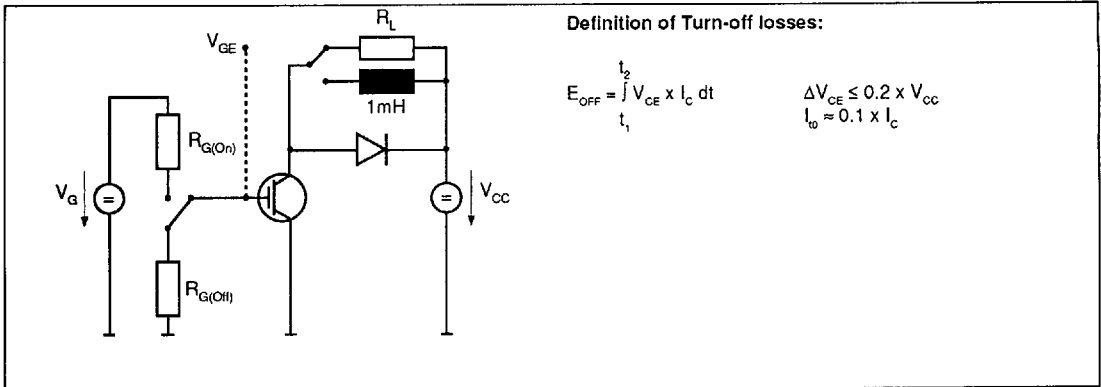


Fig.3 Test circuit

DEFINITION OF SWITCHING TIMES

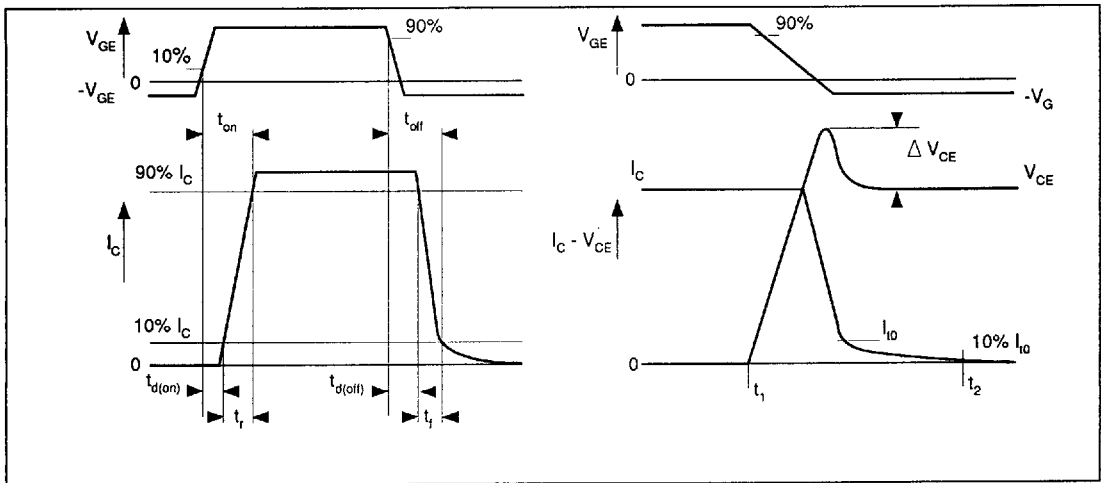


Fig.4 Definition of switching times

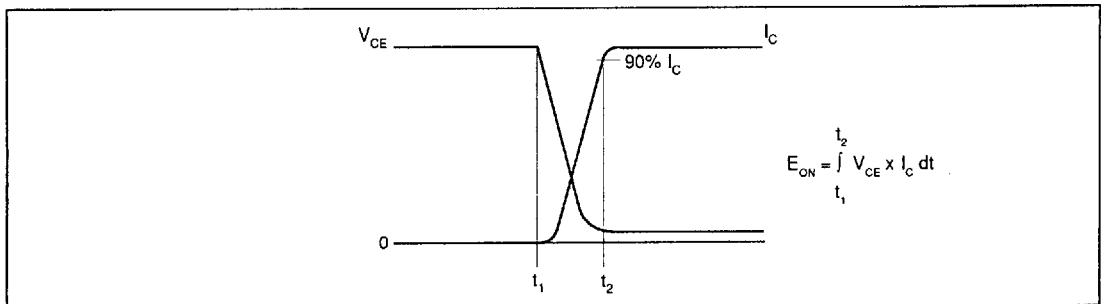


Fig.5 Definition of turn on losses

CURVES

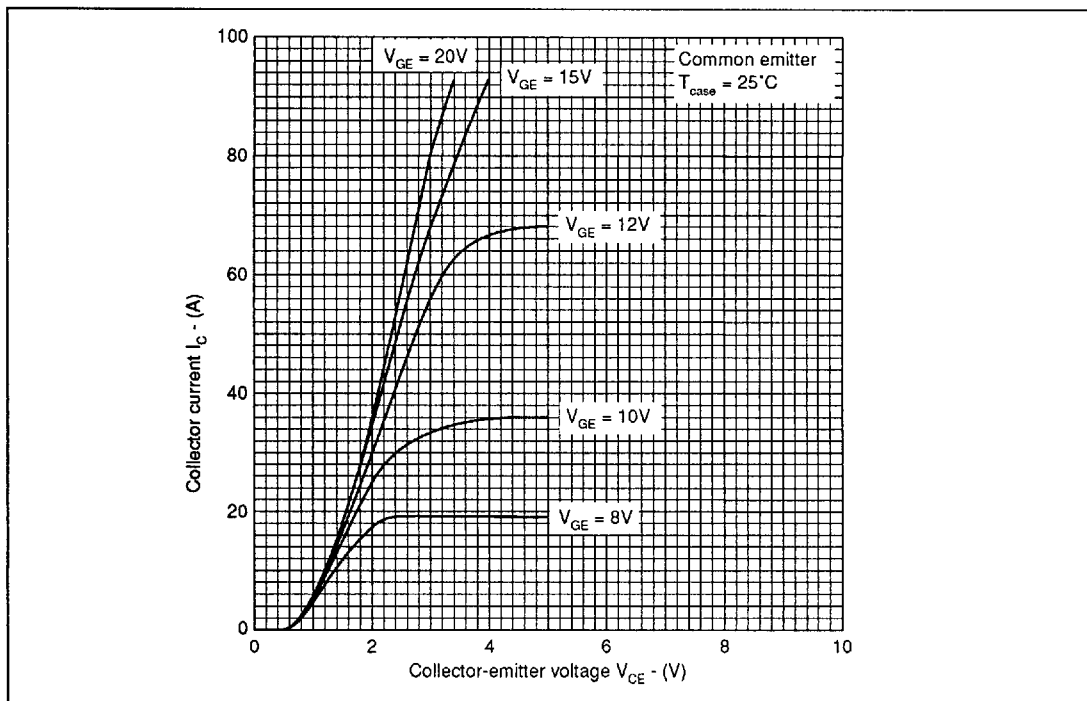


Fig.6 Typical output characteristics

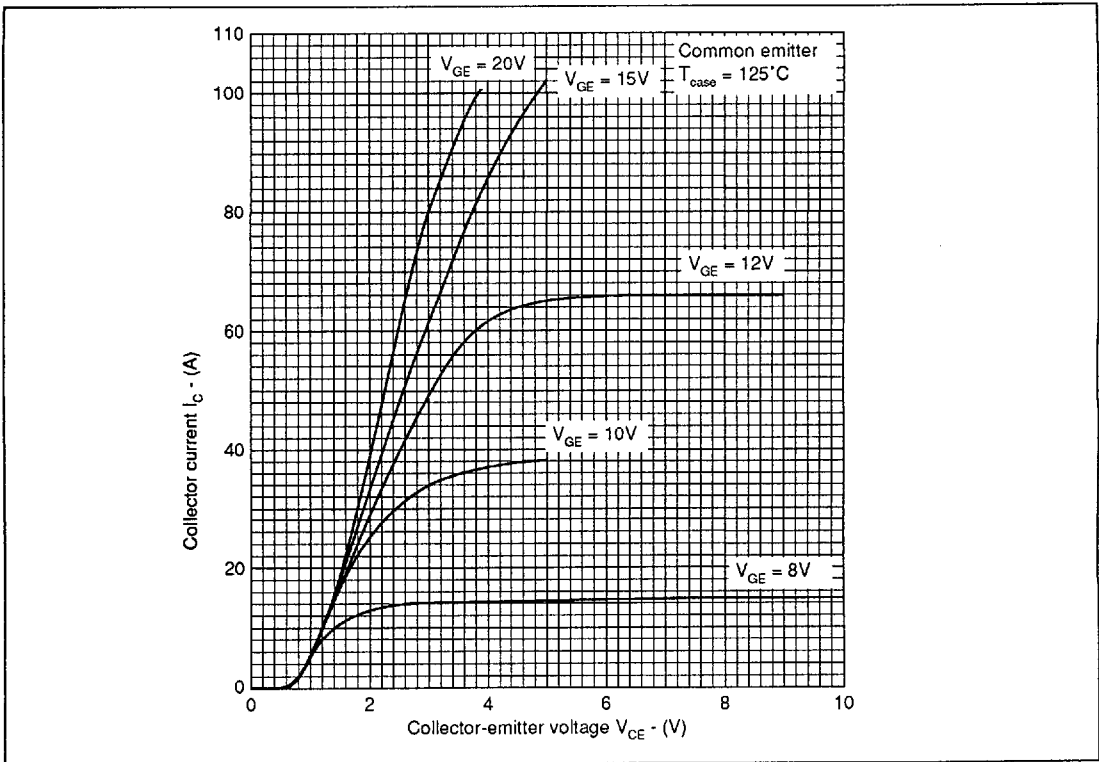


Fig. 7 Typical output characteristics

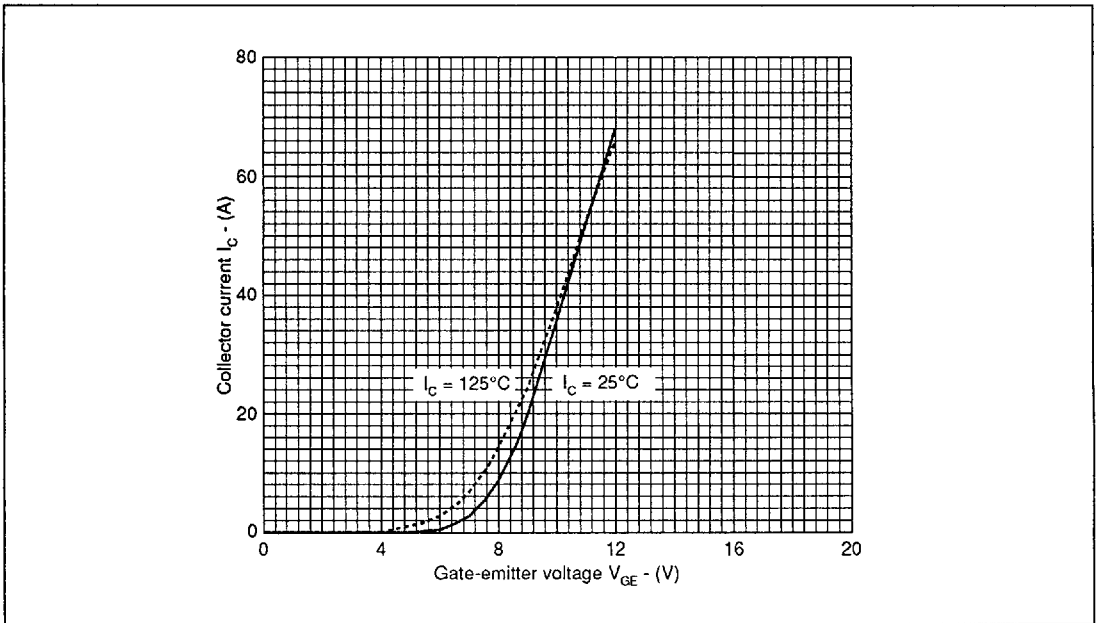


Fig.8 Typical on-state characteristics

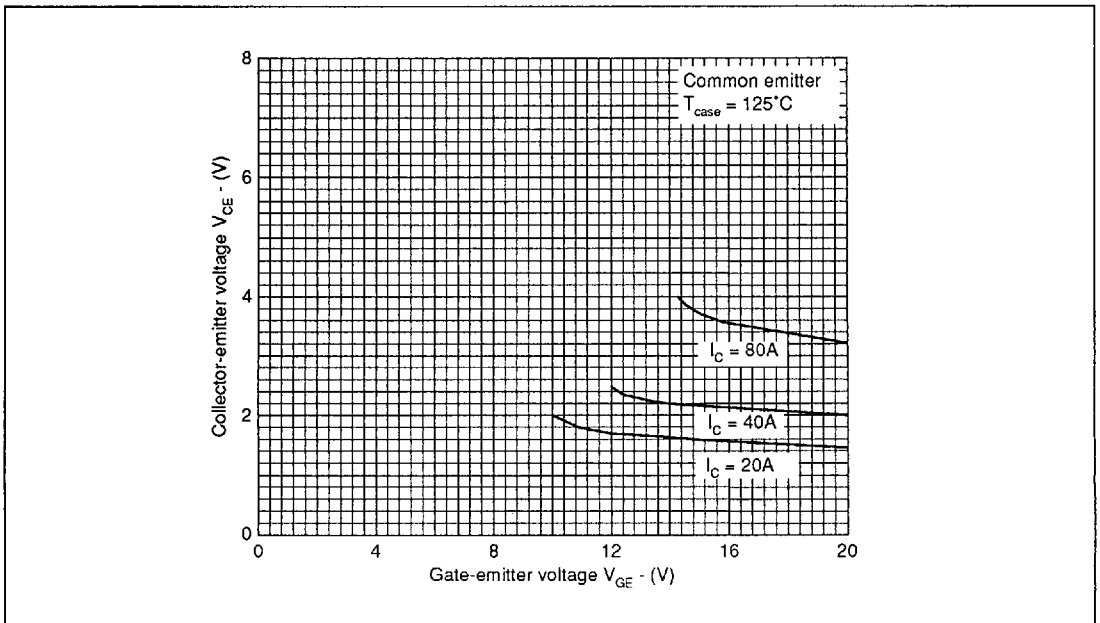


Fig.9 Typical on-state characteristics

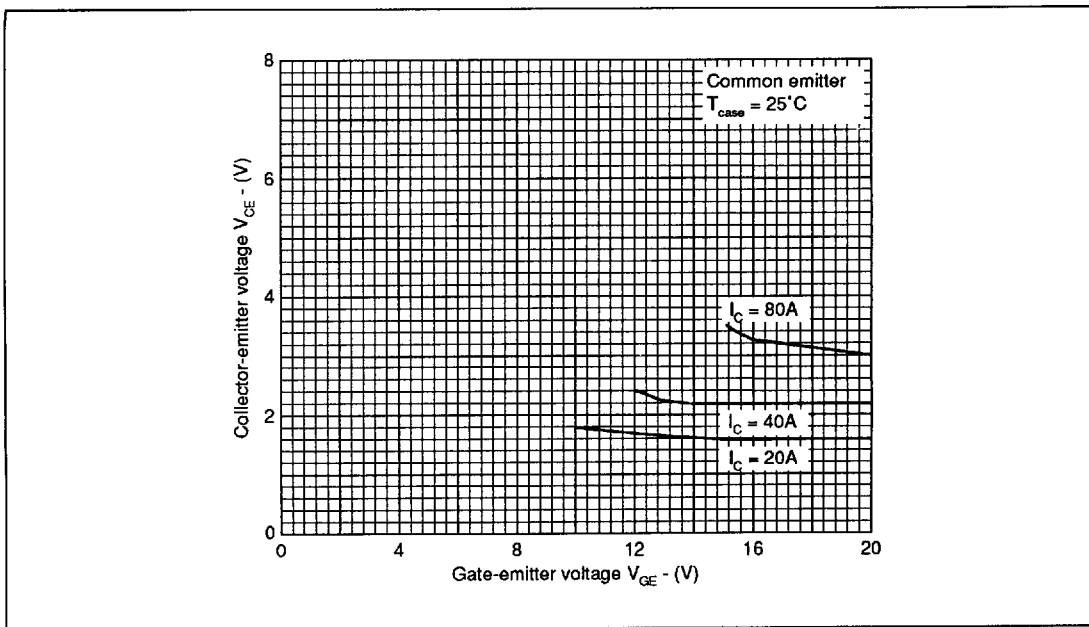


Fig.10 Typical on-state characteristics

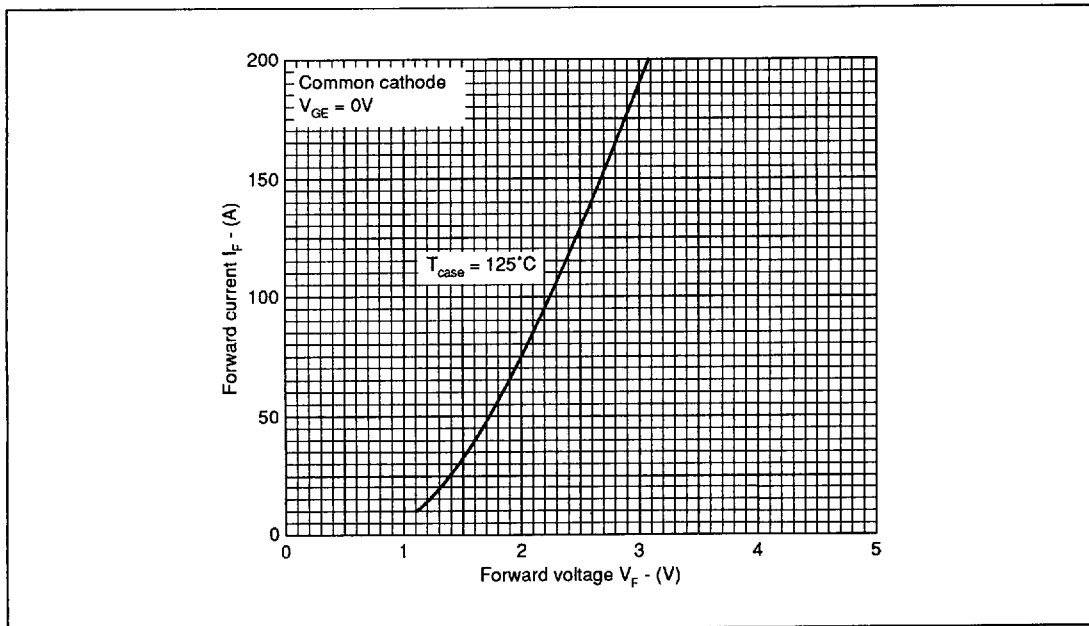


Fig.11 Diode forward characteristics

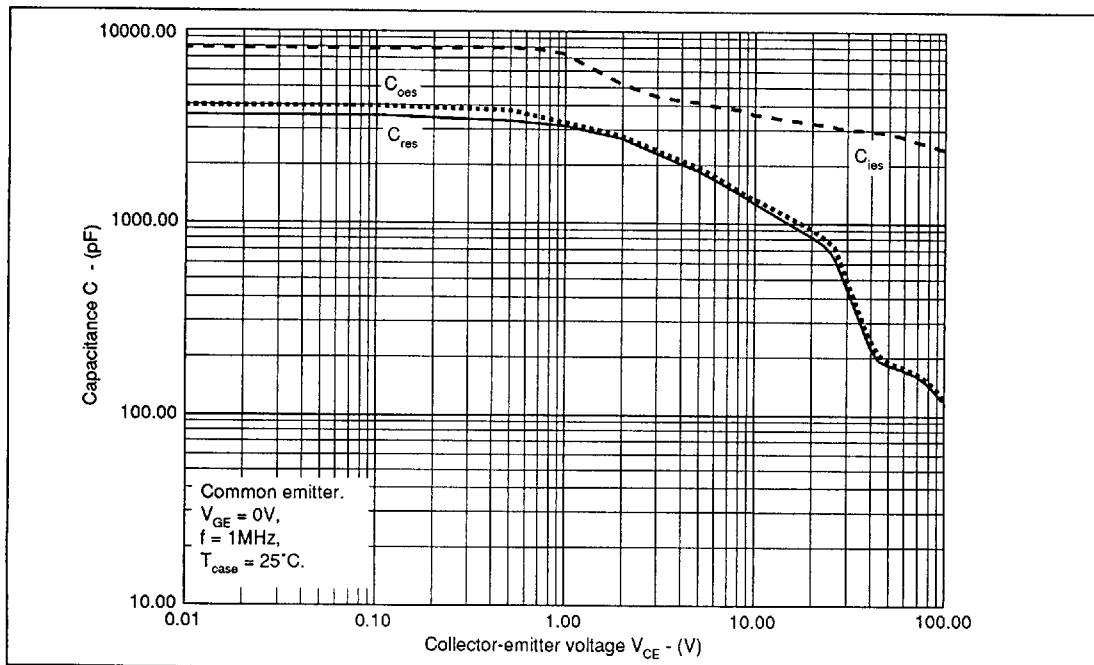


Fig.12 Typical capacitance

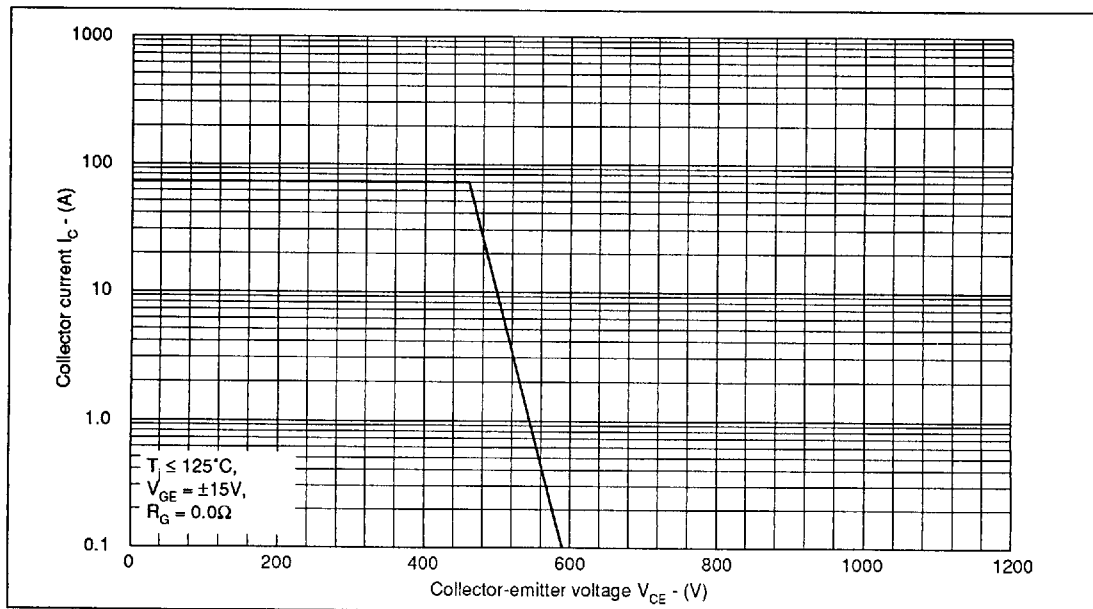


Fig.13 Reverse bias safe operating area