

HD74HC682, HD74HC684 ● 8-bit Magnitude Comparator

These magnitude comparators perform comparisons of two eight-bit binary or BCD words. All types provide $\overline{P=Q}$ outputs and provide $\overline{P>Q}$ outputs. The HD74HC682 features 20k Ω pullup termination resistors on the Q inputs for analog or switch data.

Type	P=Q	P>Q	Output Enable	20k Ω Pullup
HD74HC682	Yes	Yes	No	Yes
HD74HC684	Yes	Yes	No	No

FEATURES

- High Speed Operation
- High Output Current: Fanout of 10 LSTTL Loads
- Wide Operating Voltage: $V_{CC}=2\sim 6V$
- Low Input Current: 1 μA max.
- Low Quiescent Supply Current: I_{CC} (static)=4 μA max. ($T_a=25^\circ C$)

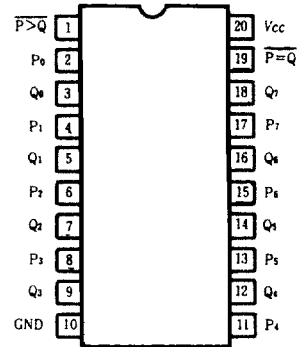
FUNCTION TABLE

Input Data P, Q	Outputs	
	$\overline{P=Q}$	$\overline{P>Q}$
P = Q	L	H
P > Q	H	L
P < Q	H	H

Note: 1. The $\overline{P<Q}$ function can be generated by applying the $\overline{P=Q}$ and $\overline{P>Q}$ Outputs to a 2-input NAND gate.

PIN ARRANGEMENT

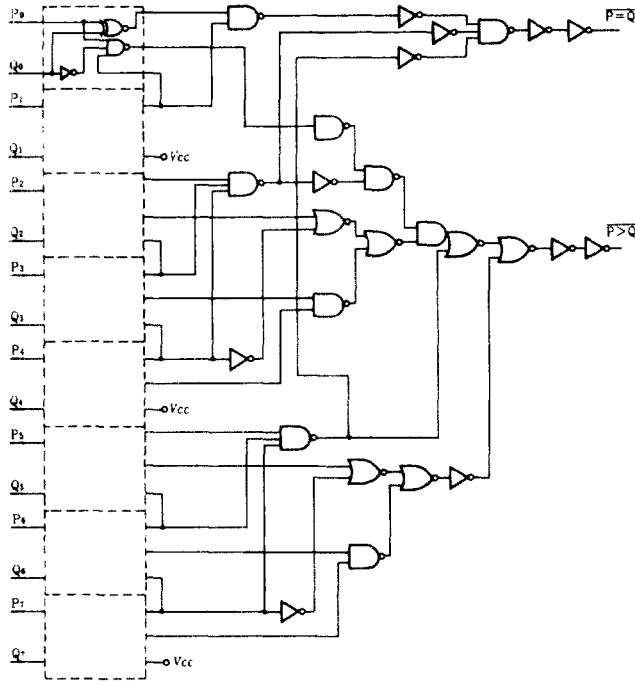
- HD74HC682
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(Top View)

■ LOGIC DIAGRAM

- HD74HC682
- HD74HC684



■ DC CHARACTERISTICS

Item	Symbol	V _{CC} (V)	Test Conditions	T _a =25°C			T _a =-40~+85°C		Unit	
				min.	typ.	max.	min.	max.		
Input Voltabe	V _{IH}	2.0		1.5	-	-	1.5	-	V	
		4.5		3.15	-	-	3.15	-		
		6.0		4.2	-	-	4.2	-		
	V _{IL}	2.0		-	-	-	0.5	-	V	
		4.5		-	-	1.35	-	1.35		
		6.0		-	-	1.8	-	1.8		
Output Voltage	V _{OH}	2.0	V _{in} = V _{IH} or V _{IL}	I _{OH} = -20 μA	1.9	2.0	-	1.9	-	V
		4.5			4.4	4.5	-	4.4	-	
		6.0			5.9	6.0	-	5.9	-	
		4.5			4.18	-	-	4.13	-	
		6.0			5.68	-	-	5.63	-	
	V _{OL}	2.0	V _{in} = V _{IH} or V _{IL}	I _{OL} = 20 μA	-	0.0	0.1	-	0.1	V
		4.5			-	0.0	0.1	-	0.1	
		6.0			-	0.0	0.1	-	0.1	
		4.5			-	-	0.26	-	0.33	
		6.0			-	-	0.26	-	0.33	
Input Current (HC684)	I _{in}	6.0	V _{in} = V _{IH} or GND	-	-	±0.1	-	±1.0	μA	
Quiescent Supply Current (HC684)	I _{CC}	6.0	V _{in} = V _{IH} or GND, I _{out} = 0 μA	-	-	4.0	-	40	μA	
Input Curent (HC682)	I _{in}	6.0	V _{in} = V _{CC}	-	-	±0.1	-	±1.0	μA	
			I _{in} = GND	-	-	-0.6	-	-0.7	mA	
Quescent Supply Current (HC682)	I _{CC}	6.0	Qn = GND, other inputs = V _{CC} or GND I _{out} = 0 μA	-	-	4.8	-	5.6	mA	
			Qn = V _{CC} , other inputs = V _{CC} or GND I _{out} = 0 μA	-	-	4.0	-	40	μA	

■ AC CHARACTERISTICS (C_L = 50pF, Input t_r = t_f = 6ns)

Item	Symbol	V _{CC} (V)	Test Conditions	T _a =25°C			T _a =-40~-85°C		Unit
				min.	typ.	max.	min.	max.	
Propagation Delay Time	t _{PLH} t _{PHL}	2.0	P or Q to P=Q	-	-	175	-	220	ns
		4.5		-	-	35	-	44	
		6.0		-	-	30	-	37	
	t _{PLH} t _{PHL}	2.0	P or Q to P>Q	-	-	200	-	250	ns
		4.5		-	-	40	-	50	
		6.0		-	-	34	-	43	
Output Rise Fall Time	t _{TLH} t _{THL}	2.0		-	-	60	-	75	ns
		4.5		-	-	12	-	15	
		6.0		-	-	10	-	13	
Input Capacitance	C _{in}	-		-	5	10	-	10	pF