

4. TELECOMMUNICATION APPLICATION (Continued)

Application	Device	Package	Circuit Function
TPH Driver	† KS0054C	Pallet	Low power CMOS 64 Bit Thermal printer Head Driver It Contains a 64 bit high speed shift register 64 bit latches output control circuit and 64 bit drivers with a driver capability of 30V/10mA
Thinking Image Processor	† KS16110	100 QFP	Shading correction and Gamma correction by pixel unit 8 bit D/A converter, 6bit A/D converter User Programmable RAM Interface -1728 word × 6 bit shading memory -1728 word × 4 bit line memory Various processing mode -Fixed threshold -Local adaptive binary -8 × 8 wave dither matrix -Error diffusion -Simple mixed -Fuzzy mixed -Smoothing
9600 bps Fax Modem	†† KS16112	68 PLCC	Group 3 fax transmission & reception Half duplex(2-wire) Max transmit level: 0dBm~15dBm Receive dynamic range: 0dBm~-43dBm Programmable dual tone generation Programmable tone detection CCITT V.29, V.27ter, T.30, V.21 channel 2(FSK), T4 conformance

† New Product †† Under Development