

DESCRIPTION

The 2532 Static Shift Register consists of enhancement mode P-Channel silicon gate MOS devices integrated on a single monolithic chip. Each of the four 80-bit registers is provided with an independent input, push-pull output and recirculation control. The single phase clock is common to all four registers. All inputs and outputs including the clock interface directly with TTL or DTL circuits without external components.

Data is entered when the clock is at a logic "1". Data is shifted when the clock goes low. When the Recirculate control is at a logic "1", data recirculates and is continuously

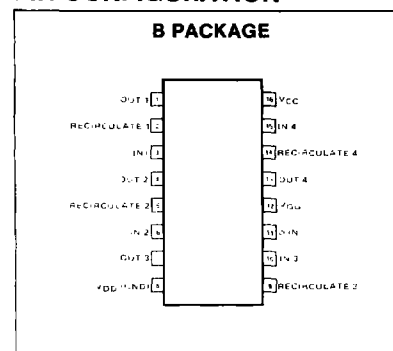
TRUTH TABLE

RECIRCULATE	FUNCTION	INPUT
0	"0" is Written	0
0	"1" is Written	1
1	Recirculate	0
1	Recirculate	1

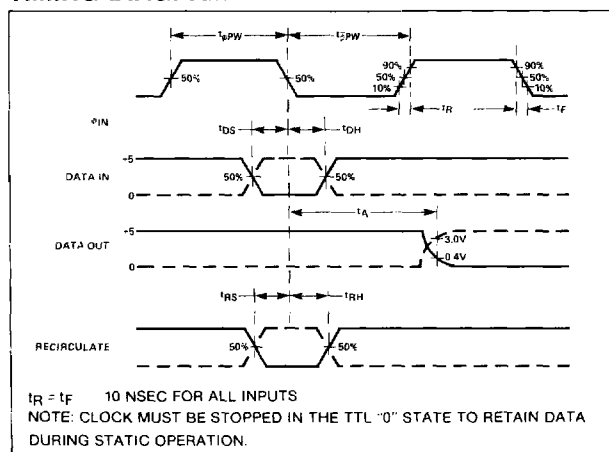
NOTE: "0" = OV, "1" = +5V

available at the output, data input is inhibited. With the Recirculate control is at a logic "0", data is entered.

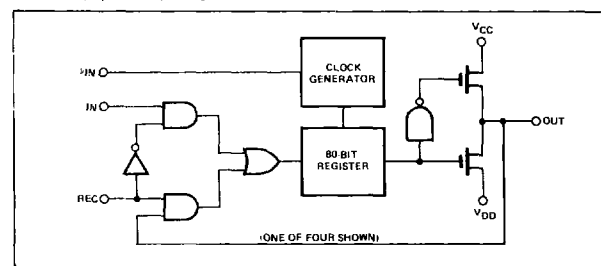
PIN CONFIGURATION



TIMING DIAGRAM



BLOCK DIAGRAM



SWITCHING CHARACTERISTICS

PARAMETER	2532		2532-1		UNIT
	MIN	MAX	MIN	MAX	
t _{RS} Recirculate set-up time	150		80		ns
t _{RH} Recirculate hold time	50		30		ns

MEMORIES