

RANDOM ACCESS PORT	V51C264A	V51C264
Maximum Access Time (ns)	120	150
Minimum Cycle Time (ns)	230	260
Maximum Column Address Access Time (ns)	60	70
SEQUENTIAL ACCESS PORT		
Maximum Shift Frequency (MHz)	40	25

Features

- 64K x 4 CMOS dynamic RAM
- Dual accessibility . . . 1 sequential port, 1 parallel
- Fast sequential port . . . 25 ns cycle time (V51C264A)
- TURBOMODE operation on sequential port allows serial transfer of entire memory content without interruption.
- RAM port exactly like 64K x 4 dynamic RAM
- CAS before RAS refresh
- All outputs and inputs fully TTL compatible
- 3-State unlatched outputs for random and sequential access
- Low power dissipation (V51C264)
 - operating current 70 mA (max)
 - standby (TTL) 3 mA (max)
 - standby (CMOS) 100 μA (max)

Description

The Vitelic V51C264 and V51C264A are high-speed, dual access 262,144 bit (256K) CMOS dynamic random-access memory components. Fabricated with Vitelic's VICMOS III technology, the V51C264 and V51C264A offer TURBOMODE operation of the serial output data to achieve hesitation free operation at serial data lengths up to the full 256K (64K x 4).

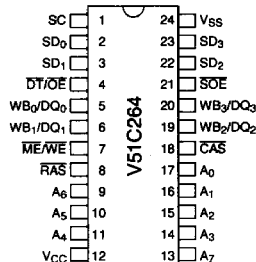
Refresh is accomplished by performing RAS only cycles, read cycles, write cycles or read-modify-write cycles to each of the 256 row addresses during a 4 ms period. Refreshing can also be accomplished by CAS before RAS cycles using the internal refresh counter.

All inputs and outputs are TTL compatible. Address lines and data-in are latched on-chip to simplify system design. Data-out is unlatched for system flexibility. Operation of the V51C264 and V51C264A is guaranteed over a temperature range of 0°C to 70°C and at a Vcc of 4.5 to 5.5 VDC. The V51C264 and V51C264A are packaged in 24 pin dual-in-line packages for insertion into mounting holes on 400 mil (10.16 mm) centers.

PIN NAMES

SOE	Serial Output Enable
SD ₀₋₃	Serial Read Output
DT/OE	Data Transfer/Output Enable
WB ₀₋₃	Write Sel. in per Bit
DO ₀₋₃	Data in and Data Out
ME/WE	Write Enable
RAS	Row Address Strobe
A ₀₋₇	Address inputs
Vcc	5V Supply
CAS	Column Address Strobe
SC	Serial Clock
VSS	Ground

PIN CONFIGURATION
 (Top View)



BLOCK DIAGRAM

