

#### **Features**

- Field Programmable with the <u>PG-3200</u> oscillator programming instrument within seconds.
- Can be programmed twice (single Frequency) or one time as Dual frequency
- CMOS Output (will interface with TTL devices)
- 3.3V or 5.0V nominal Supply Voltage
- Size: 5 x 3.2mm
- Enable/Disable Function (optional Standby function) for single frequency program only

## **Applications**

Driving A/Ds, D/As, FPGAs Digital Video Ethernet, GbE Medical Storage Area Networking COTS Broad Band Access SONET/ SDH/ DWDM Test & Measurement

Electrical Characteristics							
Parameter	Min	Тур	Max	Unit	Condition		
Programmable Frequency Range	1	-	133	MHz	(3.3V: 1 - 100MHz)		
Frequency Stability <sup>2</sup>	±25	1	±100	ppm	For all supply voltages, load changes, aging for 1 year at $25^{\circ}\text{C} \pm 2^{\circ}\text{C}$ , shock, vibration and temperatures.		
Operating Temperature Range options <sup>2</sup>	-20 -40		+70 +85	°C			
Supply Voltage <sup>1, 2</sup> V <sub>DD</sub>	2.97	-	5.5	V			
Supply Current I <sub>DD</sub> (No Load)	-	-	45 25	mA	VDD = 5.0V VDD = 3.3V		
Output Type		СМС	)S		Cload = 50pF max, V <sub>DD</sub> = 4.5~5.5V, ≤ 66MHz Cload = 25pF max, V <sub>DD</sub> = 4.5~5.5V, > 66MHz Cload = 30pF max, V <sub>DD</sub> = 2.97~3.63V, ≤ 40MHz Cload = 15pF max, V <sub>DD</sub> = 2.97~3.63V, > 40MHz		
		TTI	_		Cload = 50pF max; V <sub>DD</sub> = 4.5~5.5V, ≤ 40MHz		
Duty Cycle	-	-	-	%	See Page 2		
Output V <sub>OH</sub> (TTL Level)	2.4	-	-	V	V <sub>DD</sub> = 4.5~5.5V		
(CMOS Level)	VDD - 0.4			V	All voltages		
Output V <sub>OL</sub>	-	-	0.4	V	See Load Circuit and waveform page		
Output T <sub>RISE</sub> and T <sub>FALL</sub>	-	-	-	ns	See page 2		
Startup Time	-	-	2	ms	Time for output to reach specified frequency		
V <sub>DISABLE</sub>	-	-	0.8 0.2VDD	V	VDD = 4.5~5.5V VDD = 2.97~3.63V		
V <sub>ENABLE</sub>	2.0 0.7Vpp	1		V	VDD = 4.5~5.5V VDD = 2.97~3.63V		
Enable Time	-	-	2	ms			
Disable Time - Pin 1 low to Output Hi-Z	1	T/2	T+10	ns	T = Frequency Period		
Disable Current	-	- 0.4	-	mA	Enable/Disable: Pad 1 low, output disabled; See above Supply Current Standby option: Pad 1 low, output disabled, oscillator shutdown		
RMS Period Jitter	-	40 30	50 40	ps	≤ 33MHz > 33MHz		
Period Jitter, Pk-Pk		100 75	250 175	ps	>1,000,000 samples ≤ 33MHz > 33MHz		
Storage Temperature Range	-55	-	+125	°C			

Notes: Specifications with Pad 1 E/D open circuit

Place an appropriate power supply bypass capacitor next to device for correct operation



Duty Cycle					
Parameter	Min	Тур	Max	Unit	
TTL @1.4V level; VDD = 4.5~5.5V	45 45 40 40		55 55 60 60	%	Fo ≤ 50 MHz, CL ≤ 50pF 50 MHz < Fo ≤ 66MHz; CL ≤ 15pF 66 MHz < Fo ≤ 125MHz, CL ≤ 25pF 125 MHz < Fo ≤ 133MHz, CL ≤ 15pF
Parameter	Min	Тур	Max	Unit	
CMOS @ 0.5Vpd level; Vpd = 4.5~5.5V	45 40 40		55 60 60	%	Fo ≤ 66 MHz, CL ≤ 25pF 66 MHz < Fo ≤ 125MHz; CL ≤ 25pF 125 MHz < Fo ≤ 133MHz, CL ≤ 15pF
Parameter	Min	Тур	Max	Unit	
CMOS @ 0.5Vpd level; Vpd = 2.97~3.63V	45 40		55 60	%	Fo ≤ 40 MHz, CL ≤ 30pF 40 MHz < Fo ≤ 100MHz; CL ≤ 15pF

Rise/Fall Time								
Parameter	Min	Тур	Max	Unit				
Rise/Fall Time			1.8 1.2 0.9 3.4 4.0 2.4	ns	0.8V~2.0V, VDD = 4.5~5.5V, CL=50pF 0.8V~2.0V, VDD = 4.5~5.5V, CL=25pF 0.8V~2.0V, VDD = 4.5~5.5V, CL=15pF 0.2VDD~0.8VDD, VDD = 4.5~5.5V, CL=50pF 0.2VDD~0.8VDD, VDD = 2.97~3.63V, CL=30pF 0.2VDD~0.8VDD, VDD = 2.97~3.63V, CL=15pF			

Part Number Example: CPPX5-A7BP-XX.XXXNP								
Series Model	Package Size (mm)		Operating Temperature Range	Frequency Stability (ppm)				
СРРХ	5	-	A7	BR	-	XX.XXX	NP or NC	
	5 = 5 x 3.2		A5 = -20 to +70°C A7 = -40 to +85°C	BC = ±20 <b>BR = ±25</b> BP = ±50 B6 = ±100				



## **Mechanical Dimensions (mm)**

#### [BOTTOM VIEW] [ TOP VIEW ] $5.00 \pm 0.20$ 2.54 ± 0.15 #4 #4 #3 ± 0.15 $20 \pm 0.20$ #2 |-1.20 ± 0.15 Pin 1 Operation for Pin 1 Operation for [ SIDE VIEW ] Single Frequency Dual Frequency Output Pin 1 Pin 1 (Fsel) Output Active Open Logic'1' Freq 2 Logic'1' Active Logic '0'/GND Freq 1 Logic '0'/GND Tri-state 2.54Pad Layout mm shown Disclaimer: Recommended layout shown. Adjust layout as needed for individual process requirements.

To ensure optimal oscillator performance, place a by-pass capacitor of 0.01~0.1µF as close to the part as possible between Vdd and GND pads.

Contacts (pads): Gold (0.3 to 1.0 µm) over Nickel (1.27 to 8.89 µm)

#### Cardinal Components Inc. certifies this device is in accordance with the RoHS and REACH directives.

1.50

Cardinal Components guarantees the device does not contain the following: Cadmium, Hexavalent Chromium, Lead, Mercury, PBB's, PBDE's Weight of the Device: 0.09 grams

Moisture Sensitivity Level: 1 As defined in J-STD-020D

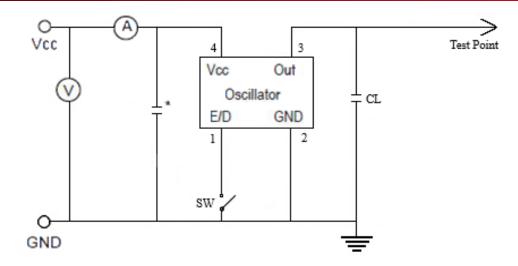
Second Level Interconnect code: e4

For Optimum Jitter Performance, Cardinal recommends:

- A ground plane under the device
- Do not route large transient signals (both current and voltage) under the device
- Do not place near a large magnetic field such as a high frequency switching power supply
- Do not place near piezoelectric buzzers or mechanical fans

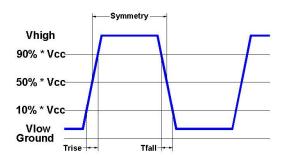


## **Electrical Test / Load Circuit**



#### Notes

CL: 15pF Includes the input capacitance of oscilloscope  $^*$  0.01 $^{\sim}$ 0.1µF external by-pass filter is recommended



## **Environmental / ESD Ratings**

Reliability: Environmental

Parameter	Condition
Mechanical Shock	MIL-STD-883, Method 2002, Condition B
Vibration	MIL-STD-883, Method 2007, Condition A
Solderability	IPC J-STD-002
Thermal Cycle	MIL-STD-883 Method 1010, Condition B

## Thermal Characteristics:

The maximum die or junction temperature is 100°C

#### **ESD Rating**

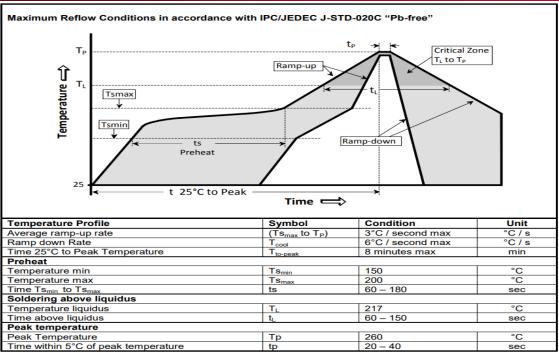
Model	Min. Voltage	Condition		
Human Body Model	2000V	MIL-STD-883 3015.7		
Machine Model	200V	EIAJ ED-4701/304		

# Absolute Maximum Ratings

Parameter	Unit
V <sub>CC</sub> Supply Voltage	-0.5V to +7.0V
Vi Input Voltage	-0.5V to V <sub>CC</sub> + 0.5V
Vo Output Voltage	-0.5V to V <sub>CC</sub> + 0.5V



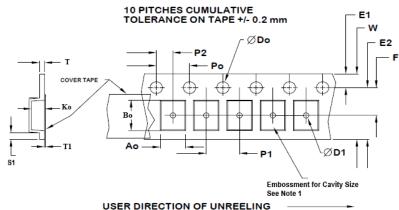
## **Reflow Cycle**



The part may be reflowed 2 times without degradation (typical for lead free processing).

#### **Tape and Reel**

Tape and Reel available for quantities of 250 to 1000 per reel, cut tape for < 250. 12mm tape, 8mm pitch.



Tape Variable Dimensions Table 2									
Tape Size	E2 typ	F	P1	W max	Ao	Во	Ko		
12mm	10.25	5.5 +0.05	8.0 +0.1	12.2	3.6±0.1	5.4±0.1	1.4±0.1		

Dimensions in mm Drawing Not to scale Note 1: Embossed cavity to conform to EIA– 481-B

Tape Constant Dimensions Table 1									
Tape Size	Do	D1 typ	E1	Ро	P2	S1 min	T typ	T1 max	
10,000	1.5	1.5	1.75	4.0	2.0	0.6	0.3	0.1	
12mm	+0.1 -0.0	1.5	±0.1	±0.1	±0.1	0.6	0.3	0.1	

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Reel Dimensions (may vary) Table 3									
		A	В	3	С	D			
Reel Size	Inches	mm	Inches	mm	mm	mm			
7	7.0	177.8	2.50	63.5	13.0	Tape size +0.4			
10	10.0	254.0	4.00	101.6	+0.5 -0.2	+0.4			
13	13.0	330.2	3.75	95.3	-0.2	-0.0			



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