



ISO9001 ISO14001 IATF16949 CHILISIN ELECTRONICS CORP.

SPECIFICATION FOR APPROVAL

Customer : _____

Customer P/N: _____

Drawing No : _____

Quantity : 0 Pcs. Date : 2020/6/23

Chilisin P/N : ACTA Series

Automotive Grade Resistor

Halogen Free
RoHS Compliant
REACH Compliant
Lead Free Solders
AEC-Q200

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Drawn by
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Approved by
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ACTA Series Specification

1 Scope:

- 1-1 This specification is applicable to lead free and halogen free of ROHS directive for ACTA series thick film chip resistors Automotive Grade Product.
- 1-2 This product is for automotive electronic application.
- 1-3 AEC-Q200 qualified , grade 1.

2 Part Numbering:

A C T A	0 0	0 4 0 2	-	100X	J	T H
Series	Internal Code	Dimension (inch)		Resistance Value	Tolerance	Taping Code
ACTA: Thick Film Chip Resistors for automotive grade		1005		J Tol: (3 digit + X)	B = ± 0.1%	H1(20,000 pcs/reel): 1005(01005)
		0201		E-24 Series	D = ± 0.5%	TH(10,000 pcs/reel): 0201/0402
		0402		10Ω=100	F = ± 1%	TP(5,000 pcs/reel): 0603/0805/1206/1210
		0603		4.7Ω=4R7	G = ± 2%	TE(4,000 pcs/reel): 2010/2512
		0805		JUMPER=000	J = ± 5%	
		1206				
		1210		F/D/B Tol: (4 digit)		
		2010		E-96/E-24 Series		
	2512		10.2Ω=10R2			
				10KΩ=1002		

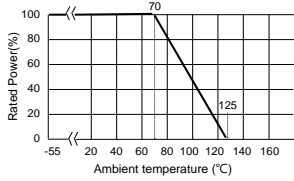
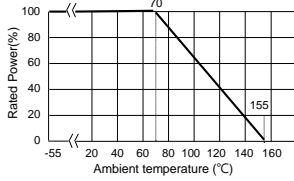
3 SPECIFICATIONS:

3.1 Resistance Range: $\geq 1\Omega$ & 0Ω

Type	Rated Power at 70°C	Max. Working Voltage	Max. Overload Voltage	T.C.R (ppm/°C)	Resistance Range				JUMPER Rated Current		JUMPER Resistance Value	
					B(±0.1%) E-24、E-96	D(±0.5%) E-24、E-96	F(±1%) E-24、E-96	G(±2%)、J(±5%) E-24	J (±5%)	F (±1%)	J (±5%)	F (±1%)
1005	1/32W	15V	30V	-200 +600	-----	-----	1Ω ≤ R < 10Ω		0.5A	0.5A	100mΩ MAX	100mΩ MAX
				±250	-----	-----	10Ω ≤ R ≤ 10MΩ					
0201	1/20W	25V	50V	-200 +400	-----	10Ω ≤ R < 10Ω	10Ω ≤ R < 10Ω	10Ω ≤ R < 10Ω	0.5A	-	50mΩ MAX.	-
				±200	47Ω ≤ R ≤ 1MΩ	10Ω ≤ R ≤ 10MΩ	10Ω ≤ R ≤ 10MΩ	10Ω ≤ R ≤ 10MΩ				
0402	1/16W	50V	100V	±100	100Ω ≤ R ≤ 1MΩ	10Ω ≤ R ≤ 1MΩ	10Ω ≤ R ≤ 22MΩ	10Ω ≤ R ≤ 22MΩ	1A	-	50mΩ MAX.	-
				±200	-----	-----	10Ω ≤ R < 10Ω	10Ω ≤ R < 10Ω				
0603	1/10W	75V	150V	±100	100Ω ≤ R ≤ 1MΩ	10Ω ≤ R ≤ 1MΩ	10Ω ≤ R ≤ 22MΩ	10Ω ≤ R ≤ 22MΩ	1A	-	50mΩ MAX.	-
				±200	-----	10Ω ≤ R < 10Ω	10Ω ≤ R < 10Ω	10Ω ≤ R < 10Ω				
0805	1/8W	150V	300V	±100	100Ω ≤ R ≤ 1MΩ	10Ω ≤ R ≤ 10MΩ	10Ω ≤ R ≤ 27MΩ	10Ω ≤ R ≤ 27MΩ	2A	2.5A	50mΩ MAX.	20mΩ MAX.
				±200	-----	10Ω ≤ R < 10Ω	10Ω ≤ R < 10Ω	10Ω ≤ R < 10Ω				
1206	1/4W	200V	400V	±100	10Ω ≤ R ≤ 1MΩ	10Ω ≤ R ≤ 10MΩ	10Ω ≤ R ≤ 27MΩ	10Ω ≤ R ≤ 27MΩ	2A	3.5A	50mΩ MAX.	20mΩ MAX.
				±200	3Ω ≤ R < 10Ω	10Ω ≤ R < 10Ω	10Ω ≤ R < 10Ω	10Ω ≤ R < 10Ω				
1210	1/2W	200V	400V	±100	100Ω ≤ R ≤ 1MΩ	10Ω ≤ R ≤ 10MΩ	10Ω ≤ R ≤ 27MΩ	10Ω ≤ R ≤ 27MΩ	2A	4A	50mΩ MAX.	20mΩ MAX.
				±200	-----	-----	10Ω ≤ R < 10Ω	10Ω ≤ R < 10Ω				
2010	3/4W	200V	400V	±100	100Ω ≤ R ≤ 1MΩ	10Ω ≤ R ≤ 10MΩ	10Ω ≤ R ≤ 20MΩ	10Ω ≤ R ≤ 20MΩ	2A	5A	50mΩ MAX.	20mΩ MAX.
				±200	-----	-----	10Ω ≤ R < 10Ω	10Ω ≤ R < 10Ω				
2512	1 W	200V	400V	±100	100Ω ≤ R ≤ 1MΩ	10Ω ≤ R ≤ 10MΩ	10Ω ≤ R ≤ 20MΩ	10Ω ≤ R ≤ 20MΩ	2A	7A	50mΩ MAX.	20mΩ MAX.
				±200	-----	-----	10Ω ≤ R < 10Ω	10Ω ≤ R < 10Ω				
Operating Temperature Range				-55°C ~ +155°C (0201: -55°C ~ +125°C)								

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3.2 Power Derating Curve:

Type	01005/ 0201	Other
Operating Temperature Range	-55°C ~ +125°C	-55°C ~ +155°C
Explain	For resistors operated in ambient temperatures above 70°C, power rating shall be derated in accordance with figure below.	For resistors operated in ambient temperatures above 70°C, power rating shall be derated in accordance with figure below.
Figure		

3.4 Voltage Rating

Rated Voltage: The resistor shall have a DC continuous working voltage or a rms. AC continuous working voltage at commercial-line frequency and wave form corresponding to the power rating, as determined from the following:

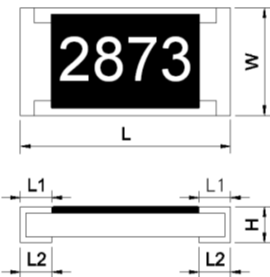
$$E = \sqrt{R \times P}$$

E= Rated voltage (v)
 P= Power rating (w)
 R= Nominal resistance(Ω)

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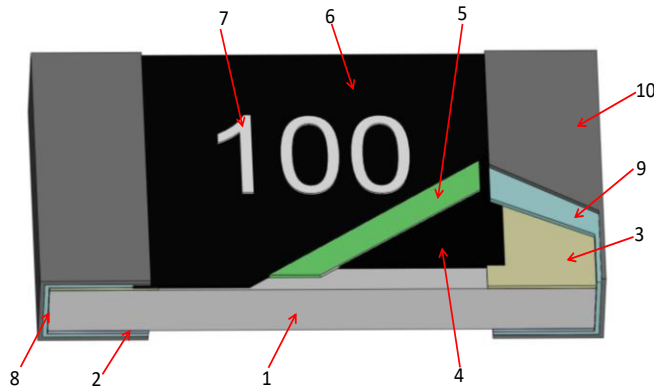
4 Dimensions:

Resistance Range: $\geq 1\Omega$ & 0Ω



Type	Dimension	L	W	H	L1	L2
1005		0.40±0.02	0.20±0.02	0.13±0.02	0.10±0.03	0.10±0.03
0201		0.60±0.03	0.30±0.03	0.23±0.03	0.10±0.05	0.15±0.05
0402		1.00±0.10	0.50±0.05	0.30±0.05	0.20±0.10	0.25±0.10
0603		1.60±0.10	0.80±0.10	0.45±0.10	0.30±0.15	0.30±0.15
0805		2.00±0.10	1.25±0.10	0.50±0.10	0.35±0.20	0.35±0.15
1206		3.05±0.10	1.55±0.10	0.50±0.10	0.45±0.20	0.35±0.15
1210		3.05±0.10	2.55±0.10	0.55±0.10	0.50±0.20	0.50±0.20
2010		5.00±0.20	2.50±0.20	0.55±0.10	0.60±0.20	0.60±0.20
2512		6.30±0.20	3.20±0.20	0.55±0.10	0.60±0.20	0.60±0.20

5 Structure Graph:



1	Ceramic substrate	6	2nd Protective coating
2	Bottom inner electrode	7	Marking
3	Top inner electrode	8	Terminal inner electrode
4	Resistive layer	9	Ni plating
5	1st Protective coating	10	Sn plating



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6 Reliability Test:

1.1 Electrical Performance Test

No	Item	Conditions	Specifications	
			Resistors	Jumper
1-1-1	High Temperature Exposure (Storage)	Put the specimens in the chamber with temperature of 155±3°C for 1000 hours. Then take them out to stabilize in room temperature for 24±4hr or more, and measure of its resistance variance rate. Experiment evidence: AEC-Q200	01005: ΔR=±2.0% 1、0.1%、0.5%、1% : ΔR=±1.0% 2、2%、5%: ΔR=±2.0%	Refer to item 3. general specifications
1-1-2	Temperature Cycling	Put the specimens in the High & low temperature test chamber with temperature varies from -55°C to 125°C for 5 minutes and total 1000 cycles. Then take them out to stabilize in room temperature for 24±4hr or more, and measure of its resistance variance rate. Experiment evidence: AEC-Q200	ΔR=±2.0% No mechanical damage.	Refer to item 3. general specifications
1-1-3	Short Time Overload	Applied 2.5 times rated voltage for 5 seconds and release the load for about 30 minutes, then measure its resistance variance rate. (Rated voltage refer to item 3. general specifications) Applied Maximum overload current Refer to JIS-C5201-1 4.13	01005: ΔR=±2.0% 1、0.1%、0.5%、1% : ΔR=±1.0% 2、2%、5%: ΔR=±2.0% No evidence of mechanical damage. No short or burned on the appearance.	Refer to item 3. general specifications
1-1-4	Biased Humidity	Solder the specimens on the test PCB and put them into the constant temperature humidity chamber with 85±2°C and 85±5%RH. Then apply the test voltage that calculates based on the 10% of rated power for 1000hrs. Then take them out to stabilize in room temperature for 24±4hr or more, and measure of its resistance variance rate. Experiment evidence: AEC-Q200	01005: ΔR=±2.0% 1、0.1%、0.5%、1% : ΔR=±1.0% 2、2%、5%: ΔR=±2.0% No mechanical damage, short or burning-out phenomenon.	Refer to item 3. general specifications
1-1-5	Operational Life	Solder the specimens on the test PCB and Put them in the chamber with temperature of 125±3°C and load the rated voltage for 1000 hours. Then take them out to stabilize in room temperature for 24±4hr or more, and measure of its resistance variance rate. Note: The input voltage shall refer to the power de-rating curve (referring to page 2, No.3.1) Experiment evidence: AEC-Q200	01005: ΔR=±2.0% 1、0.1%、0.5%、1% : ΔR=±1.0% 2、2%、5%: ΔR=±2.0% No mechanical damage, short or burning-out phenomenon.	Refer to item 3. general specifications
1-1-6	Resistance to Soldering Heat	The specimens are fully immersed into the Pb-free solder pot, then take them out to stabilize for 1 hour or more and measure of its resistance variance rate. Temp of solder pot : 260±5°C Soldering duration : 10±1sec. Experiment evidence AEC-Q200	01005: ΔR=±2.0% ΔR=±1.0% No cosmetic defect on terminal or peel-off of side end.	Refer to item 3. general specifications
1-1-7	ESD	Put the specimens on the test fixture and two (2)discharges (2KVDC) shall be applied to each PUT, one (1) with a positive polarity and one (1) with a negative polarity. Afterwards, the specimens stabilize for 30min or more and measure of its resistance variance rate. The test is performed with direct contact and regular discharge mode. The resistor and capacitor used on the spearhead is 2000Ω and 150pF respectively. Experiment evidence AEC-Q200	ΔR= ±3.0% No cosmetic defect on terminal or peel-off of side end.	Refer to item 3. general specifications



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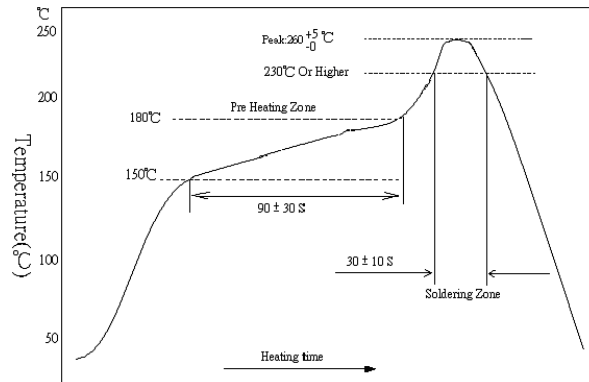
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1.1 Electrical Performance Test				
No	Item	Conditions of Test	Specifications	
			Resistors	Jumper
1-1-8	Solderability	<p>est method:</p> <p>Test item 1 (solder pot test): Method B Precondition: The specimens are subjected to 155°C dry bake for 4hrs±15min. The specimens are immersed into the flux first, then fully immersed into the solder pot, at a temperature of 235± 5°C for 5+0/-0.5 sec. Then rinse with water and observe the soldering coverage under the microscope.</p> <p>Test item 2 (Leaching test): Method D The specimens are immersed into the flux first, then fully immersed into the solder pot, at a temperature of 260±5°C for 30+0/-0.5 sec. Then rinse with water and observe the soldering coverage under the microscope. Experiment evidence AEC-Q200</p>	1.Soldering coverage over 95% 2.At the edge of terminal, the object underneath (e.g. white ceramic) shall not expose.	
1-1-9	Electrical Characterization	$TCR \text{ (ppm / } ^\circ\text{C)} = \frac{(R2 - R1)}{R1 (T2 - T1)} \times 10^6$ <p>R1: Resistance at room temperature (Ω) R2: Resistance at -55°C or +125°C (Ω) T1: Room temperature (°C) T2: Temperature -55°C or +125°C Experiment evidence: AEC-Q200</p>	Refer to item 3. general specifications	NA
1-1-10	Board Flex (Bending Test)	<p>Solder the specimens on the test PCB and put the PCBA onto the Bending Tester. Add force at the central part of PCB, and the duration of the applied forces shall be 60 (+ 5) Sec. Measure of its resistance variance rate in load. Bending depth (D): 0402 、 0603 、 0805 = 5mm 01005 、 0201 、 1206 、 1210 = 3mm 2010 、 2512 = 2mm</p> <p>Experiment evidence: AEC-Q200</p>	$\Delta R = \pm 1.0\%$ No mechanical damage, peel-off of side end or chip crack.	Refer to item 3. general specifications

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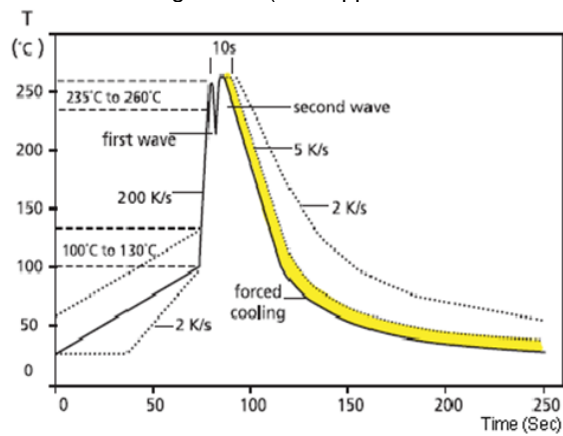
7 Recommend Soldering Method:

7.1 Lead Free Reflow Soldering Profile



Remark: The peak temperature of soldering heat is 260 +5/-0 °C for 10 seconds.

7.2 Lead Free Double-Wave Soldering Profile.(This applies to 0603 size inclusive above products)

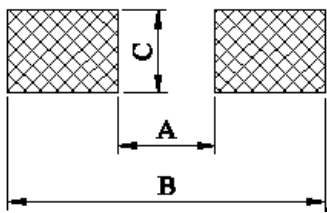


7.3 Soldering Iron: temperature 350°C ±10°C , dwell time shall be less than 3 sec.

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8 Recommend Land Pattern: (For Reflow Soldering)

When a component is soldered, the resistance after soldering changes slightly depending on the size of the soldering area and the amount of soldering. When designing a circuit, it is necessary to consider the effect of a decrease or increase in its resistance.



TYPE	DIM		
	A	B	C
1005	0.2	0.5	0.2
0201	0.3	1	0.4
0402	0.5	1.5	0.6
0603	0.8	2.1	0.9
0805	1.2	3	1.3
1206	2.2	4.2	1.6
1210	2.2	4.2	2.8
2010	3.5	6.1	2.8
2512	3.8	8	3.5

8.2 Automobile Electronic Application:

This specification is for automobile electronic use.

CHILISIN will take no responsibility if any damage,

cost or loss occurs when the product has been used in any special circumstances.

- (a) Information 、 entertainment 、 navigation 、 audio control units.
- (b) Comfortable door, window, seat control unit.
- (c) Internal lighting control unit.

8.3 Automobile Electronic Application:

If consumer intends to use our company product in special environment or condition (including but not limited to those mentioned below),

then will need to make individual recognition of product features and reliability accordingly.

- (a) Used in high temperature and humidity environment
- (b) Exposed to sea breeze or other corrosive gas, such as Cl₂ 、 H₂S 、 NH₃ 、 SO₂ and NO₂.
- (c) Used in non-verified liquids including water, oil, chemical and organic solvents.
- (d) Using non-verified resin or other coating material to seal or coat our Company product.

After soldering, it is necessary to use water-soluble detergents to clean residual solder fluxes, even though no-clean fluxes are recommended.



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8.4 Momentary Overload Precautions:

The product might be out of function when momentary overloaded.

Please make sure to avoid momentary overloading while using and preserving.

8.5 Operation and Processing Precautions:

- (a) Avoid damage to the edge of resistor and protective layer caused by mechanical stress.
- (b) Handle with care when printing circuit board (PCB) is divided or fixed on support body, because bending of printing circuit board (PCB) mounting will make mechanical stress for resistors.
- (c) Make sure the power rating is under the limit when using the resistor.
When power rating is over the limit, the resistor will be overloaded.
There might be machinery damage due to the climbing temperature
- (d) If the resistor will be exposed under massive impact load (shock wave) in a short period of time, the working environment must be set up well before use.
- (e) Please make evaluation and confirmation when the product is well used in your company and have a through consideration of its fail-safe design to ensure the system safety.

9 Stock period:

1.1 The temperature condition must be controlled at $25\pm 5^{\circ}\text{C}$, the R.H. must be controlled at $60\pm 15\%$. The stock can maintain quality level in two years.

1.2 Please avoid the mentioned harsh environment below when storing to ensure product performance and its' weldability. Places exposed to sea breeze or other corrosive gas, such as Cl_2 、 H_2S 、 NH_3 、 SO_2 and NO_2 .

1.3 When the product is moved and stored, please ensure the correct orientation of the box. Do not drop or squeeze the box. Otherwise, the electrode or the body of the product may be damaged.

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9 Plating Thickness:

9.1 Ni: $\geq 2\mu\text{m}$

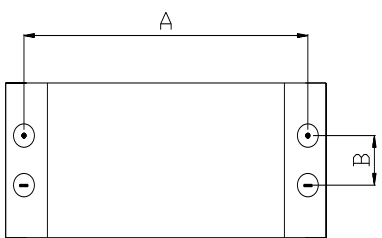
9.2 Sn(Tin): $\geq 3\mu\text{m}$

9.3 Sn(Tin): Matte Sn

10 Rule of package empty quantity:

Each reel that empty quantities don't exceed 0.1% of whole quantities and continuous 2pcs (included) are allowed.

11 Measurement Point:

Bottom electrode		Unit : mm	
	DIM	A	B
	TYPE		
⊙	1005	0.44±0.05	0.22±0.05
⊙	0201	0.44±0.05	0.22±0.05
⊖	0402	0.80±0.05	0.24±0.05
⊖	0603	1.35±0.05	0.35±0.05
⊙	0805	1.80±0.05	0.35±0.05
⊖	1206	2.90±0.05	0.35±0.05
⊙	1210	2.90±0.05	0.35±0.05
⊖	2010	4.50±0.05	1.15±0.05
⊙	2512	5.90±0.05	1.60±0.05





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12 Stock period:

The temperature condition must be controlled at $25\pm 5^{\circ}\text{C}$, the R.H. must be controlled at $60\pm 15\%$. The stock can maintain quality level in two years.

13 The carton packaged for electronic-information products is made by the symbol as follows: (For china)

	
Marking for control of pollution cause by electronic-information products	Marking for package recovery