

**CMOS INTERRUPT CONTROLLER (M32/IRC)**

**DESCRIPTION**

M33242SP/J (M32/IRC) is an interrupt request controller (IRC). It controls local and bus interrupts and interrupt vectors in a system based on the G<sub>MICRO</sub> family MPU (M33210GS-20/FP-20, M33220GS-20, M33230GS-20).

It comprises both the IRG (Interrupt Request Generator) that processes external interrupt requests, and the IRH (Interrupt Request Handler) that processes interrupts from several IRGs.

It can also be used with a VME bus.

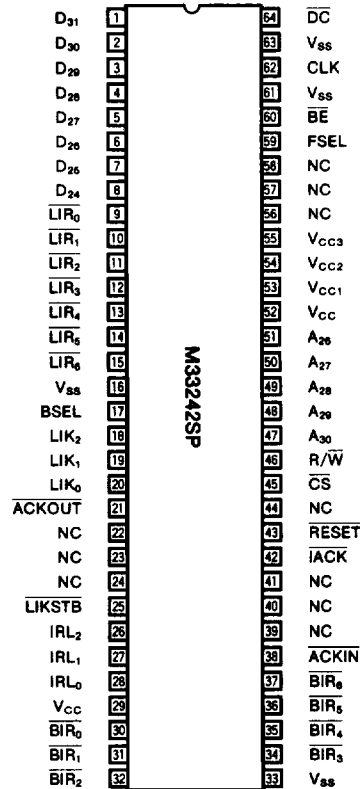
**FEATURES**

- Seven local interrupts and seven bus interrupts (only IRH)
- Cause of interrupt can be expanded
- Fast interrupt acknowledge daisychain
- Interrupt Generator (IRG) function and Interrupt Handler (IRH) function
- M32/IRC can be used with M32 family bus or VME bus
- Interrupt conditions can be set with each interrupt input
- Interrupt polling is possible
- No initialization required after a reset, immediate recovery to interrupt handling ready condition
- Irregular interrupts can be handled
- Maximum operation frequency : 20MHz
- Package : M33242SP 64pin shrink DIP  
M33242J 68pin PLCC

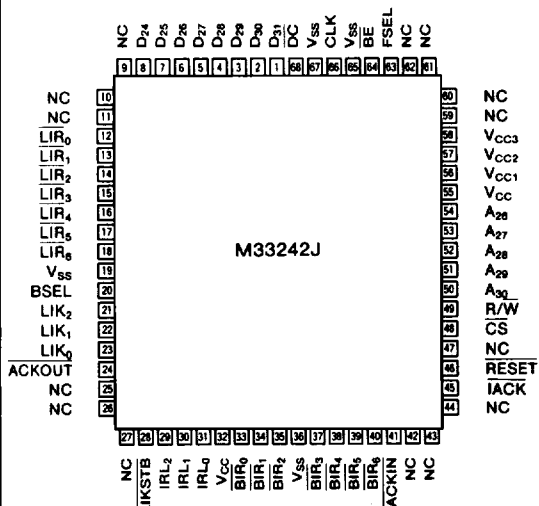
**APPLICATION**

Interrupt controller for M32 family

**PIN CONFIGURATION (TOP VIEW)**



Outline 64P4X-A



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**BLOCK DIAGRAM**

