

DATA SHEET

74F07

Inverter/buffer drivers

Product data
Supersedes data of 1992 Jul 24

2004 Mar 12

Hex inverter/buffer drivers (open-collector)

74F07

FEATURES

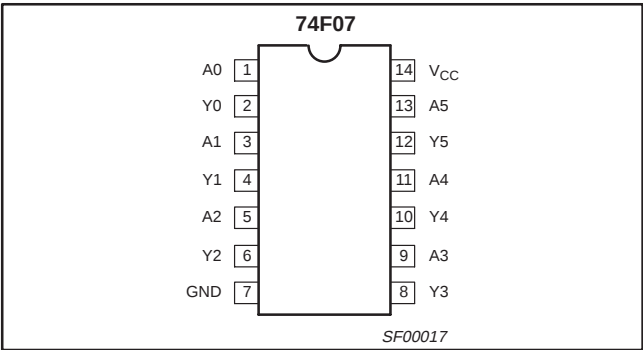
- Open Collector output drive 64mA
- High speed
- 12V output termination voltage

TYPE	TYPICAL PROPAGATION DELAY	TYPICAL SUPPLY CURRENT (TOTAL)
74F07	4.5ns	32mA

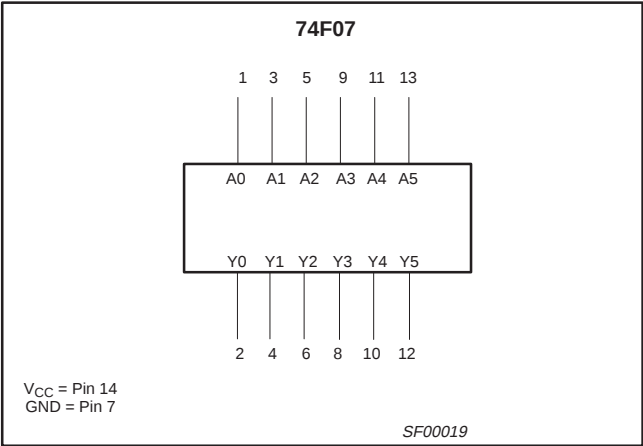
ORDERING INFORMATION

DESCRIPTION	TYPE NUMBER	PKG DWG #
14-pin plastic small outline package	N74F07D	SOT108-1
14-pin plastic dual in-line package	N74F07N	SOT27-1

PIN CONFIGURATIONS



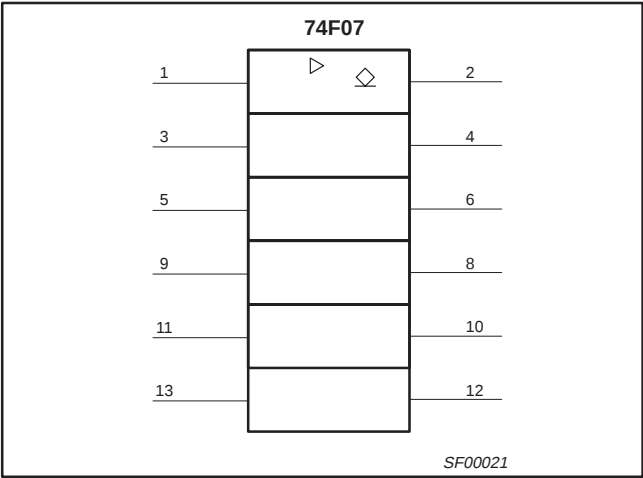
LOGIC SYMBOLS



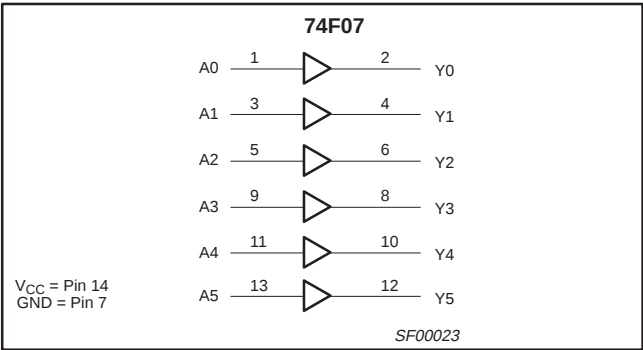
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IEC/IEEE SYMBOLS



LOGIC DIAGRAMS



INPUT AND OUTPUT LOADING AND FAN OUT TABLE

PINS	DESCRIPTION	74F (U.L.) HIGH/LOW	LOAD VALUE HIGH/LOW
An	Data inputs	1.0/1.0	20µA/0.6mA
Yn	Data outputs	OC/106.7	OC/64mA

NOTES:

1. One (1.0) FAST unit load is defined as: 20µA in the High state and 0.6mA in the Low state.
2. OC = Open Collector

FUNCTION TABLE

INPUTS	OUTPUTS
An	Yn
L	L
H	H

NOTES:

1. H = High voltage level
2. L = Low voltage level

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ABSOLUTE MAXIMUM RATINGS

(Operation beyond the limit set forth in this table may impair the useful life of the device.

Unless otherwise noted these limits are over the operating free air temperature range.)

SYMBOL	PARAMETER	RATING	UNIT
V_{CC}	Supply voltage	−0.5 to +7.0	V
V_{IN}	Input voltage	−0.5 to +7.0	V
I_{IN}	Input current	−30 to +5	mA
V_{OUT}	Voltage applied to output in High output state	−0.5 to 12	V
I_{OUT}	Current applied to output in Low output state	128	mA
T_{amb}	Operating free air temperature range	0 to +70	°C
T_{stg}	Storage temperature range	−65 to +150	°C

RECOMMENDED OPERATING CONDITIONS

SYMBOL	PARAMETER	LIMITS			UNIT
		MIN	NOM	MAX	
V_{CC}	Supply voltage	4.5	5.0	5.5	V
V_{IH}	High-level input voltage	2.0			V
V_{IL}	Low-level input voltage			0.8	V
I_{IK}	Input clamp current			−18	mA
V_{OH}	High-level output voltage			12	V
I_{OL}	Low-level output current			64	mA
T_{amb}	Operating free air temperature range	0		+70	°C

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DC ELECTRICAL CHARACTERISTICS

(Over recommended operating free-air temperature range unless otherwise noted.)

SYMBOL	PARAMETER		TEST CONDITIONS ¹			LIMITS			UNIT
						MIN	TYP ²	MAX	
I _{OH}	High-level output current		V _{CC} = MIN, V _{IL} = MAX, V _{OH} = MAX, V _{IH} = MIN					250	μA
V _{OL}	Low-level output voltage		V _{CC} = MIN, V _{IL} = MAX, V _{IH} = MIN	I _{OL} = MAX	±10% V _{CC}		0.30	0.50	V
					±5% V _{CC}		0.30	0.50	V
V _{IK}	Input clamp voltage		V _{CC} = MIN, I _I = I _{IK}				−0.73	−1.2	V
I _I	Input current at maximum input voltage		V _{CC} = MAX, V _I = 7.0V					100	μA
I _{IH}	High-level input current		V _{CC} = MAX, V _I = 2.7V					20	μA
I _{IL}	Low-level input current		V _{CC} = MAX, V _I = 0.5V					−0.6	mA
I _{CC}	Supply current (total)	I _{CCH}	V _{CC} = MAX				10	14	mA
		I _{CCL}					32	45	mA

NOTES:

- For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions for the applicable type.
- All typical values are at $V_{CC} = 5\text{V}$, $T_{\text{amb}} = 25^\circ\text{C}$.
- Not more than one output should be shorted at a time. For testing I_{OS} , the use of high-speed test apparatus and/or sample-and-hold techniques are preferable in order to minimize internal heating and more accurately reflect operational values. Otherwise, prolonged shorting of a High output may raise the chip temperature well above normal and thereby cause invalid readings in other parameter tests. In any sequence of parameter tests, I_{OS} tests should be performed last.

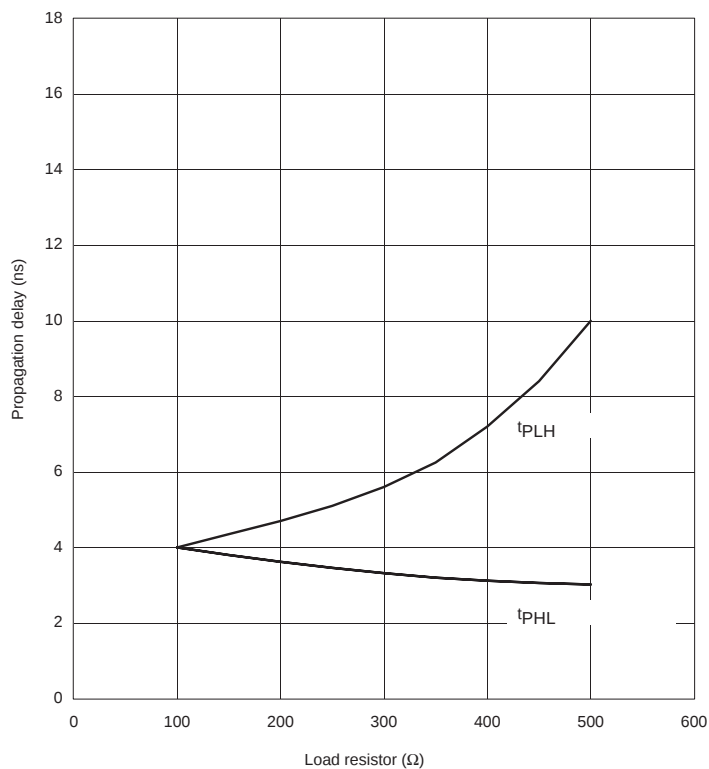
AC ELECTRICAL CHARACTERISTICS

SYMBOL	PARAMETER	TEST CONDITION	LIMITS					UNIT
			V _{CC} = +5.0V T _{amb} = +25°C C _L = 50pF, R _L = 100Ω			V _{CC} = +5.0V ± 10% T _{amb} = 0°C to +70°C C _L = 50pF, R _L = 100Ω		
			Min	Typ	Max	Min	Max	
t _{PLH}	Propagation delay An to Yn	Waveform 1	2.0	4.0	6.0	2.0	6.5	ns
t _{PHL}			3.0	5.0	7.0	2.5	7.5	

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TYPICAL PROPAGATION DELAYS VERSUS LOAD FOR OPEN COLLECTOR OUTPUTS



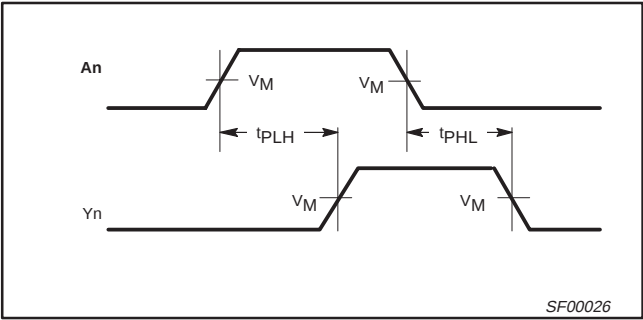
SF00024

NOTE:

When using Open-Collector parts, the value of the pull-up resistor greatly affects the value of the t_{PLH} . For example, changing the specified pull-up resistor value from 500Ω to 100Ω will improve the t_{PLH} up to 50% with only a slight increase in the t_{PHL} . However, if the value of the pull-up resistor is changed, the user must make certain that the total I_{OL} current through the resistor and the total I_{IL} 's of the receivers does not exceed the I_{OL} maximum specification.

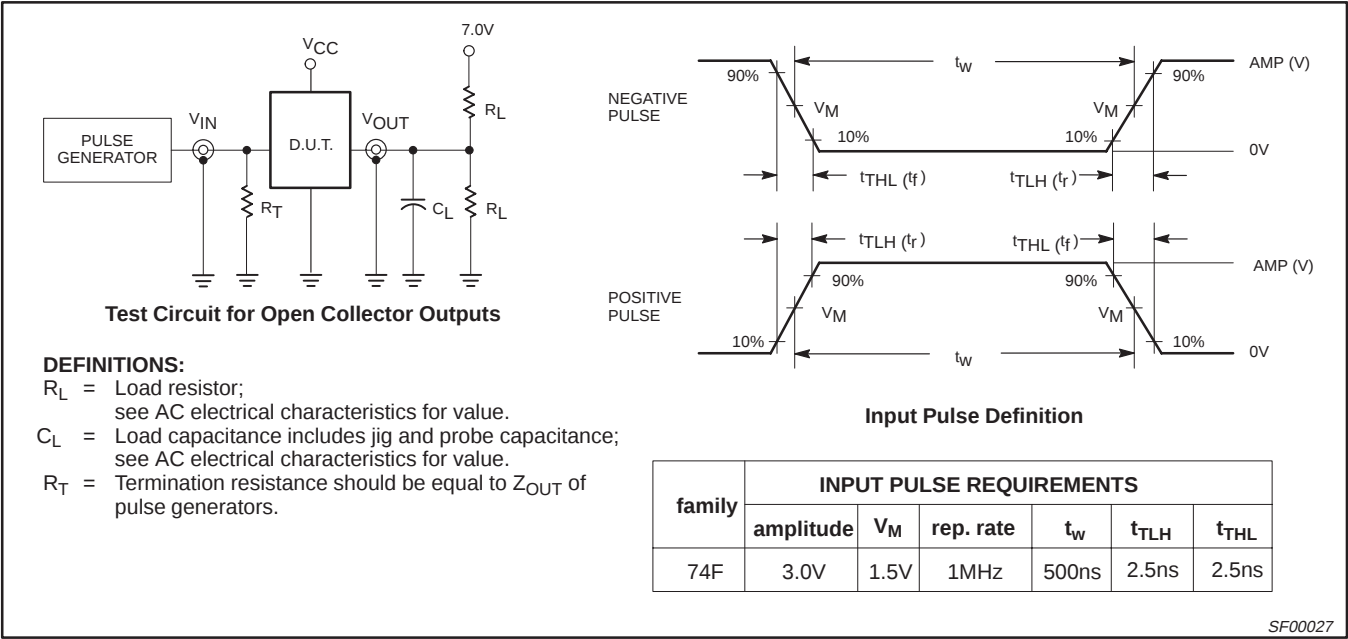
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AC WAVEFORMS



Waveform 1. Propagation delay for non-inverting outputs
NOTE:
For all waveforms, $V_M = 1.5V$.

TEST CIRCUIT AND WAVEFORMS

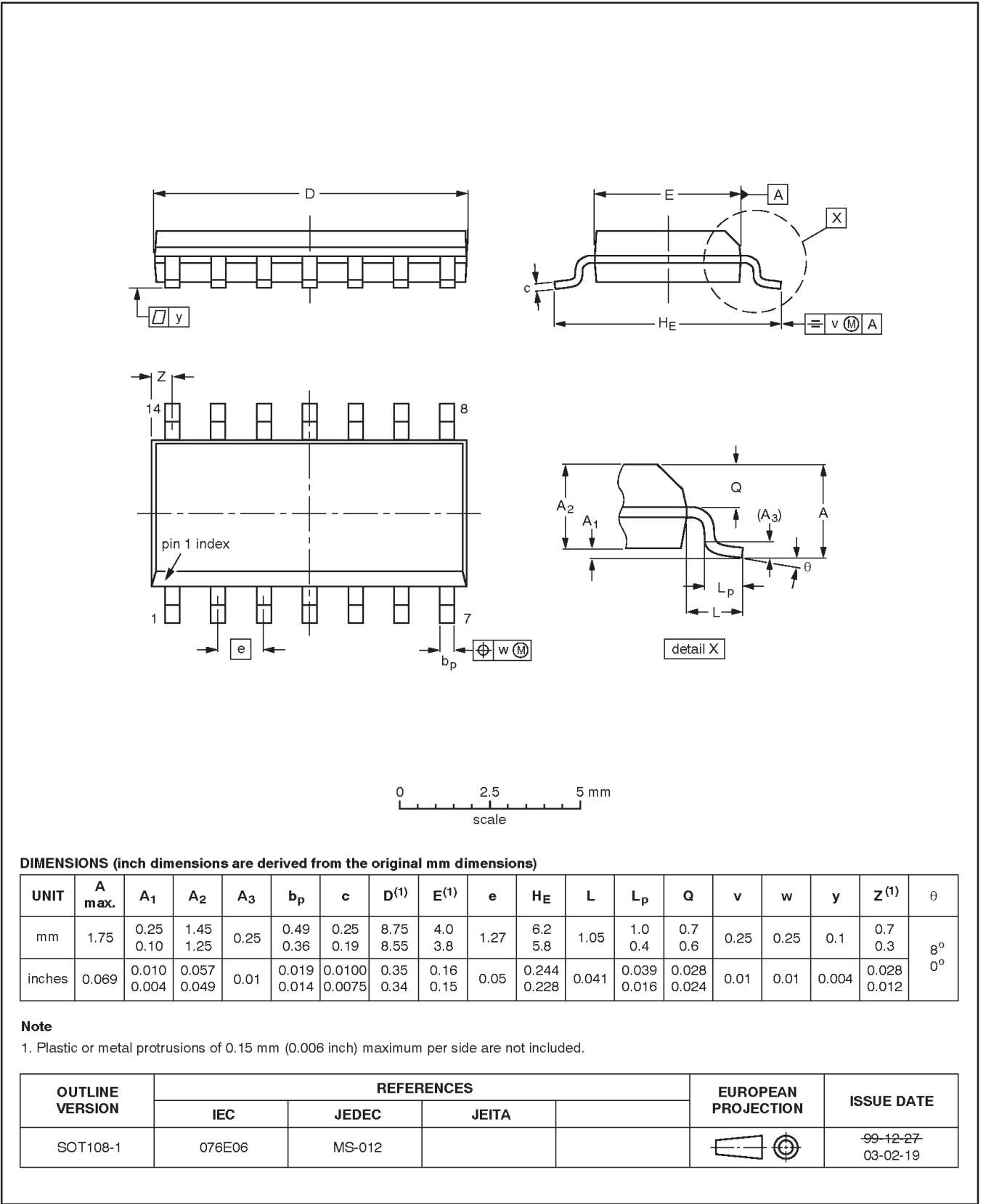


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SO14: plastic small outline package; 14 leads; body width 3.9 mm

SOT108-1

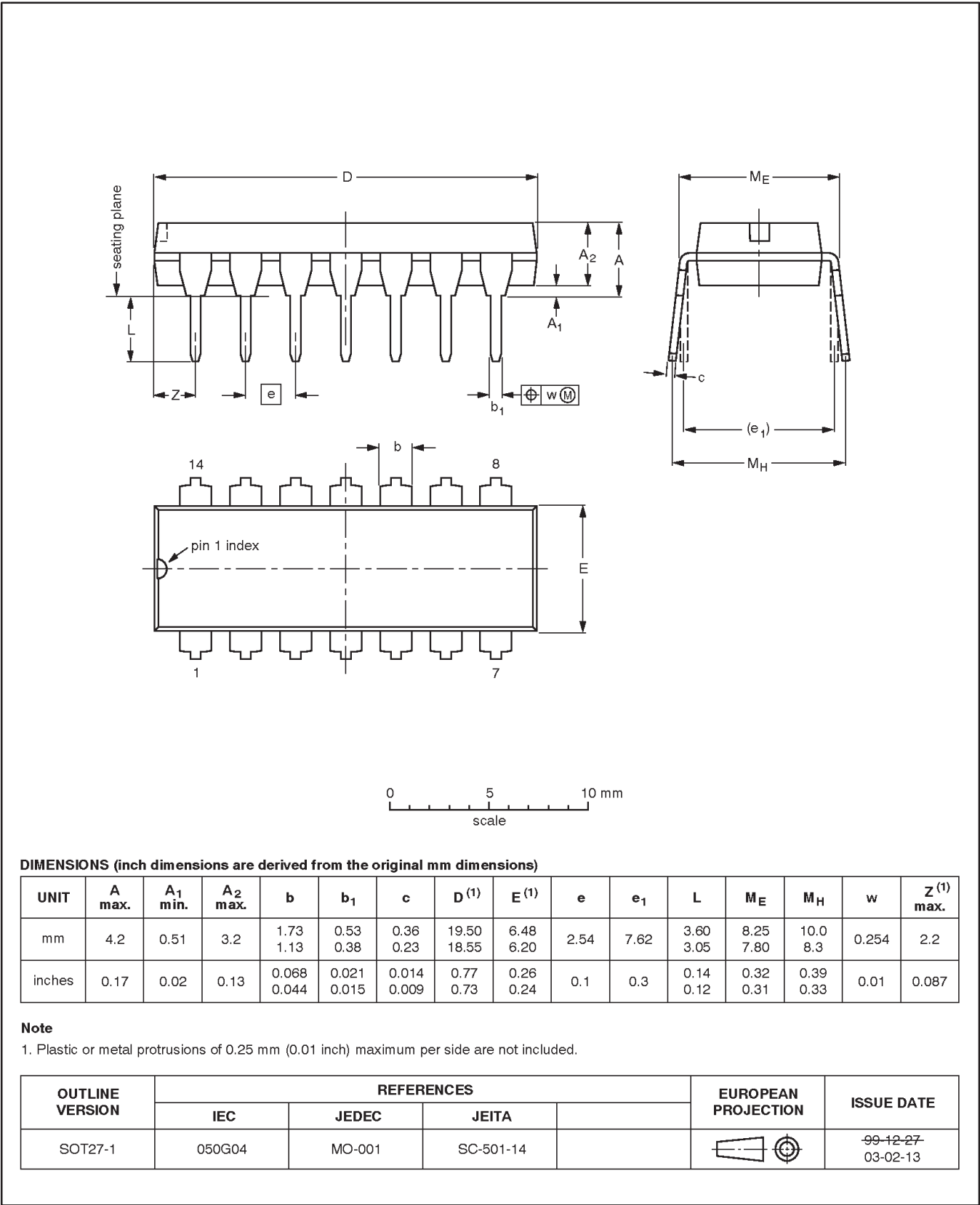


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DIP14: plastic dual in-line package; 14 leads (300 mil)

SOT27-1



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REVISION HISTORY

Rev	Date	Description
_3	20040312	Product data (9397 750 13033); supersedes data sheet 74F06_A_7_A_2 of 1992 Jul 24 (9397 750 05054). Modifications: • Delete all references to 74F06A and 74F07A (product discontinued). • Separate 74F06 and 74F07 into standalone data sheets.
_2	19920724	Product data (9397 750 05054); supersedes previous version.

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Data sheet status

Level	Data sheet status ^[1]	Product status ^{[2] [3]}	Definitions
I	Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
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Limiting values definition — Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 60134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

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