



iFX7160

10 ns FLEXlogic™ FPGA FAMILY WITH SRAM OPTION

- High Performance FPGA (Field Programmable Gate Array)
 - Deterministic 10 ns Pin-to-Pin Propagation Delays
 - 80 MHz System Clock Frequency
- 10,000 Equivalent Logic Gates or up to 20,480 Bits of SRAM
- 0.8 μ CHMOS* Technology
 - Power Management Options
 - Minimize Active Power Consumption (2.5 mA/MHz)
 - Zero Power Standby
- JTAG 1149.1 Compatible Test Port
 - Supports Boundary Scan and In-Circuit Reconfiguration/Programming
- 16 Configurable Function Blocks (CFBs) Linked by a 100% Connectable Matrix
 - Improves Fitting of Complex Designs
- Any CFB Can Be Either 24V10 Logic or SRAM Block
 - Up to 160 Complex Macrocells
 - 128 x 10 SRAM Configuration
 - CFB Selectable 3.3V or 5V Outputs
 - Open-Drain Output Option
- 24V10 Macrocell Features
 - Dual Feedback on All I/O Pins
 - Allocation Supports up to 16 Product Terms per Macrocell with No Performance Penalty
 - 12 Clock Options
 - Flexible Preset/Clear Options
 - Selectable D/T/JK/SR Flip-Flops
 - Fast 12-Bit Identity Compare Option
- Supported by Industry Standard Design and Programming Tools

Package Options

Pins	Package	Macrocells	I/O	CLK/ Inputs	JTAG/V _{pp}	V _{CC} /GND
132	PQFP	160	92	4	5	31
196	PQFP	160	160	8	5	23

*CHMOS is a patented process of Intel Corporation.