



Look-Ahead Carry Block

**ELECTRICALLY TESTED PER:
5962-8774601**

The 10579 is a high speed, low power, standard MECL complex function that is designed to perform the look-ahead carry function. This device can be used with the 10581 4-bit ALU directly, or with the 10580 dual arithmetic unit in any computer, instrumentation or digital communication application requiring high speed arithmetic operation on long words.

When used with the 10581, the 10579 performs a second order or higher look-ahead. Second order carry is valuable for longer binary words. As an example, addition of two 32-bit words is improved from 30 nanoseconds with ripple-carry techniques. The 10579 may also be used in many other applications. It can, for example, reduce system package count when used to generate functions of several variables.

3

- 415 mW Max/Pkg (No Load)
- $t_{pd} = 3.0$ ns typ (Carry, Propagate)
4.0 ns typ (Generate)
- $t_r, t_f = 2.3$ ns typ (20% - 80%)

PIN ASSIGNMENTS

FUNCTION	PIN ASSIGNMENTS			BURN-IN (CONDITION C)
	DIL	FLATS	LCC	
V _{CC1}	1	5	2	GND
G _G	2	6	3	51 Ω to V _{TT}
C _{n + 4}	3	7	4	51 Ω to V _{TT}
G ₀	4	8	5	OPEN
G ₃	5	9	7	GND
C _{n + 2}	6	10	8	51 Ω to V _{TT}
G ₁	7	11	9	GND
V _{EE}	8	12	10	V _{EE}
G ₂	9	13	12	OPEN
P ₁	10	14	13	GND
C _n	11	15	14	OPEN
P ₂	12	16	15	OPEN
P ₃	13	1	17	GND
P ₀	14	2	18	OPEN
P _G	15	3	19	51 Ω to V _{TT}
V _{CC2}	16	4	20	GND

BURN - IN CONDITIONS:

V_{TT} = - 2.0 V MAX / - 2.2 V MIN

V_{EE} = - 5.7 V MAX / - 5.2 V MIN

Military 10579

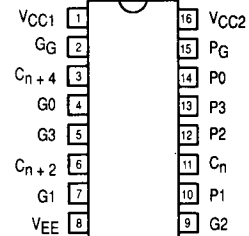


AVAILABLE AS

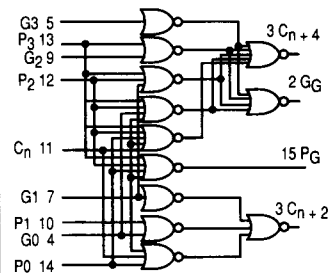
- 1) JAN: N/A
 - 2) SMD: 5962-8774601
 - 3) 883: 10579/BXAJC
- X = CASE OUTLINE AS FOLLOWS:

PACKAGE: CERDIP: E
CERFLAT: F
LCC: 2

The letter "M" appears before the slash on LCC.



LOGIC DIAGRAM

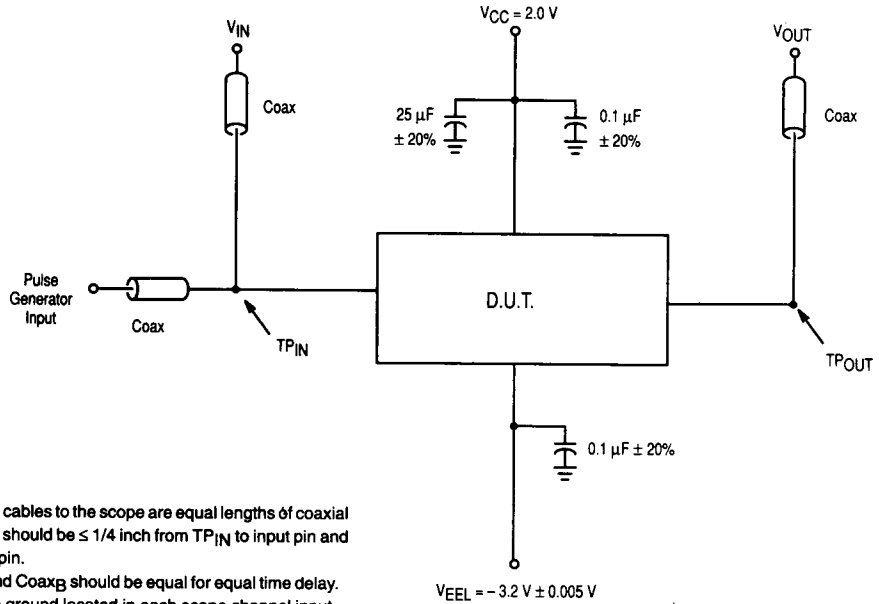


$$P_G = P_0 + P_1 + P_2 + P_3$$

$$G_G = (G_0 + P_1 + P_2 + P_3)(G_1 + P_2 + P_3)G_3$$

$$C_{n+2} = (C_n + P_0 + P_1)(G_0 + P_1)G_1$$

$$C_{n+4} = (C_n + P_0 + P_1 + P_2 + P_3)(G_0 + P_1 + P_2 + P_3)(G_2 + P_3)G_3$$



NOTES

1. All input and output cables to the scope are equal lengths of coaxial cable. Wire length should be $\leq 1/4$ inch from TP_{IN} to input pin and TP_{OUT} to output pin.
2. Length of Coax_A and Coax_B should be equal for equal time delay.
3. 50Ω termination to ground located in each scope channel input.
4. $t_r = t_f = 2.0 \text{ ns} \pm 0.2 \text{ ns}$ (20% to 80%)
5. Unused outputs should be loaded 100Ω to ground.
6. Frequency = 1.0 MHz.

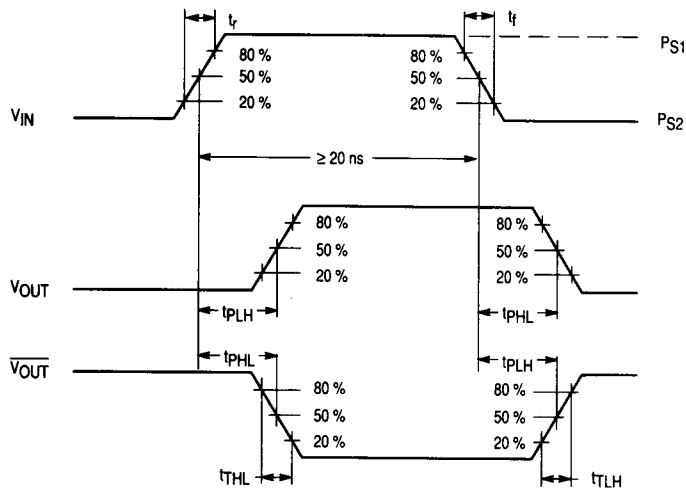


Figure 1. Switching Test Circuit and Waveforms

10579 QUIESCENT LIMIT TABLE *

* ELECTRICAL CHARACTERISTICS

Each MECL 10K series circuit has been designed to meet the dc specifications shown in the test table, after thermal equilibrium has been established. The circuit is in a test socket or mounted on a printed circuit board and transverse air flow greater than 500 linear fpm is maintained. Outputs are terminated through a 100 Ω resistor to -2.0 volts.

Test Temperature	Test Voltage Values (Volts)							
	V _{IH}	V _{IL}	V _{IH1}	V _{IL1}	PS1	PS2	V _{EE}	V _{EEL}
T _A = 25 °C	-0.780	-1.950	-1.105	-1.475	+1.11	+0.31	-5.2	-3.2
T _A = 125 °C	-0.650	-1.950	-1.000	-1.400	+1.24	+0.36	-5.2	-3.2
T _A = -55 °C	-0.880	-1.920	-1.255	-1.510	+1.01	+0.28	-5.2	-3.2

Symbol	Parameter	Limits						Units	TEST VOLTAGE APPLIED TO PINS BELOW									
		+25 °C		+125 °C		-55 °C			Pinouts referenced are for DIL package, check Pin Assignments VCC = 0 V, Output Load = 100 Ω to -2.0 V									
		Subgroup 1	Subgroup 2	Subgroup 1	Subgroup 2	Subgroup 3	V _{IH}		V _{IL}	V _{IH1}	V _{IL1}	V _{EE}	V _{CC}	P. U. T.				
	Functional Parameters:	Min	Max	Min	Max	Min	Max											
V _{OH}	High Output Voltage	-0.93	-0.78	-0.825	-0.63	-1.09	-0.88	V	4, 5, 7, 9, 14					8	1, 16	2, 3, 6, 15		
V _{OL}	Low Output Voltage	-1.85	-1.62	-1.82	-1.545	-1.92	-1.655	V		4, 5, 7, 9, 14				8	1, 16	2, 3, 6, 15		
V _{OL1}	Low Output Voltage	-1.85	-1.60	-1.82	-1.525	-1.92	-1.635	V	5, 7, 9, 10, 12, 13, 14					8	1, 16	2, 3, 6, 15		
V _{OH1}	High Output Voltage	-0.95	-0.78	-0.845	-0.63	-1.10	-0.88	V	4, 5, 7, 9, 10, 12 - 14		4, 5, 10 - 13			8	1, 16	2, 3, 6, 15		
I _{IH1}	Input Current High		225		380		380	μA	5, 9					8	1, 16	5, 9		
I _{IH2}	Input Current High		270		460		460	μA	4, 7, 11					8	1, 16	4, 7, 11		
I _{IH3}	Input Current High		355		600		600	μA	14					8	1, 16	14		
I _{IH4}	Input Current High		395		670		670	μA	12					8	1, 16	12		
I _{IH5}	Input Current High		440		750		750	μA	10, 13					8	1, 16	10, 13		
I _{IL}	Input Current Low	0.5		0.3		0.5		μA			4, 7, 9, 14			8	1, 16	4, 7, 9, 14		
I _{EE}	Power Supply Current	-72		-79		-79		mA						8	1, 16	8		

10579 QUIESCENT LIMIT TABLE *

* ELECTRICAL CHARACTERISTICS

Each MECL 10K series circuit has been designed to meet the dc specifications shown in the test table, after thermal equilibrium has been established. The circuit is in a test socket or mounted on a printed circuit board and transverse air flow greater than 500 linear fpm is maintained. Outputs are terminated through a 100 Ω resistor to - 2.0 volts.

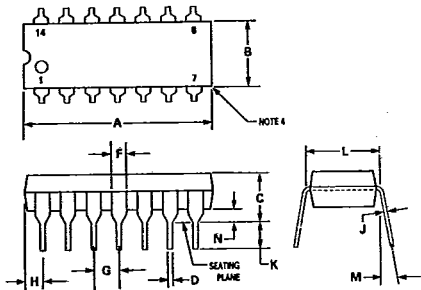
Test Temperature	Test Voltage Values (Volts)									
	V _{IH}	V _{IL}	V _{IH1}	V _{IL1}	PS1	PS2	VEE	VEEL		
T _A = 25 °C	-0.780	-1.950	-1.105	-1.475	+1.11	+0.31	-5.2	-3.2		
T _A = 125 °C	-0.650	-1.950	-1.000	-1.400	+1.24	+0.36	-5.2	-3.2		
T _A = -55 °C	-0.880	-1.920	-1.255	-1.510	+1.01	+0.28	-5.2	-3.2		

Symbol	Parameter	Limits						Units	TEST VOLTAGE APPLIED TO PINS BELOW							
		+ 25 °C		+ 125 °C		- 55 °C			Pinouts referenced are for DIL package, check Pin Assignments V _{CC} = 2.0 V, Output Load = 100 Ω to GND							
		Subgroup 9	Subgroup 10	Subgroup 11	Subgroup 9	Subgroup 10	Subgroup 11		V _{IN}	V _{OUT}	V _{CC}	VEEL	PS1	P. U. T.		
t _{RLH}	Rise Time	1.1	3.5	1.0	4.1	1.0	3.9	ns	5, 9, 10 11, 13	2, 3 6, 15	1, 16	8	4, 5, 7, 9, 10, 12, 13	2, 3, 6, 15		
t _{FHL}	Fall Time	1.1	3.5	1.0	4.1	1.0	3.9	ns	5, 9, 10 11, 13	2, 3 6, 15	1, 16	8	4, 5, 7, 9, 10, 12, 13	2, 3, 6, 15		
t _{pd}	Propagation Delay G or C to Carry Out	1.0	5.5	1.0	6.4	1.0	5.9	ns	5, 9, 10 11, 13	2, 3 6, 15	1, 16	8	4, 5, 7, 9, 10, 12, 13	2, 3, 6, 15		
t _{pd}	Propagation Delay G to GG	1.0	5.5	1.0	6.4	1.0	5.9	ns	5, 9, 10 11, 13	2, 3 6, 15	1, 16	8	4, 5, 7, 9, 10, 12, 13	2, 3, 6, 15		
t _{pd}	Propagation Delay P to PG	1.0	3.5	1.0	4.1	1.0	3.9	ns	5, 9, 10 11, 13	2, 3 6, 15	1, 16	8	4, 5, 7, 9, 10, 12, 13	2, 3, 6, 15		
t _{pd}	Propagation Delay P ro GG	1.0	5.5	1.0	6.4	1.0	5.9	ns	5, 9, 10 11, 13	2, 3 6, 15	1, 16	8	4, 5, 7, 9, 10, 12, 13	2, 3, 6, 15		

PACKAGE OUTLINE DIMENSIONS (continued)

T-90-20

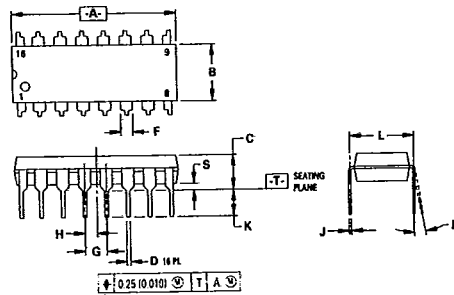
P SUFFIX
PLASTIC PACKAGE
CASE 646-06



DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	18.16	19.56	0.715	0.770
B	6.10	6.60	0.240	0.260
C	3.69	4.69	0.145	0.185
D	0.38	0.53	0.015	0.021
F	1.02	1.78	0.040	0.070
G	2.54 BSC		0.100 BSC	
H	1.32	2.41	0.052	0.095
J	0.20	0.38	0.008	0.015
K	2.92	3.43	0.115	0.135
L	7.62 BSC		0.300 BSC	
M	0°	10°	0°	10°
N	0.29	1.01	0.015	0.039

- NOTES:
- LEADS WITHIN 0.13 mm (0.005) RADIUS OF TRUE POSITION AT SEATING PLANE AT MAXIMUM MATERIAL CONDITION.
 - DIMENSION "L" TO CENTER OF LEADS WHEN FORMED PARALLEL.
 - DIMENSION "B" DOES NOT INCLUDE MOLD FLASH.
 - ROUNDED CORNERS OPTIONAL.

P SUFFIX
PLASTIC PACKAGE
CASE 648-08

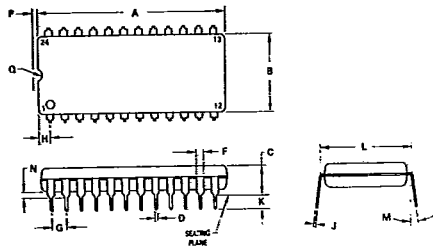


DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	18.80	19.55	0.740	0.770
B	6.35	6.85	0.250	0.270
C	3.69	4.44	0.145	0.175
D	0.39	0.53	0.015	0.021
F	1.02	1.77	0.040	0.070
G	2.54 BSC		0.100 BSC	
H	1.27	1.77	0.050	0.070
J	0.21	0.38	0.008	0.015
K	2.80	3.30	0.110	0.130
L	7.50	7.74	0.295	0.305
M	0°	10°	0°	10°
S	0.51	1.01	0.020	0.040

- NOTES:
- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 - CONTROLLING DIMENSION: INCH.
 - DIMENSION "L" TO CENTER OF LEADS WHEN FORMED PARALLEL.
 - DIMENSION "B" DOES NOT INCLUDE MOLD FLASH.
 - ROUNDED CORNERS OPTIONAL.

P SUFFIX
PLASTIC PACKAGE
CASE 649-03

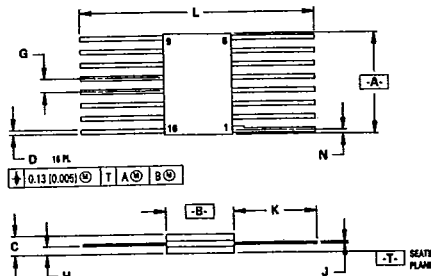
(PW SUFFIX
FOR MC10H181
ONLY)



DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	31.50	32.13	1.240	1.265
B	13.21	13.72	0.520	0.540
C	4.70	5.21	0.185	0.205
D	0.38	0.51	0.015	0.020
F	1.02	1.52	0.040	0.060
G	2.54 BSC		0.100 BSC	
H	1.65	2.16	0.065	0.085
J	0.20	0.30	0.008	0.012
K	2.92	3.43	0.115	0.135
L	14.99	15.49	0.590	0.610
M	10°		10°	
N	0.51	1.02	0.020	0.040
P	0.13	0.38	0.005	0.015
Q	0.51	0.76	0.020	0.030

- NOTES:
- LEADS WITHIN 0.13 mm (0.005) RADIUS OF TRUE POSITION AT SEATING PLANE AT MAXIMUM MATERIAL CONDITION.
 - DIMENSION "L" TO CENTER OF LEADS WHEN FORMED PARALLEL.

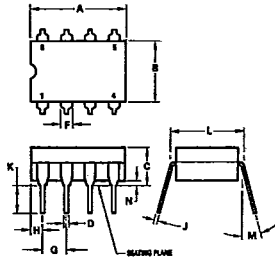
F SUFFIX
CERAMIC PACKAGE
CASE 650-05



DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	9.40	9.90	0.370	0.390
B	6.73	6.80	0.265	0.269
C	1.53	2.15	0.060	0.085
D	0.38	0.48	0.014	0.019
G	1.27 BSC		0.050 BSC	
H	0.64	0.01	0.025	0.040
J	0.11	0.17	0.004	0.007
K	6.35	9.39	0.250	0.370
L	18.93	—	0.745	—
N	—	0.50	—	0.020

- NOTES:
- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 - CONTROLLING DIMENSION: INCH.
 - DIMENSION "A" AND "B" ALLOW FOR LID MISMATCH, AND GLASS MENISCUS.
 - DIMENSION "H" SHALL BE MEASURED AT THE POINT OF EXIT OF THE LEAD FROM THE BODY.
 - LEAD NUMBER 1 IDENTIFIED BY TAB ON LEAD OR DOT ON COVER.
 - DIMENSION "J" INCLUDES SOLDER LEAD FINISH.
 - LEAD NUMBERS SHOWN FOR REFERENCE ONLY.

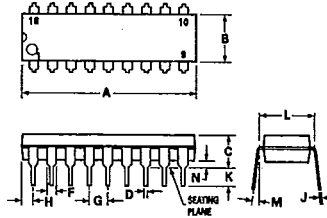
**L SUFFIX
CERAMIC PACKAGE
CASE 693-02**



DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	9.91	10.92	0.390	0.430
B	6.22	6.99	0.245	0.275
C	4.32	5.08	0.170	0.200
D	0.41	0.51	0.016	0.020
F	1.40	1.65	0.055	0.065
G	2.54 BSC		0.100 BSC	
H	1.14	1.65	0.045	0.065
J	0.20	0.30	0.008	0.012
K	3.18	4.08	0.125	0.160
L	7.37	7.87	0.290	0.310
M	—	15°	—	15°
N	0.51	1.02	0.020	0.040

- NOTES:
 1. LEADS WITHIN 0.13 mm (0.005) RAD OF TRUE POSITION AT SEATING PLANE AT MAXIMUM MATERIAL CONDITION.
 2. DIMENSION "L" TO CENTER OF LEADS WHEN FORMED PARALLEL.

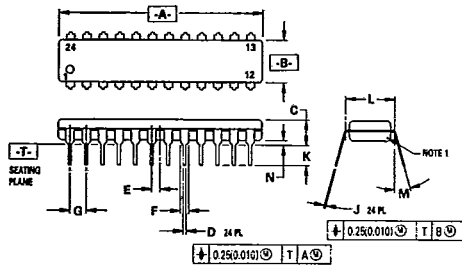
**P SUFFIX
PLASTIC PACKAGE
CASE 707-02**



DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	22.27	23.24	0.875	0.915
B	6.10	6.60	0.240	0.260
C	3.56	4.57	0.140	0.180
D	0.36	0.56	0.014	0.022
F	1.27	1.78	0.050	0.070
G	2.54 BSC		0.100 BSC	
H	1.02	1.52	0.040	0.060
J	0.20	0.30	0.008	0.012
K	2.92	3.43	0.115	0.135
L	7.62 BSC		0.300 BSC	
M	0°	15°	0°	15°
N	0.51	1.02	0.020	0.040

- NOTES:
 1. POSITIONAL TOLERANCE OF LEADS (D), SHALL BE WITHIN 0.25mm(0.010) AT MAXIMUM MATERIAL CONDITION, IN RELATION TO SEATING PLANE AND EACH OTHER.
 2. DIMENSION "L" TO CENTER OF LEADS WHEN FORMED PARALLEL.
 3. DIMENSION B DOES NOT INCLUDE MOLD FLASH.

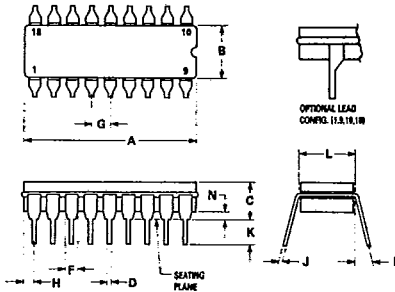
**P SUFFIX
PLASTIC PACKAGE
CASE 724-03**



DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	31.25	32.13	1.230	1.265
B	6.35	6.85	0.250	0.270
C	3.68	4.44	0.145	0.175
D	0.38	0.51	0.015	0.020
E	1.27 BSC		0.050 BSC	
F	1.02	1.52	0.040	0.060
G	2.54 BSC		0.100 BSC	
J	0.18	0.30	0.007	0.012
K	2.80	3.56	0.110	0.140
L	7.62 BSC		0.300 BSC	
M	0°	15°	0°	15°
N	0.51	1.01	0.020	0.040

- NOTES:
 1. CHAMFERED CONTOUR OPTIONAL.
 2. DIM "L" TO CENTER OF LEADS WHEN FORMED PARALLEL.
 3. DIMENSIONS AND TOLERANCES PER ANSI Y14.34, 1982.
 4. CONTROLLING DIMENSION: INCH.

**L SUFFIX
CERAMIC PACKAGE
CASE 726-04**



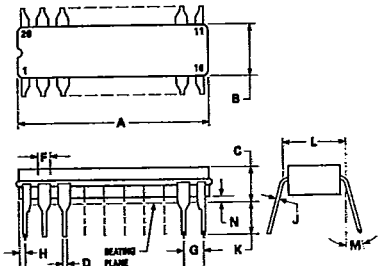
DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	22.35	23.11	0.880	0.910
B	6.10	7.49	0.240	0.295
C	—	5.08	—	0.200
D	0.38	0.53	0.015	0.021
F	1.40	1.78	0.055	0.070
G	2.54 BSC		0.100 BSC	
H	0.51	1.14	0.020	0.045
J	0.20	0.30	0.008	0.012
K	3.18	4.32	0.125	0.170
L	7.62 BSC		0.300 BSC	
M	0°	15°	0°	15°
N	0.51	1.02	0.020	0.040

- NOTES:
 1. LEADS, TRUE POSITIONED WITHIN 0.25 mm (0.010) DIA. AT SEATING PLANE, AT MAXIMUM MATERIAL CONDITION.
 2. DIM "L" TO CENTER OF LEADS WHEN FORMED PARALLEL.
 3. DIM "A" & "B" INCLUDES MENISCUS.
 4. "F" DIMENSION IS FOR FULL LEADS. "HALF" LEADS ARE OPTIONAL AT LEAD POSITIONS 1, 9, 10, AND 18.

PACKAGE OUTLINE DIMENSIONS (continued)

1

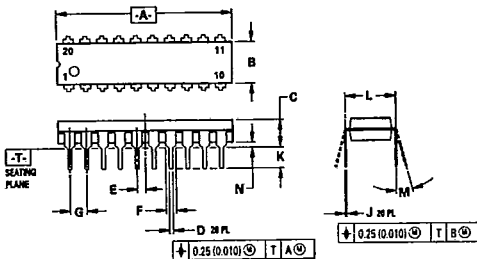
**L SUFFIX
CERAMIC PACKAGE
CASE 732-03**



DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	23.88	25.15	0.940	0.990
B	6.80	7.49	0.260	0.295
C	3.81	5.08	0.150	0.200
D	0.38	0.56	0.015	0.022
F	1.40	1.65	0.055	0.065
G	2.54 BSC		0.100 BSC	
H	0.51	1.27	0.020	0.050
J	0.20	0.30	0.008	0.012
K	3.18	4.06	0.125	0.160
L	7.62 BSC		0.300 BSC	
M	0°	15°	0°	15°
N	0.25	1.02	0.010	0.040

- NOTES:
 1. LEADS WITHIN 0.25 mm (0.010) DIA., TRUE POSITION AT SEATING PLANE, AT MAXIMUM MATERIAL CONDITION.
 2. DIM L TO CENTER OF LEADS WHEN FORMED PARALLEL.
 3. DIM A AND B INCLUDES MENISCUS.

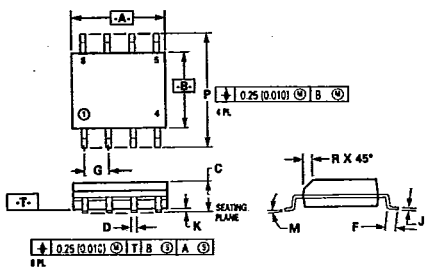
**P SUFFIX
PLASTIC PACKAGE
CASE 738-03**



DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	25.55	27.17	1.010	1.070
B	6.10	6.60	0.240	0.260
C	3.81	4.57	0.150	0.180
D	0.38	0.56	0.015	0.022
E	1.27 BSC		0.050 BSC	
F	1.27	1.77	0.050	0.070
G	2.54 BSC		0.100 BSC	
J	0.21	0.38	0.008	0.015
K	2.80	3.55	0.110	0.140
L	7.62 BSC		0.300 BSC	
M	0°	15°	0°	15°
N	0.51	1.01	0.020	0.040

- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. DIMENSION "L" TO CENTER OF LEAD WHEN FORMED PARALLEL.
 4. DIMENSION "B" DOES NOT INCLUDE MOLD FLASH.

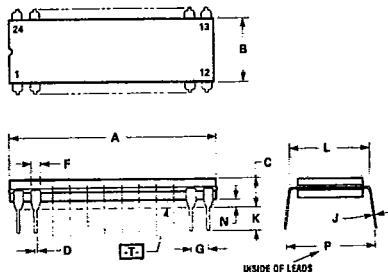
**D SUFFIX
PLASTIC SOIC PACKAGE
CASE 751-03**



DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	4.80	5.00	0.189	0.196
B	3.90	4.00	0.150	0.157
C	1.35	1.75	0.054	0.068
D	0.35	0.49	0.014	0.019
F	0.40	1.25	0.015	0.049
G	0.27 BSC		0.050 BSC	
J	0.18	0.25	0.007	0.009
K	0.10	0.25	0.004	0.009
M	0°	7°	0°	7°
P	5.80	6.20	0.229	0.244
R	0.25	0.50	0.010	0.019

- NOTES:
 1. DIMENSIONS "A" AND "B" ARE DATUMS AND "T" IS A DATUM SURFACE.
 2. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 3. CONTROLLING DIM: MILLIMETER.
 4. DIMENSION "A" AND "B" DO NOT INCLUDE MOLD PROTRUSION.
 5. MAXIMUM MOLD PROTRUSION 0.15 (0.006) PER SIDE.

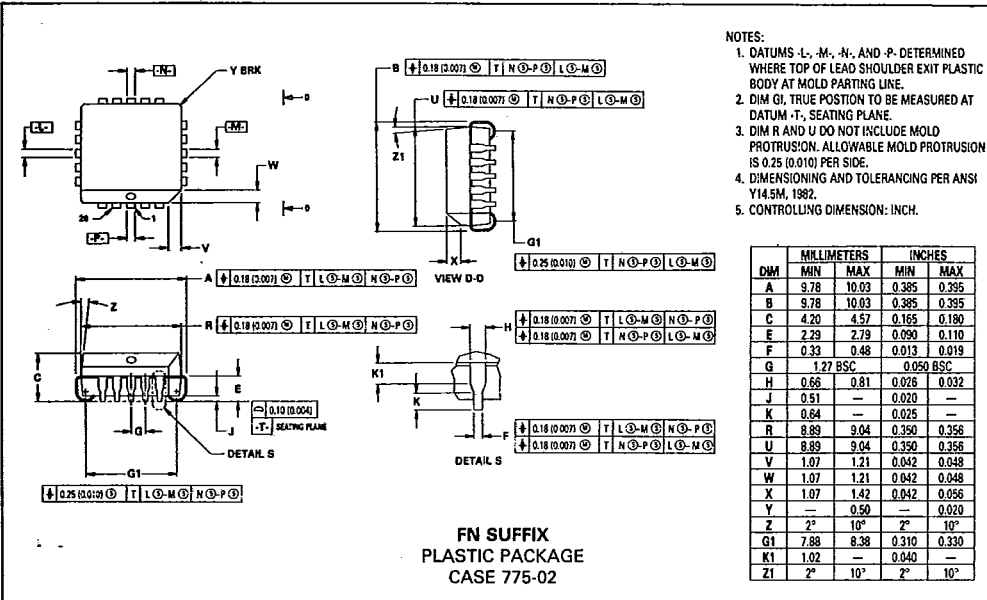
**L SUFFIX
CERAMIC PACKAGE
CASE 758-01**



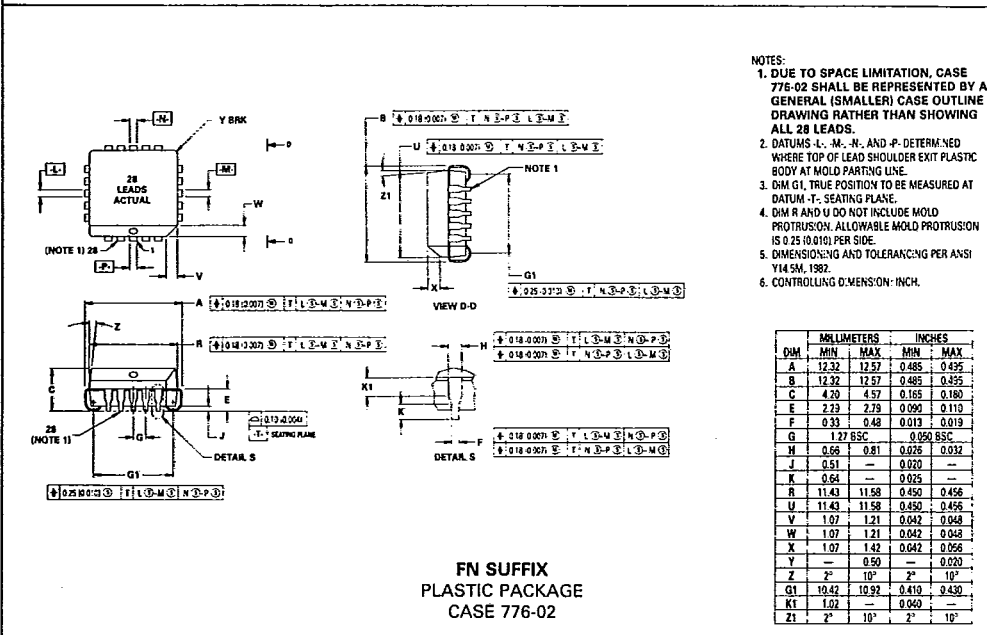
DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	31.50	32.64	1.240	1.285
B	7.24	7.75	0.285	0.305
C	3.68	4.44	0.145	0.175
D	0.38	0.53	0.015	0.021
F	1.14	1.97	0.045	0.062
G	2.54 BSC		0.100 BSC	
J	0.20	0.33	0.008	0.013
K	2.54	4.19	0.100	0.165
L	7.62	7.87	0.300	0.310
N	0.51	1.27	0.020	0.050
P	9.14	10.16	0.360	0.400

- NOTES:
 1. DIMENSION A IS DATUM.
 2. POSITIONAL TOLERANCE FOR LEADS: 24 PLACES $\pm 0.25 (0.010) \text{ } \text{\textcircled{T}} \text{ } \text{\textcircled{A}}$
 3. $\text{\textcircled{T}}$ IS SEATING PLANE.
 4. DIMENSION L TO CENTER OF LEADS WHEN FORMED PARALLEL.
 5. DIMENSIONING AND TOLERANCING PER ANSI Y14.5, 1973.

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- NOTES:
- DATUMS -L-, M-, N-, AND -P- DETERMINED WHERE TOP OF LEAD SHOULDER EXIT PLASTIC BODY AT MOLD PARTING LINE.
 - DIM G1, TRUE POSITION TO BE MEASURED AT DATUM -T-, SEATING PLANE.
 - DIM R AND U DO NOT INCLUDE MOLD PROTRUSION. ALLOWABLE MOLD PROTRUSION IS 0.25 (0.010) PER SIDE.
 - DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 - CONTROLLING DIMENSION: INCH.



- NOTES:
- DUE TO SPACE LIMITATION, CASE 776-02 SHALL BE REPRESENTED BY A GENERAL (SMALLER) CASE OUTLINE DRAWING RATHER THAN SHOWING ALL 28 LEADS.
 - DATUMS -L-, M-, N-, AND -P- DETERMINED WHERE TOP OF LEAD SHOULDER EXIT PLASTIC BODY AT MOLD PARTING LINE.
 - DIM G1, TRUE POSITION TO BE MEASURED AT DATUM -T-, SEATING PLANE.
 - DIM R AND U DO NOT INCLUDE MOLD PROTRUSION. ALLOWABLE MOLD PROTRUSION IS 0.25 (0.010) PER SIDE.
 - DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 - CONTROLLING DIMENSION: INCH.

MECL Logic Surface Mount

WHY SURFACE MOUNT?

Surface Mount Technology is now being utilized to offer answers to many problems that have been created in the use of insertion technology.

Limitations have been reached with insertion packages and PC board technology. Surface Mount Technology offers the opportunity to continue to advance the State-of-the-Art designs that cannot be accomplished with Insertion Technology.

Surface Mount Packages allow more optimum device performance with the smaller Surface Mount configuration. Internal lead lengths, parasitic capacitance and inductance that placed limitations on chip performance have been reduced.

The lower profile of Surface Mount Packages allows more boards to be utilized in a given amount of space. They are stacked closer together and utilize less total volume than insertion populated PC boards.

Printed circuit costs are lowered with the reduction of the number of board layers required. The elimination or reduction of the number of plated through holes in the board, contribute significantly to lower PC board prices.

Surface Mount assembly does not require the preparation of components that are common on insertion technology lines. Surface Mount components are sent directly to the assembly line, eliminating an intermediate step.

Automatic placement equipment is available that can place Surface Mount components at the rate of a few thousand per hour to hundreds of thousands of components per hour.

Surface Mount Technology is cost effective, allowing the manufacturer the opportunity to produce smaller units and offer increased functions with the same size product.

MECL AVAILABILITY IN SURFACE MOUNT

Motorola is now offering MECL 10K and MECL 10KH in the PLCC (Plastic Leaded Chip Carrier) packages.

MECL in PLCC may be ordered in conventional plastic rails or on Tape and Reel. Refer to the Tape and Reel section for ordering details.

TAPE AND REEL

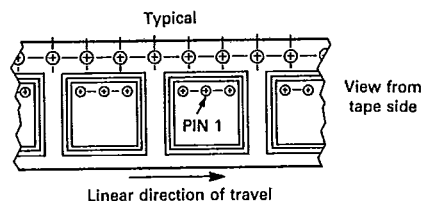
Motorola has now added the convenience of Tape and Reel packaging for our growing family of standard Integrated Circuit products. The packaging fully conforms to

the latest EIA RS-481A specification. The antistatic embossed tape provides a secure cavity sealed with a peel-back cover tape.

GENERAL INFORMATION

- Reel Size 13 inch (330 mm) Suffix: R2
- Tape Width 16 mm
- Units/Reel 1000

MECHANICAL POLARIZATION



ORDERING INFORMATION

- Minimum Lot Size/Device Type = 3000 Pieces.
- No Partial Reel Counts Available.
- To order devices which are to be delivered in Tape and Reel, add the appropriate suffix to the device number being ordered.

EXAMPLE:

ORDERING CODE

MC10100FN
 MC10100FNR2
 MC10H100FN
 MC10H100FNR2
 MC12015D
 MC12015DR2

SHIPMENT METHOD

Magazines (Rails)
 13 inch Tape and Reel
 Magazines (Rails)
 13 inch Tape and Reel
 Magazines (Rails)
 13 inch Tape and Reel

DUAL-IN-LINE PACKAGE TO PLCC PIN CONVERSION DATA

The following tables give the equivalent I/O pinouts of Dual-In-Line (DIL) packages and Plastic Leaded Chip Carrier (PLCC) packages.

Conversion Tables

16 PIN DIL	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
20 PIN PLCC	2	3	4	5	7	8	9	10	12	13	14	15	17	18	19	20

20 PIN DIL	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
20 PIN PLCC	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20

24 PIN DIL	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24
28 PIN PLCC	2	3	4	5	6	7	9	10	11	12	13	14	16	17	18	19	20	21	23	24	25	26	27	28