181 Orig

005023

New 995520

5023

ADD

ANADIGICS

Your GaAs IC Source

Advanced Product Information

ATH8010

8ns TRACK/HOLD

R1=100 Ohms , C1=5 pF	MIN	TYP	MAX	UNITS
ANALOG INPUT/OUTPUT				
Voltage Range ±		2.5	4*	V
Input Resistance/Capacitance	500/1			Ohms/pF
Output Current ±	20			mA
Output Impedance @ DC		<1		Ohms
Capacitive Load	5		20	pF
* Supplies ±8v , R1>200 Ohms				
TRANSFER CHARACTERISTICS				
Gain Error : Initial	0.99		1.1	V/V
Gain Error : Drift		100		ppm/°C
Linearity Error (-30 to +70 °C)		0.1		% F.S.R.
Offset Voltage (Track Mode)		50		mV
Offset Voltage : Drift		30		µV/°C
Pedestal (Track to Hold)		30	60	mV
Pedestal : Drift		10		µV/°C
Pedestal : Variation with Vin		100		µV/V
DYNAMIC CHARACTERISTICS				
Delay Time			0.5	nS
Acquisition Time		8		nS
Track/Hold Settling		5		nS
Aperature Time Uncertainty		1		pS RMS
Noise : Track		0.6		mV RMS
Noise : Hold				
Bandwidth , Large Signal		180		MHz
Slew Rate		1.5		V/nS
Feedthrough Attenuation @ 20 MHz		-40		dB
Feedthrough Attenuation @ 1 MHz		-60		dB
Droop Rate		30		mV/µS
Harmonic Dist. (Track , f=20 Mhz , $2v_{pp}$)			60	dB
GATE INPUT CHARACTERISTICS *				
Voltage High	-0.8		-1.1	V
Voltage Low	-1.8		-2.0	V
Current			200	nA
Capacitance			2	pF
* 1 Std. ECL 100k Load				
POWER SUPPLY REQUIREMENTS				
Supply Voltage Range	±5		±8	V
P.S.R.R. : Analog		60		dB
P.S.R.R. : Digital		60		dB
Current Drain , DC		60		mA
Power Consumption	500	600	1000	mW
Operating Temperature , Case	-30		+70	°C
Storage Temperature	-55		+125	°C

Specs at 25 °C unless noted otherwise

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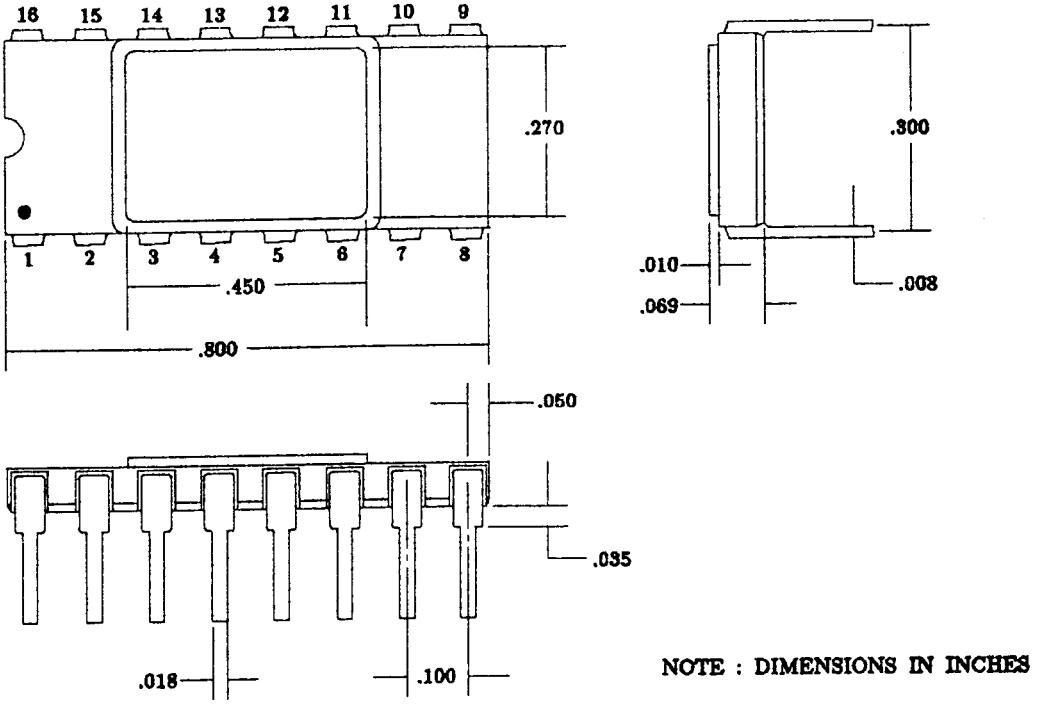
DIMENSIONS AND PIN NUMBERS



NOTE : DIMENSIONS IN INCHES

CIRCUIT SCHEMATIC