

CHAPTER 12 PINOUTS

12.1. 82497 CACHE CONTROLLER PINOUTS

The 82497 Cache Controller pinout is the same as the 82496 pinout with two exceptions. First, 9 pins become 3.3V V_{CC3} supply inputs. Second, pins previously used as BT[3:0] and MBT[3:0] signals are now V_{SS} pins.

Figures 12-1 and 12-2 show the top view and the pin side view of the 822497 Cache Controller pinout.

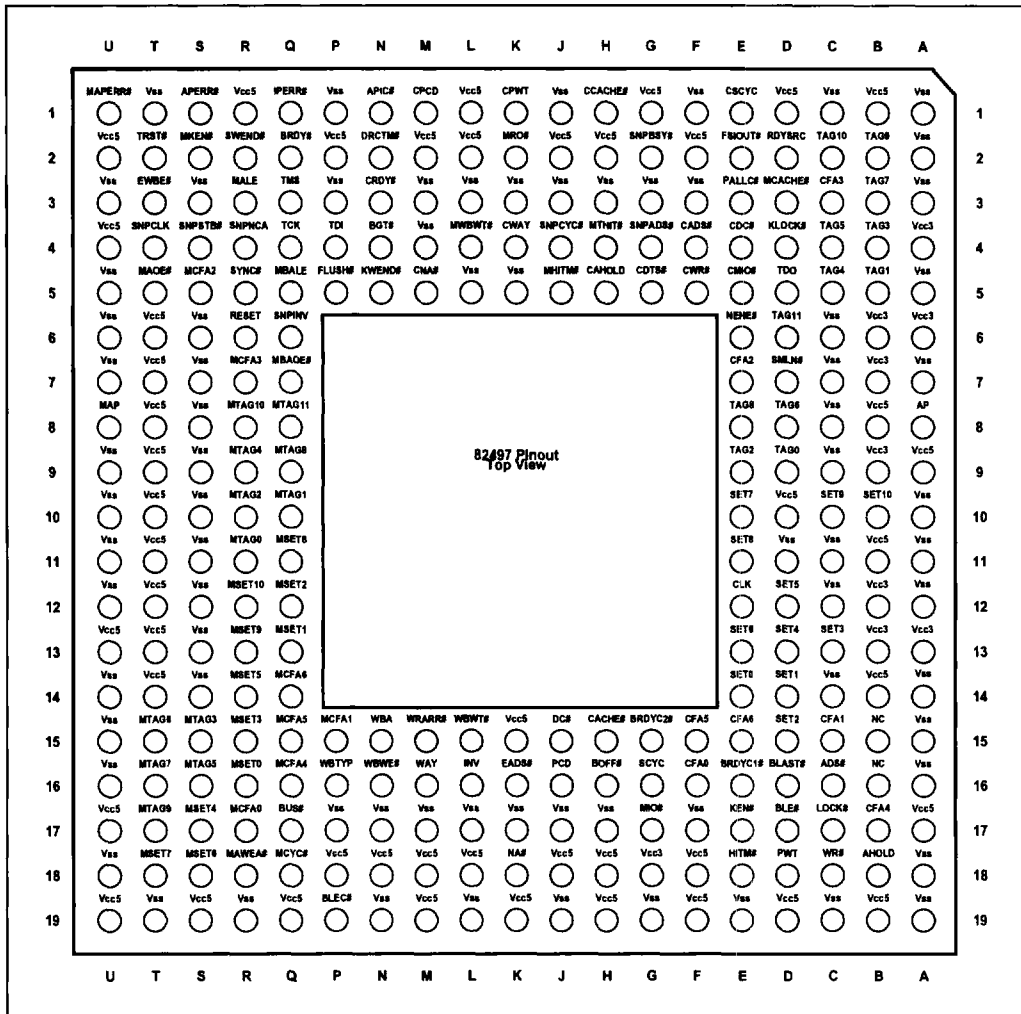


Figure 12-1. 82497 Cache Controller Pinout Top Side View

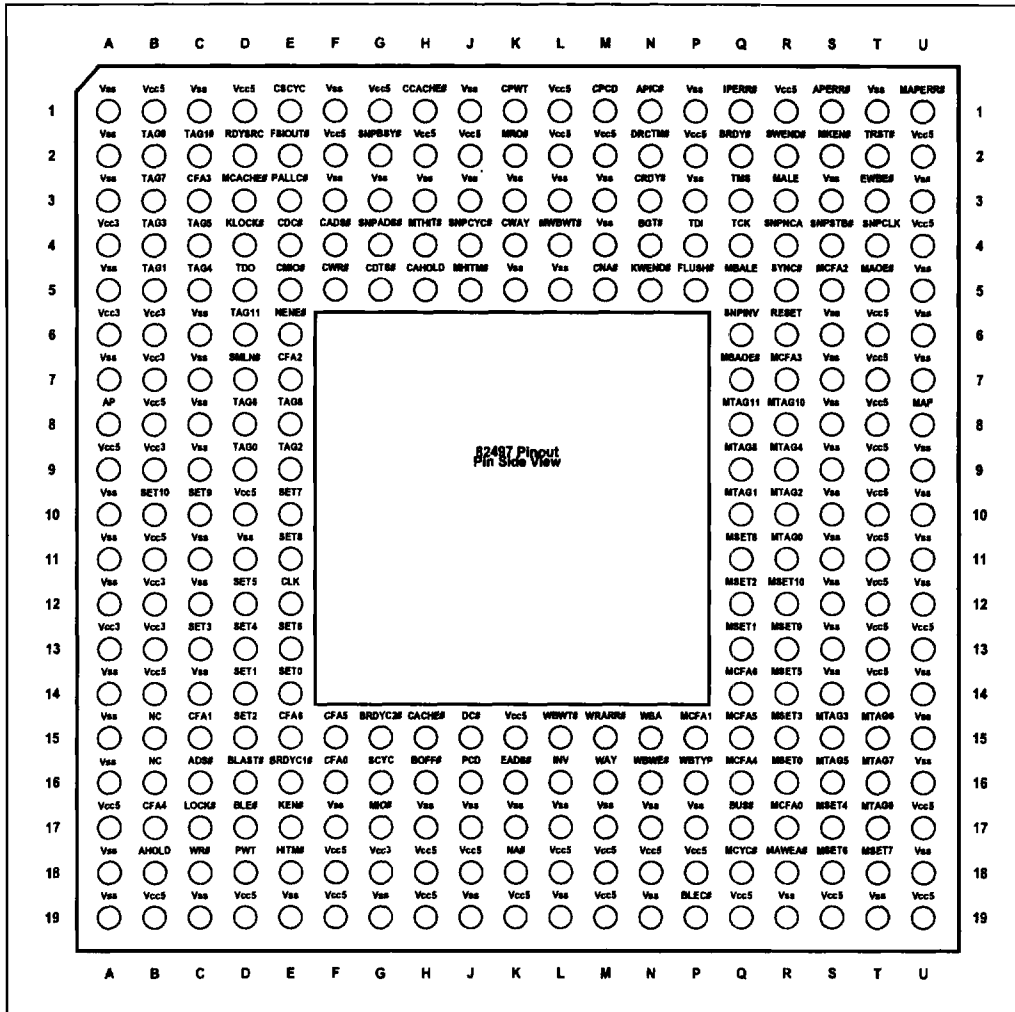


Figure 12-2. 82497 Cache Controller Pinout Pin Side View

12.2. 82492 CACHE MEMORY PINOUT

The 82492 Cache SRAM pinout is the same as the 82491 pinout except that 2 pins become 3.3V V_{CC3} supply inputs.

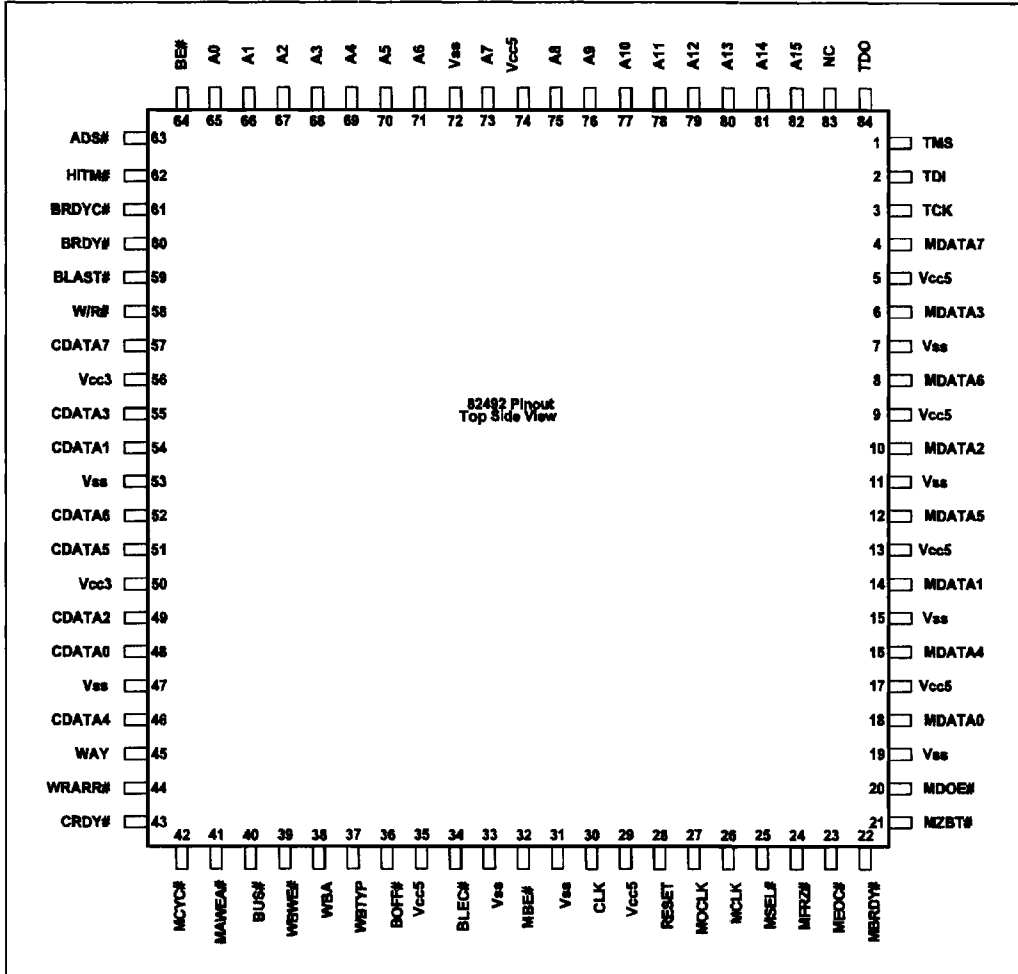


Figure 12-3. 82492 Cache Memory Pinout Top Side View