

## FEATURES

- 3.3V and -5V power supply
- 620ps propagation delay
- Fully differential design
- Supports low voltage operation
- ESD protection of 2000V
- Available in 20-pin SOIC package

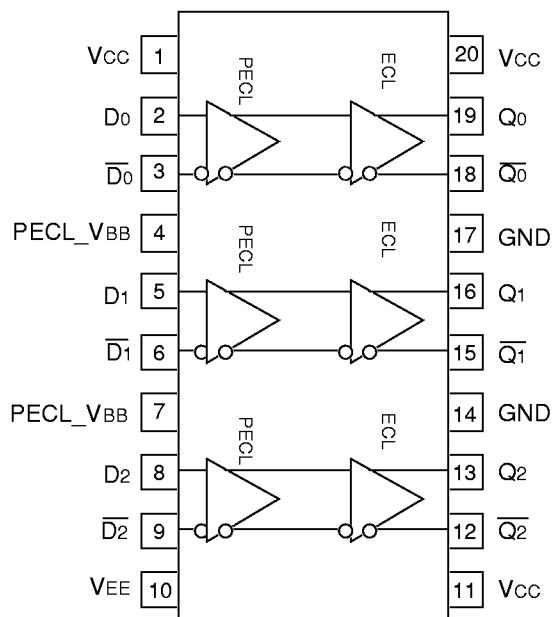
## DESCRIPTION

The SY100EL91L is a triple LVPECL-to-ECL or LVPECL to LVECL translator.

A  $V_{BB}$  output is provided for interfacing with single ended PECL signals at the input. If a single ended input is to be used, the  $V_{BB}$  output should be connected to the  $\bar{D}$  input. The active signal would then drive the D input. When used, the  $V_{BB}$  output should be bypassed to ground via a  $0.01\mu F$  capacitor. The  $V_{BB}$  output is designed to act as the switching reference for the EL91L under single ended input switching conditions. As a result this pin can only source/sink up to  $0.5mA$  of current.

To accomplish the level translation the EL91L requires three power rails. The  $V_{CC}$  supply should be connected to the positive supply, and the  $V_{EE}$  pin should be connected to the negative power supply. The GND pins as expected are connected to the system ground plane. Both  $V_{EE}$  and  $V_{CC}$  should be bypassed to ground via  $0.01\mu F$  capacitors.

Under open input conditions, the  $\bar{D}$  input will be biased at  $V_{CC}/2$  and the D input will be pulled to GND. This condition will force the Q output to a LOW, ensuring stability.


 SOIC  
 TOP VIEW

## PIN NAMES

Pin	Function
D <sub>n</sub>	PECL Inputs
Q <sub>n</sub>	ECL Outputs
PECL_VBB	PECL Reference Voltage Output

## FUNCTION TABLE

Function	V <sub>CC</sub>	V <sub>EE</sub>
LVPECL-to-ECL	3.3V	-5.0V
LVPECL-to-LVECL	3.3V	-3.3V

### 3.3V LVPECL INPUT DC ELECTRICAL CHARACTERISTICS

Symbol	Parameter	TA = -40°C			TA = 0°C			TA = +25°C			TA = +85°C			Unit
		Min.	Typ.	Max.	Min.	Typ.	Max.	Min.	Typ.	Max.	Min.	Typ.	Max.	
V <sub>CC</sub>	Power Supply Voltage	3.0	—	3.8	3.0	—	3.8	3.0	3.3	3.8	3.0	—	3.8	V
V <sub>IH</sub>	Input HIGH Voltage <sup>(1)</sup>	2.135	—	2.420	2.135	—	2.420	2.135	2.350	2.420	2.135	—	2.420	V
V <sub>IL</sub>	Input LOW Voltage <sup>(1)</sup>	1.490	—	1.825	1.490	—	1.825	1.490	—	1.825	1.490	—	1.825	V
I <sub>IH</sub>	Input HIGH Current	—	—	150	—	—	150	—	—	150	—	—	150	μA
I <sub>IL</sub>	Input LOW Current	D <sub>n</sub> —600	0.5 —	—	0.5 —600	—	—	0.5 —600	—	—	0.5 —600	—	—	μA
V <sub>BB</sub>	Output Reference <sup>(1)</sup>	1.920	—	2.040	1.920	—	2.040	1.920	—	2.040	1.920	—	2.040	V
I <sub>CC</sub>	Power Supply Current	—	—	10	—	—	10	—	6.0	10	—	—	10	mA

**NOTES:**

1. These levels are for V<sub>CC</sub> = 3.3V. Level specifications will vary 1:1 with V<sub>CC</sub>.

### ECL/LVECL OUTPUT DC ELECTRICAL CHARACTERISTICS

Symbol	Parameter	TA = -40°C			TA = 0°C			TA = +25°C			TA = +85°C			Unit
		Min.	Typ.	Max.										
V <sub>EE</sub>	Power Supply ECL Voltage LVECL	-4.2 -3.0	— —	-5.5 -3.8	V									
V <sub>OH</sub>	Output HIGH Voltage	-1085	—	-880	-1025	—	-880	-1025	-955	-880	-1025	—	-880	mV
V <sub>OL</sub>	Output LOW Voltage	-1830	—	-1555	-1810	—	-1620	-1810	-1705	-1620	-1810	—	-1620	mV
I <sub>EE</sub>	Power Supply Current	—	—	28	—	—	28	—	22	28	—	—	30	mA

## AC ELECTRICAL CHARACTERISTICS

LVPECL: V<sub>CC</sub> = +3.0V to +3.8V, ECL: V<sub>EE</sub> = -4.2V to -5.5V, LVECL: V<sub>EE</sub> = -3.0V to -3.8V

Symbol	Parameter	TA = -40°C			TA = 0°C			TA = +25°C			TA = +85°C			Unit
		Min.	Typ.	Max.	Min.	Typ.	Max.	Min.	Typ.	Max.	Min.	Typ.	Max.	
tPLH	Propagation Delay Diff.	490	590	690	510	610	710	520	620	720	560	660	760	ps
tPHL	D to Q S.E.	440	590	740	460	610	760	470	620	770	510	660	810	ps
tskew	Within-Device Skew <sup>(1)</sup>													ps
	Output-to-Output	—	40	100	—	40	100	—	40	100	—	40	100	ps
	Part-to-Part (Diff.)	—	—	200	—	—	200	—	—	200	—	—	200	ps
	Duty Cycle (Diff.)	—	25	—	—	25	—	—	25	—	—	25	—	ps
V <sub>PP</sub>	Minimum Input Swing <sup>(2)</sup>	150	—	—	150	—	—	150	—	—	150	—	—	mV
V <sub>CMR</sub>	Common Mode Range <sup>(3)</sup>													V
	V <sub>PP</sub> < 500mV	1.3	—	V <sub>CC</sub> -0.2	1.2	—	V <sub>CC</sub> -0.2	1.2	—	V <sub>CC</sub> -0.2	1.2	—	V <sub>CC</sub> -0.2	
	V <sub>PP</sub> > 500mV	1.5	—	V <sub>CC</sub> -0.2	1.4	—	V <sub>CC</sub> -0.2	1.4	—	V <sub>CC</sub> -0.2	1.4	—	V <sub>CC</sub> -0.2	
t <sub>r</sub>	Output Rise/Fall Times Q (20% to 80%)	320	400	580	320	400	580	320	400	580	320	400	580	ps
t <sub>f</sub>														ps

### NOTES:

1. Skew is measured between outputs under identical transitions.
2. Minimum input swing for which AC parameters are guaranteed. The device has a DC gain of ~40.
3. The CMR range is referenced to the most positive side of the differential input signal. Normal operation is obtained if the HIGH level falls within the specified range and the peak-to-peak voltage lies between V<sub>PP</sub> min. and 1V.

## PRODUCT ORDERING CODE

Ordering Code	Package Type	Operating Range
SY100EL91LZC	Z20-1	Commercial
SY100EL91LZCTR	Z20-1	Commercial

## 20 LEAD PLASTIC SOIC (Z20-1)

FILE/REV #: PD0028A03

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