



High-Speed CMOS 64Kx4 SRAM with Common I/O

QS8828
ADVANCE
INFORMATION

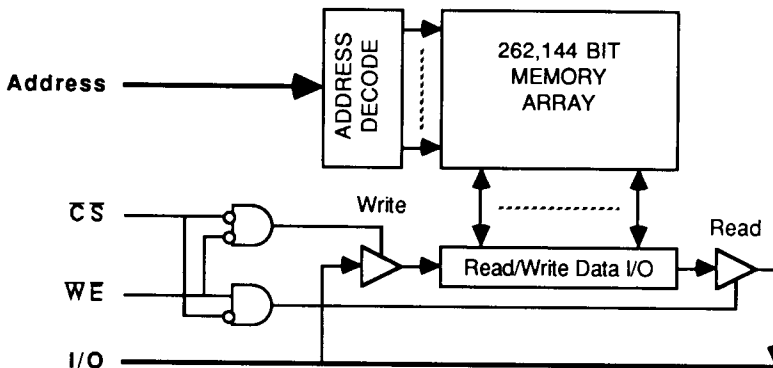
FEATURES/BENEFITS

- High Speed Access and Cycle times
- 15ns/20ns/25ns Commercial
- 20ns/25ns/35ns Military
- TTL compatible I/O
- Low power, high-speed QCMOS™ technology
- Military product compliant to MIL-STD-883, Class B
- 6-Transistor cell for high reliability
- Ideal for reliable, dense memory systems
- Available in 24-pin DIP, 24-pin ZIP, 24-pin 300 mil SOJ & SOIC
- Low Standby current
- JEDEC standard pinout

DESCRIPTION

The QS8828 is a high-speed 256K SRAM organized as 64Kx4 . It is manufactured in a high-performance CMOS process, and it based on a 6-transistor cell design for high reliability of data retention. The high-speed access times of the QS8828 make it useful in cache data RAM, cache tag RAMs, high-speed scratchpad memories, look-up tables, pipelined DSP and bit-slice systems. Low operating power and excellent latch-up and ESD protection are provided.

FUNCTIONAL BLOCK DIAGRAM

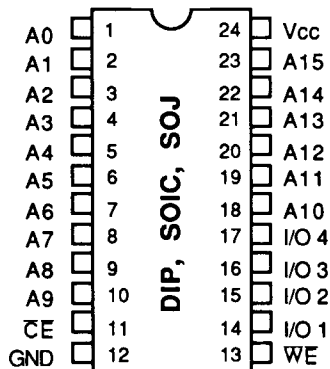


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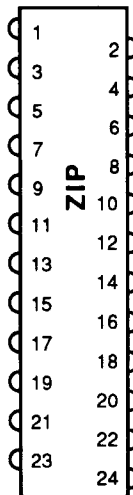
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PIN CONFIGURATIONS



ALL PINS TOP VIEW



PIN DESCRIPTION

Pin	Name	I/O	Function
A	A	I	Address
I/O1 - I/O4	I/O	I/O	Data
CS	CS	I	Chip Select
WE	WE	I	Write Enable

FUNCTION TABLE

CS	WE	I/O	Power	Function
H	X	High Z	Standby	Deselect
L	H	Data Out	Active	Read
L	L	Data In	Active	Write