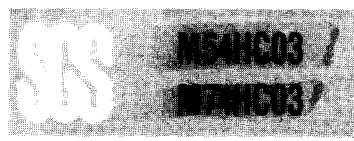


HS-C²MOS™ INTEGRATED CIRCUITS

041172



PRODUCT PREVIEW

QUAD 2-INPUT NAND (OPEN DRAIN)

DESCRIPTION

The M54/74HC03 is a high speed CMOS QUAD 2-INPUT NAND (OPEN DRAIN) fabricated in silicon gate C²MOS technology.

It has the same high speed performance of LSTTL combined with true CMOS low power consumption. All inputs are equipped with protection circuits against static discharge or transient excess voltage.

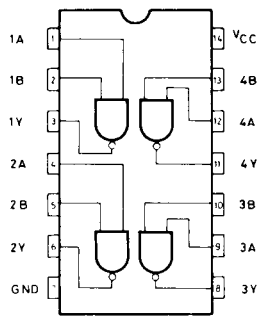
B1 Plastic Package **F1** Ceramic Package **C1** Chip Carrier

ORDERING NUMBERS: M54HC03 F1
M74HC03 B1
M74HC03 F1
M74HC03 C1

FEATURES

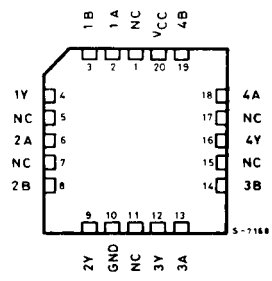
- Low Power Dissipation
 $I_{CC} = 1 \mu A$ (Max.) at $T_A = 25^\circ C$
- High Noise Immunity
 $V_{NIH} = V_{NIL} = 28\% V_{CC}$ (Min.)
- Output Drive Capability
10 LSTTL Loads
- Symmetrical Output Impedance
 $|I_{OH}| = I_{OL} = 4 \text{ mA}$ (Min.)
- Balanced Propagation Delays
 $t_{PLH} = t_{PHL}$
- Wide Operating Voltage Range
 $V_{CC} \text{ (opr)} = 2V \text{ to } 6V$
- Pin and Function compatible with 54/74LS03

PIN CONNECTIONS (top view)



Dual in line

CHIP CARRIER



NC = No Internal Connection

INPUT AND OUTPUT EQUIVALENT CIRCUIT

