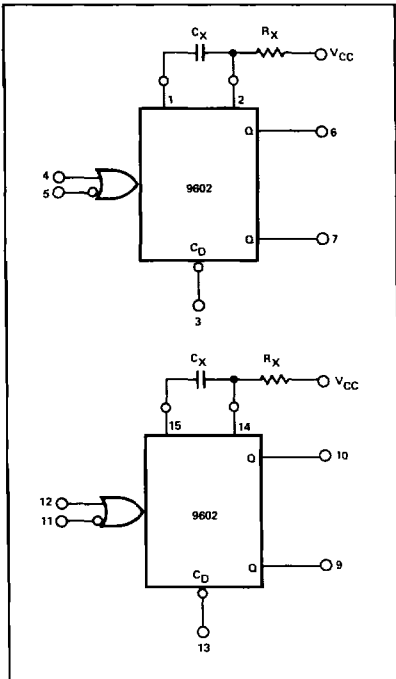
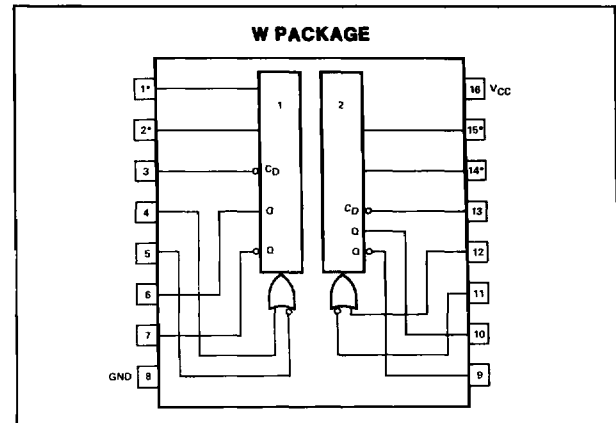
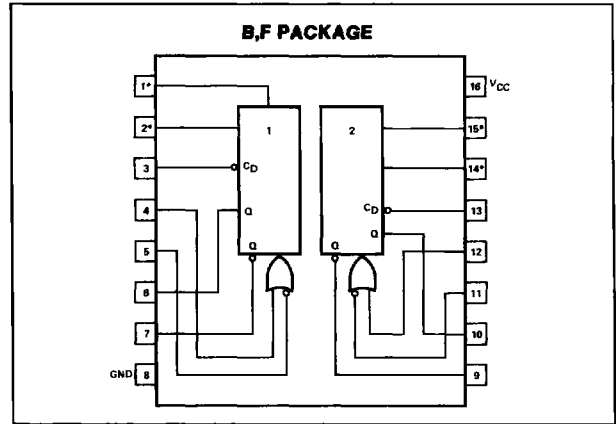


SPEED/PACKAGE AVAILABILITY
 S9602 F,W N9602 B,F

BLOCK DIAGRAM



PIN CONFIGURATION



SWITCHING CHARACTERISTICS $V_{CC} = 5V \pm 10\%$, $T_A = -55^\circ C$ to $125^\circ C$

TEST CONDITIONS			9602			UNIT
			$R_X = 5.0\Omega$ $C_X = 0\Omega$ $C_L = 15pF$			
PARAMETER	FROM INPUT	TO OUTPUT	MIN	TYP	MAX	UNIT
Propagation delay time						
t_{PLH} Low-to-high		True		25	35	ns
t_{PHL} High-to-low		Complement		29	43	
$t_{(min)}$		True		72	90	ns
		Complement		78	100	
t			3.08	3.42	3.76	μs
$V_{CC} = 5V \pm 5\%$, $T_A = 0^\circ C$ to $75^\circ C$						
Propagation delay time						
t_{PLH} Low-to-high		True		25	40	ns
t_{PHL} High-to-low		Complement		29	48	
$t_{(min)}$		True		72	100	ns
		Complement		78	110	
t			3.08	3.42	3.76	μs

Load circuit and typical waveforms are shown at the front of section.

LOGIC