

Full Bridge Power Amplifier

FEATURES

- Dual Power Operational Amplifiers
- ±2A Output Current Guaranteed
- Precision Current Sense Amplifier
- Two Supply Monitoring Inputs
- Parking Function and Under-Voltage Lockout
- Safe Operating Area Protection
- 3V to 35V Operation

DESCRIPTION

The UC3176/7 family of full bridge power amplifiers is rated for a continuous output current of 2A. Intended for use in demanding servo applications such as disk head positioning, the onboard current sense amplifier can be used to obtain precision control of load current, or where voltage mode drive is required, a standard voltage feedback scheme can be used. Output stage protection includes foldback current limiting and thermal shutdown, resulting in a very rugged device.

Auxiliary functions on this device include a dual input under-voltage comparator that can be programmed to respond to low voltage conditions on two independent supplies. In response to an under-voltage condition the power Op-Amps are inhibited and a high current, 100mA, open collector drive output is activated. A separate Park/Inhibit command input.

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BLOCK DIAGRAM



ABSOLUTE MAXIMUM RATINGS (Note 1)

Input Supply voltage, (+VIN)
Park/Inhibit, UV1 and UV2 inputs (zener clamped)
Maximum forced voltage0.3V to 10V
Maximum forced current
Other Input Voltages
Alsink and Blsink Voltages
Open Collector Output Voltages
A and B Output Currents (Continuous)
Source Internally Limited
Sink
Total Supply Current (Continuous)
Parking Drive Output Current (Continuous) 200mA
Supply OK Output Current, UC3177 (Continuous) 30mA
Operating Junction Temperature55°C to +150°C
Power Dissipation at $TC = +75^{\circ}C$
QP package 4W
Storage Temperature
Note 1: Unless otherwise indicated, voltages are reference to
ground and currents are positive into, negative out of, the
specified terminals.
THERMAL DATA

QP package:

Thermal Resistance Junction to Leads, θ_{JL}	15°C/W
Thermal Resistance Junction to Ambient, θ_{JA}	50°C/W

CONNECTION DIAGRAM

	PACKAGE PIN FUNCTION			
PLCC-28 (Top View)	FUNCTION PIN			
QP Package	+VIN	1		
	B Output	2		
	BISINK(Sense)	3		
	BISINK	4		
	N/C	5-7		
4 3 2 1 28 27 26	B- Input	8		
25	*	9		
	Park/Inhibit	10		
	Parking Drive	11		
u/ 23	Gnd (Heat Flow Pins)	12-18		
8 22	UV1	19		
[9 21]	UV2	20		
10 20	Current Feedback	21		
11 19	A+ Input	22		
<u>12 13 14 15 16 17 18</u>	A- Input	23		
	N/C	24		
	Alsink	25		
	Alsınk(Sense)	26		
	A Output	27		
	Gnd	28		
	*Pin 9: UC3176, B+ Inp UC3177, Supply	ut ⁄ OK		

ELECTRICAL CHARACTERISTICS: Unless otherwise stated, specifications hold for TA = 0 to 70°C, +VIN = 12V, TA = TJ.

PARAMETER	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Input Supply					-
Supply Current	+VIN = 12V		18	25	mA
	+VIN = 35V		21	30	mA
UVOL Threshold	+VIN low to high		2.8	3.0	V
	Threshold Hysteresis		220	300	mV
Power, Amplifier, A and B					
Input Offset Voltage	VCM = 6V, VOUT = 6V			8	mV
Input Bias Current	Vсм = 6V, Except A+ Input	-500	-100		nA
Input Bias Current at A+/Reference Input	(A+/Ref - BISINK)/36kohms; TJ = 25°C	23	28	35	μA/V
Input Offset Current B Amp (UC3176 Only)	Vcm = 6V			200	nA
CMRR	VCM = 1 to 33V, +VIN =35V, VOUT = 6V	70	100		dB
PSRR	+VIN = 5 to 35V, VCM = 2.5V	70	100		dB
Large Signal Voltage Gain	Vout = 3V, w/iout = 1A to Vout = 9V, w/iout = -1A	1.5	4		V/mV
Thermal Feedback	+VIN = 20V, Pd = 20W at opposite output		25	200	μV/W
Saturation Voltage	loυτ = -2A, High Side, TJ = 25°		1.9		V
	CIOUT = 2A, Low Side, TJ = 25°C		1.6		V
	Total Vsat at 2A, TJ = 25°C		3.5	3.7	V
Unity Gain Bandwidth			1		MHz
Slew Rate			1		V/µs
Differential IOUT Sense Error Current	IOUT(A) = -IOUT(B), /IOUT/- /AISINK - BISINK/				
in Bridge Configuration	lout ≤200mA		3.0	6.0	mA
	Iout ≤ 2A		5.0	10	mA
High Side Current Limiting	=VIN - VOUT < 12V		-2.7	-2.0	Α

PARAMETER	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Current Sense Amplifier	•	_	-		•
Input Offset Voltage	VCM = 0V, A+/Ref at 6V			3	mV
	Ref = 2V to 20V, +VIN = 35, change with Ref				
	input voltage			600	μV/V
Thermal Gradient Sensitivity	+VIN = 20V, Ref = 10V Pd = 20W @ A or B				
	output		5.0	75	μV/W
PSRR	Ref = 2.5V, +VIN = 5 to 35V	70	100		dB
Gain	/Alsink - Blsink/ $\leq 0.5V$	7.8	8	8.1	V/V
Slew Rate			2		V/µS
3dB Bandwidth			1		MHz
Max Output Current	ISOURCE = $+VIN - VOUT = 0.5V$	2.5	3.5		mA
Output Saturation Voltage	ISOURCE = 1.5mA, High Side		0.15	0.30	V
	ISINK = 5mA, Low Side		1.4	1.7	V
Under-Voltage Comparator					
Threshold Voltage	Low to High, other input at 5V	1.44	1.50	1.56	V
	Threshold Hysteresis	50	70	80	mV
Input Current	Input = 2V, other input at 5V	-2	05		μΑ
Supply OK V _{SAT} (UC3177 Only)	IOUT = 5mA			0.45	V
Supply OK Leakage (UC3177 Only)	Vout = 35V			5	μΑ
Park/Inhibit				_	
Park/Inhibit Thl'd		1.1	1.3	1.7	V
Park/Inhibit Input Current	At threshold		60	100	μA
Parking Drive Saturation Voltage	I _{OUT} = 100mA		0.3	0.7	V
Parking Drive Leakage	$V_{OUT} = 35V$			15	μΑ
Thermal Shutdown					
Shutdown Temperature			165		°C

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UC3176 UC3177

APPLICATION AND OPERATION INFORMATION





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UC3176, FULL BRIDGE POWER AMPLIFIER

Device Status: Active

- > Description
- > Features
- > Datasheets
- > Pricing/Samples/Availability
- > Application Notes
- > Applications

Parameter Name	UC3176
Brushless DC Motor Control	No
DC Motor Control	No
Linear Power Amplifiers	Yes
Stepper Motor Controllers	No
Pin Count	28
Operating Supply (max) (V)	35
Operating Supply (min) (V)	3
Peak Output Current (A)	2.0
Operating Supply Current (mA)	18
Current Limit	Yes
Differential Current Sense Amplifier	Yes
Output Clamp Diodes	Yes
Thermal Shutdown	Yes
Tachometer	No
Linear Control	Yes
Switching Control	No

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To view the following documents, <u>Acrobat Reader 3.x</u> is required. To download a document to your hard drive, right-click on the link and choose 'Save'.

Datasheets

Full datasheet in Acrobat PDF: slus285.pdf (389 KB)

Pricing/Samples/Availability

Orderable Device	<u>Package</u>	<u>Pins</u>	<u>Temp (°C)</u>	<u>Status</u>	<u>Price/unit</u> <u>USD (100-999)</u>	<u>Pack Qty</u>	<u>Availability / Samples</u>
UC3176QP	<u>FN</u>	28	0 TO 70	ACTIVE	6.91	1	Check stock or order
UC3176QPTR	<u>FN</u>	28	0 TO 70	ACTIVE	6.07	1	Check stock or order

Application Reports

- ELECTROSTATIC DISCHARGE APPLICATION NOTE (SSYA008 Updated: 05/05/1999)
- THERMAL CHARACTERISTICS OF LINEAR AND LOGIC PACKAGES USING JEDEC PCB DESIGNS (SZZA017A - Updated: 09/10/1999)

Table Data Updated on: 8/11/2000

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