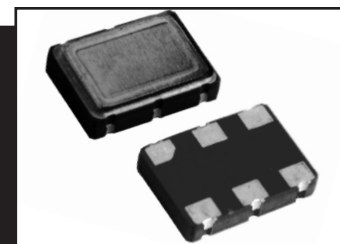


MINIATURE CERAMIC • HCMOS • SURFACE MOUNT • 5 Vdc or 3.3 Vdc
VOLTAGE-CONTROLLED CRYSTAL OSCILLATORS
ASLV and ASVV



FEATURES:

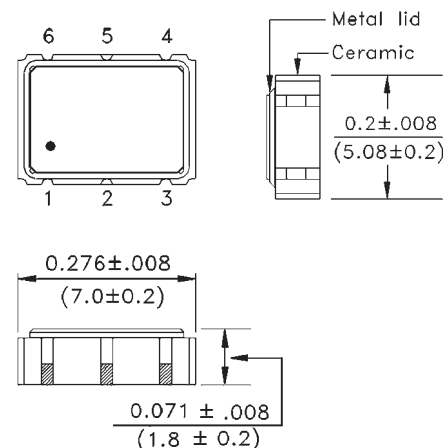
- Low Profile (1.8 mm) ceramic VCXO.
- Enable / Disable Options.
- 7.0 mm x 5.0 mm Metal Lid.
- 5 Vdc or 3 Vdc.
- Suitable for reflow methods.

APPLICATIONS:

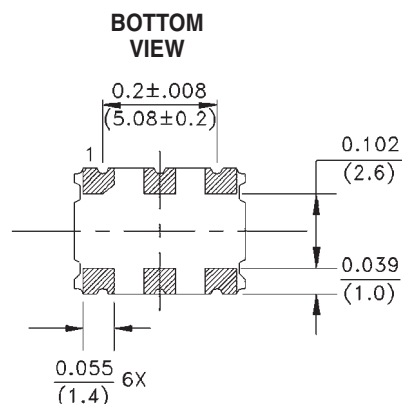
- Phase Locked Loops (PLLs)
- Synthesizers.
- Clock Recovery.

STANDARD SPECIFICATIONS

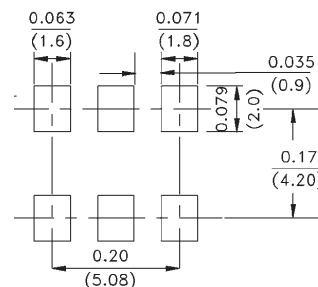
PARAMETERS	ASLV	ASVV
Frequency Range (F_0)	8.0 MHz - 36.0 MHz	
Operating Temperature (T_{OPR})	0°C to +70°C (See Options)	
Storage Temperature (T_{STO})	-55°C to +125°C	
Frequency Stability ($\Delta F / F_0$)	± 50 ppm max.	
Supply Voltage (V_{DD})	5 Vdc $\pm 5\%$	3.3 Vdc $\pm 5\%$
Input Current (I_{DD})	30 mA max.	20 mA max.
Duty Cycle or Symmetry	40 / 60% max. (See Options)	
Rise and Fall Times (T_R / T_F)	8 ns max.	
Output Load	15 pF or 2 TTL	
Output Voltage (V_{OH})	0.9 * V_{DD} min.	
(V_{OL})	0.4 Vdc max.	
Transfer Function	Positive	
Voltage Control (V_C)	2.5 Vdc ± 2.0 V	1.65 Vdc ± 1.5 V
Frequency Deviation	± 100 ppm min.	± 80 ppm min.
Linearity	$\pm 10\%$ max.	
Start-up Time (T_{OSC})	10 ms max.	
Tristate Function (V_{IH})	"1" or Open: Oscillation ≥ 2.2 V	
(Option "-A" ONLY) (V_{IL})	"0": (Hi Z) < 0.8 V Output Disabled in high impedance	
Output Disable / Enable Time	100 ns max. (for Option "- A" ONLY)	



Dimensions: Inches (mm)

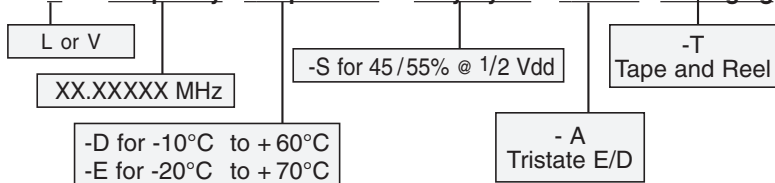


RECOMMENDED LAND PATTERN



ORDERING OPTIONS

AS X V - Frequency - Temperature - Duty Cycle - Tristate - Packaging



PIN NO.	FUNCTION
1	Voltage Control V_C
2	NC or Tristate E/D
3	GND / Case
4	Output
5	N / C
6	V_{DD}

ABRACON IS
 ISO 9001 / QS 9000
 CERTIFIED

NOTE: Left blank if standard • All specifications and markings subject to change without notice

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