



Intel® Ethernet Controller I350-AM2

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Essentials

Export specifications

Product Collection	Gigabit Ethernet Controllers (up to 2.5GbE)
Code Name	Products formerly Powerville
Status	Launched
Launch Date	Q2'11
Expected Discontinuance	1H'29
TDP	2.8 W
Operating Temperature Range	-10°C to 55°C
Operating Temperature (Maximum)	55 °C
Operating Temperature (Minimum)	-10 °C
Recommended Customer Price	\$26.25

Supplemental Information

Datasheet	View now
Product Brief	View now

Networking Specifications

Port Configuration	Dual
Data Rate Per Port	1GbE
System Interface Type	PCIe v2.0 (5.0 GT/s)
NC Sideband Interface	Yes

Technical Documentation

Connectivity

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Jumbo Frames Supported

Yes

Interfaces Supported

1000Base-T, SGMII, SERDES

Package Specifications

Package Size

17mm x 17mm

Intel® Virtualization Technology for Connectivity

On-chip QoS and Traffic Management

Yes

Virtual Machine Device Queues (VMDq) ?

Yes

PCI-SIG* SR-IOV Capable ?

Yes

Advanced Technologies

Intel® Virtualization Technology for Connectivity (VT-c)

Intel® VT-c

Fiber Channel over Ethernet ?

No

IEEE 1588 ?

Yes

iWARP/RDMA ?

No

Intel® Data Direct I/O Technology ?

Yes

More support options for Intel® Ethernet Controller I350-AM2



Product Support



Downloads and Software



Support Community



Warranty and Replacement



Need more help?

Contact support



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Refer to Datasheet for formal definitions of product properties and features.

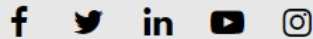
‡ This feature may not be available on all computing systems. Please check with the system vendor to determine if your system delivers this feature, or reference the system specifications (motherboard, processor, chipset, power supply, HDD, graphics controller, memory, BIOS, drivers, virtual machine monitor-VMM, platform software, and/or operating system) for feature compatibility. Functionality, performance, and other benefits of this feature may vary depending on system configuration.

"Announced" SKUs are not yet available. Please refer to the Launch Date for market availability.

System and Maximum TDP is based on worst case scenarios. Actual TDP may be lower if not all I/Os for chipsets are used.

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