





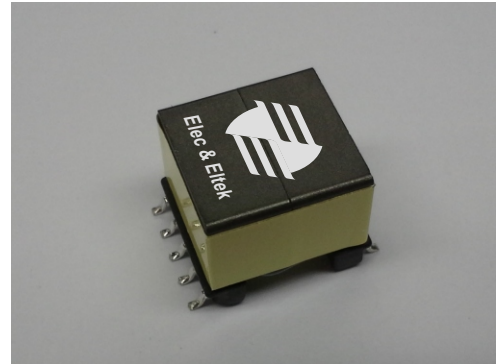


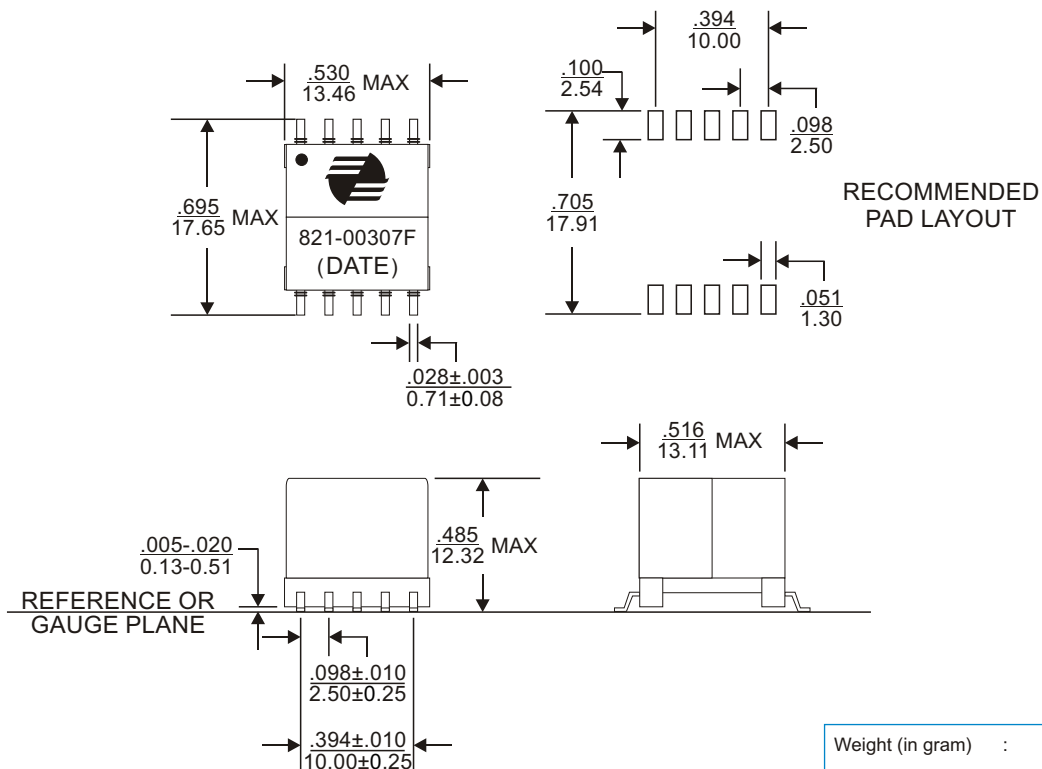
-  Designed to match with Texas Instruments TNET D4000 Chipsets in CO application
-  Available in SMD Mount packages
-  Excellent THD performance
-  Designed to meet UL1950 supplementary Insulation requirements for working voltages up to 250Vrms
-  Operating temperature 0°C to +85°C.
-  RoHS compliant



ELECTRICAL SPECIFICATION @ 25°C

Part Number	Turns Ratio (±2%) (10-7):(1-4)	OCL(1-4) @10KHz, 0.1Vrms (MH±10%)	Leakage Inductance @10KHz, 0.1V (uH Max) (1-4) with 10-7 shorted	DCR (Ohm Max)		Isolation Voltage (Vrms)	Interwinding Capacitance@10KHz, 0.1V(pF Max)
				(10-7)	(1-4)		
821-00307F	1:1.95	1.5	12.0	2.0	2.0	1500	40

MECHANICAL DIMENSIONS

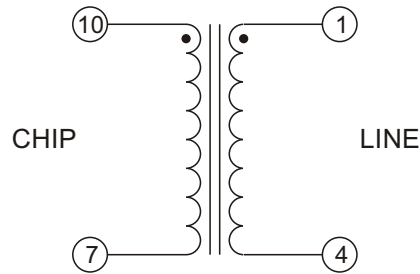


Notes:

1. All dimensions are specified in $\frac{\text{inches}}{\text{mm}}$ with higher precedence in inches.
2. Unless otherwise specified, all tolerances are $\pm \frac{.010}{0.25}$.



SCHEMATICS



FOR MORE INFORMATION, PLEASE CONTACT

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