



XC5A SERIES (HC/ACMOS/TTL), 5.0 VDC TRISTATE 5 x 7 mm, SMD



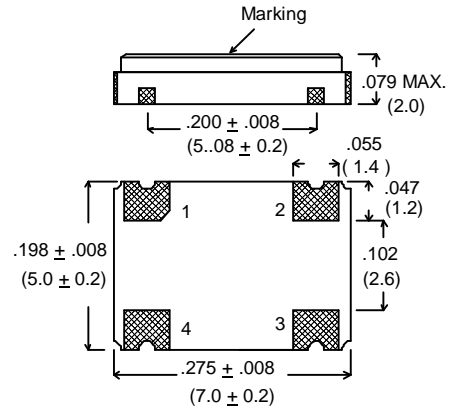
FEATURES:

- High Shock & Vibration
- Excellent Jitter Performance
- Military Screening Tests Available

APPLICATIONS::

- Optical Networking, SONET/SDH
- Broadband Data Transmission
- High Shock & Vibration Environments

Frequency Range	1.0 MHz to 105.0 MHz
Overall Frequency Accuracy (See Note Below)	See Options Below
Operating Temperature Range	See Options Below
Storage Temperature Range	-62 °C to +125 °C
Input Voltage	+ 5.0 VDC ± 10%
Input Current @ +5.0 VDC (No Load)	
1.0 MHz to 8.0 MHz	4 mA Max.
8.1 MHz to 32.0 MHz	10 mA Max.
32.1 MHz to 64.0 MHz	40 mA Max.
64.1 MHz to 105.0 MHz	60 mA Max.
Output	HCMOS/TTL Compatible
Output Load	10K // 15 pf or 8 TTL loads Max.
High Level	0.9 V _{DD} Min.
Low Level	0.1 V _{DD} Max.
Symmetry @ 50% Level	60/40% (55/45% Optional)
Rise & Fall Times (10% to 90% of Output)	
≤ 40 MHz	7 nS Max.
> 40 MHz	5 nS Max.
Enable / Disable Input Function	
Open or High (> 3.5V)	Normal Output
Low (< 0.8 V)	Output disabled into a HI-Z state
Start-Up Time	10 mS Max.
Phase Jtter (10 KHz to 20 MHz Integrated)	0.3 pS rms Typical
Aging @ +25 °C	± 0.0005% (± 5 PPM) / year Max.
Package – Seal	Ceramic, Hermetic, Resistance Welded
Pad Finish	0.3 to 1.0 µm gold over 1.27 to 8.9 µm nickel
Solder Reflow Temp/Time	260 °C Max for 10 Seconds Max.



Dimensions are in inches (mm)
All dimensions are typical unless otherwise specified

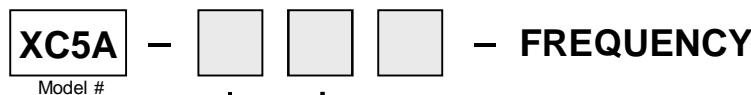
Pad #	Function
1	E/D (Optional)
2	GND
3	OUTPUT
4	V _{DD}

An External 0.01µF Bypass Capacitor is required between VDD and GND.

NOTE: Overall Frequency Accuracy Includes, Initial Accuracy at 25 °C, Frequency changes over Operating Temperature, Aging over 5 years, Frequency changes due to Supply Voltage & Load Variations.

Contact Xsis Engineering for any other special requirements.

ORDERING INFORMATION (Select from options below) :



Overall Frequency Accuracy Options

- | | |
|---------------|-------------------|
| 1 = ± 50 PPM | -10 °C to +70 °C |
| 2 = ± 25 PPM | -10 °C to +70 °C |
| 3 = ± 100 PPM | -40 °C to +85 °C |
| 4 = ± 50 PPM | -40 °C to +85 °C |
| 5 = ± 25 PPM | -40 °C to +85 °C |
| 6 = ± 100 PPM | -55 °C to +125 °C |
| 7 = ± 75 PPM | -55 °C to +125 °C |
| 8 = ± 20 PPM | -40 °C to +85 °C |
| 9 = ± 50 PPM | -55 °C to +105 °C |

Symmetry & E/D Options

- | |
|---------------------|
| A = 60/40% & E/D |
| B = 55/45% & E/D |
| D = 60/40% & No E/D |
| E = 55/45% & No E/D |

Screening Options

- | |
|-------------------------------------|
| X = No Screening |
| M = 100% Screening |
| MP = Enhanced Reliability Screening |

Example: XC5A - 3AM - 24.000 MHz = HC/ACMOS/TTL, 5 VDC, Tristate Output, 60/40% Symmetry ± 100 PPM Overall Frequency Accuracy Over -40°C to +85 °C, 100% Screened

Rev 12/13