TOKEN RING RETIMING MODULES

Token Ring Active Retimed Concentrators

- Clock extraction and data retiming
- Supports 4 Mbps and 16 Mbps data rates
- Supports UTP/STP applications
- Advanced CMOS Technology
- Compliant with IEEE 802.5

Electrical Specifications @ 25°C

Part Number	Description	Package
PE-67530	Clock Extraction/Data Retiming	TH

Description

The PE-67530 Module is designed to provide extraction and data re-timing functions for an active concentrator node. A complete concentrator node can be formed by using PE-67530, a filter module, an equalizer, a phantom current detect circuit, a high speed comparator, and two common mode chokes.

All necessary circuitry is included for operation at both 4 Mbps and 16 Mbps data rates

Small size and low power consumption is achieved by implementing most of PE-67530's active circuitry in a custom CMOS ASIC designed by Pulse.

Phase lock technology is employed to extract embedded clock information from the data stream. When compared with re-timers based on LC tank circuits, a PLL guarantees start-up lock without the need for external frequency acquisition circuitry.

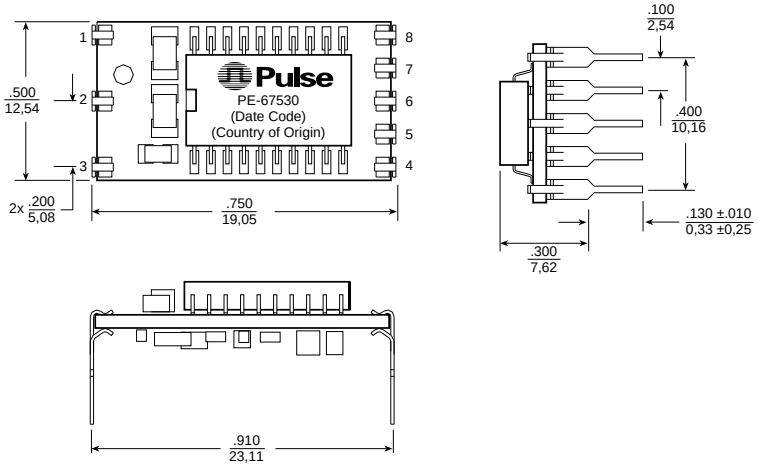
The PE-67530 contains circuitry for detecting the rate/speed of the input data. By jumpering the speed detect (SPEED) output to the speed select (SPDSEL) input, PE-67530 automatically acquires and operates at both 4 Mbps and 16 Mbps data rates.

Mechanical

Weight . . . . . .1.8 grams
Tube 40/tube

Dimensions: Inches / mm

Unless otherwise specified,all tolerances are ± .010 / 0,25



Block Diagram