HARRIS SEMICONDUCTOR

HI20201, CX20201-1, CX20202-1

10-Bit, 160 MSPS Ultra High-Speed D/A Converter

January 1997

Features

- 160 MSPS Throughput Rate
- 10-Bit (HI20201, CX20201-1, CX20202-1) Resolution
- 0.5 LSB Differential Linearity Error
- Low Glitch Noise
- Analog Multiplying Function
- Low Power Consumption 420mW
- Evaluation Board Available

Applications

- Wireless Communications
- Signal Reconstruction
- Direct Digital Synthesis
- High Definition Video Systems
- Digital Measurement Systems
- Radar

Description

The HI20201, CX20201-1, CX20202-1 is a 160MHz ultra high speed D/A converter. The converter is based on an R/2R switched current source architecture that includes an input data register with a complement feature and is Emitter Coupled Logic (ECL) compatible.

The HI20201, CX20201-1, CX20202-1 is a 10-bit accurate D/A with a linearity error of 1 LSB.

The HI20201, CX20201-1, CX20202-1 is available in a commercial temperature range and are offered in a 28 lead plastic SOIC (300 mil) and a 28 lead plastic DIP package.

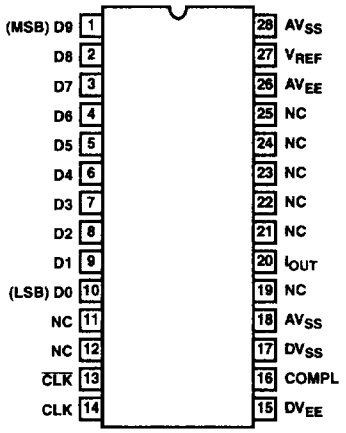
Ordering Information

PART NUMBER	TEMP. RANGE (^{o}C)	PACKAGE	PKG. NO.
HI20201JCB, CX20201A-1	-20 to 75	28 Ld SOIC	M28.3A-S
HI20201JCP, CX20202A-1	-20 to 75	28 Ld PDIP	E28.6A-S
HI20201-EV	25	Evaluation Kit	

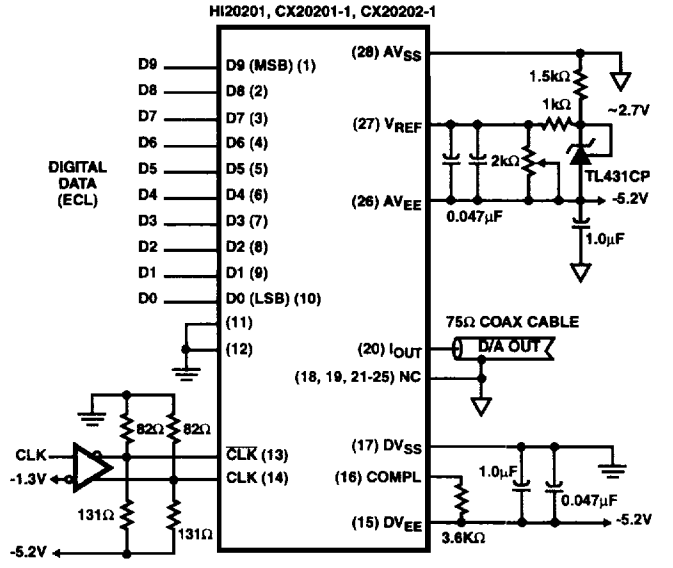
5 STANDARD PRODUCTS

Pinout

HI20201, CX20201-1, CX20202-1
(PDIP, SOIC)
TOP VIEW



Typical Applications Circuit



CAUTION: These devices are sensitive to electrostatic discharge. Users should follow proper IC Handling Procedures.

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