



# 5STR 04T2032

Old part no. TP 907FC-370-20

## Reverse Conducting Thyristor

### Properties

- § Integrated freewheeling diode
- § Optimized for low dynamic losses

### Applications

- § Traction

### Key Parameters

$V_{DRM}$	= 2 000	V
$I_{TAVm}$	= 374	A
$I_{TSM}$	= 5 000	A
$V_{TO}$	= 1.748	V
$r_T$	= 0.653	m $\Omega$
$t_q$	= 32	$\mu$ s

### Types

	$V_{DRM}$
5STR 04T2032..2040	2 000 V
5STR 04T1832..1840	1 800 V

Conditions:  
 $T_j = -40 \div 125$  °C, half sine waveform,  
 $f = 50$  Hz

### Mechanical Data

$F_m$	Mounting force	10 $\pm$ 2 kN
$m$	Weight	0.20 kg
$D_s$	Surface creepage distance	13 mm
$D_a$	Air strike distance	8 mm

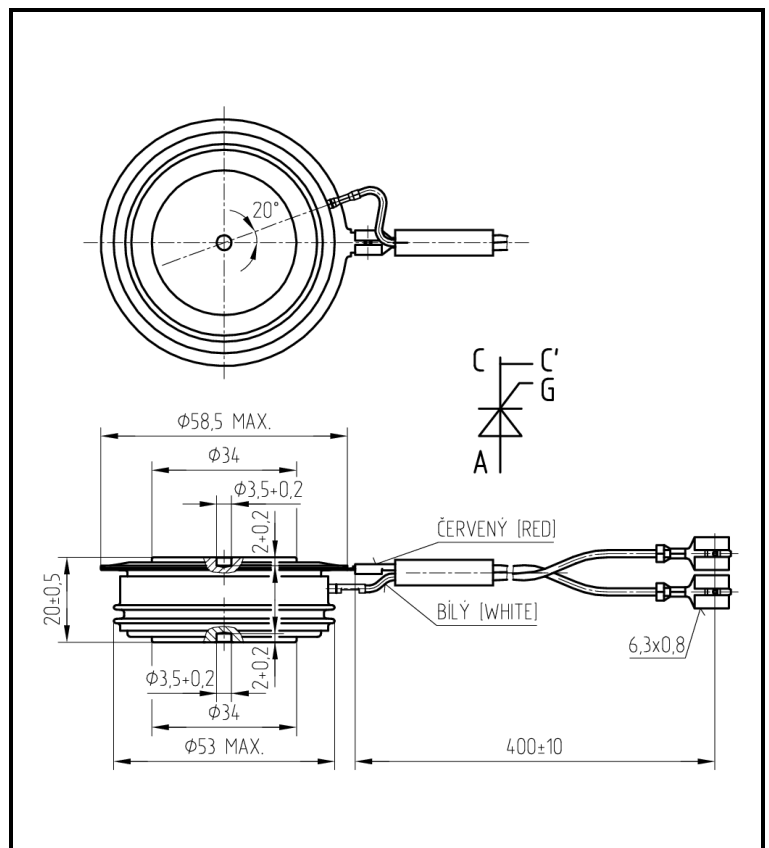


Fig. 1 Case



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<b>Maximum Ratings - Thyristor</b>			<b>Maximum Limits</b>	<b>Unit</b>
$V_{DRM}$	Repetitive peak off-state voltage $T_j = -40 \div 125 \text{ }^\circ\text{C}$	5STR 04T2032..2040 5STR 04T1832..1840	2 000 1 800	V
$I_{TRMS}$	RMS on-state current $T_c = 70 \text{ }^\circ\text{C}$ , half sine waveform, $f = 50 \text{ Hz}$		588	A
$I_{TAVm}$	Average on-state current $T_c = 70 \text{ }^\circ\text{C}$ , half sine waveform, $f = 50 \text{ Hz}$		374	A
$I_{TSM}$	Peak non-repetitive surge half sine pulse, $V_R = 0 \text{ V}$	$t_p = 10 \text{ ms}$ $t_p = 8.3 \text{ ms}$	5 000 5 300	A
$I^2t$	Limiting load integral half sine pulse, $V_R = 0 \text{ V}$	$t_p = 10 \text{ ms}$ $t_p = 8.3 \text{ ms}$	125 000 118 000	A <sup>2</sup> s
$(di_T/dt)_{cr}$	Critical rate of rise of on-state current $I_T = 1\,000 \text{ A}$ , $V_D = 0.67 V_{DRM}$ , half sine waveform, $f = 50 \text{ Hz}$		400	A/ms
$(dv_D/dt)_{cr}$	Critical rate of rise of off-state voltage $V_D = 0.67 V_{DRM}$		1 000	V/ms
$P_{AV}$	Maximum average gate power losses		5	W
$I_{GTM}$	Peak gate current		25	A
$V_{GTM}$	Peak gate voltage		15	V
$V_{RGTM}$	Reverse peak gate voltage		2	V
$T_{jmin} - T_{jmax}$	Operating temperature range		-40 $\div$ 125	$^\circ\text{C}$
$T_{stgmin} - T_{stgmax}$	Storage temperature range		-40 $\div$ 125	$^\circ\text{C}$

Unless otherwise specified  $T_j = 125 \text{ }^\circ\text{C}$

<b>Maximum Ratings - Diode</b>			<b>Maximum Limits</b>	<b>Unit</b>
$V_{RRM}$	Repetitive peak reverse voltage $T_j = -40 \div 125 \text{ }^\circ\text{C}$	5STR 04T2032..2040 5STR 04T1832..1840	2 000 1 800	V
$I_{FRMS}$	RMS forward current $T_c = 70 \text{ }^\circ\text{C}$ , half sine waveform, $f = 50 \text{ Hz}$		453	A
$I_{FAVm}$	Average forward current $T_c = 70 \text{ }^\circ\text{C}$ , half sine waveform, $f = 50 \text{ Hz}$		288	A
$I_{FSM}$	Peak non-repetitive surge half sine pulse, $V_R = 0 \text{ V}$	$t_p = 10 \text{ ms}$ $t_p = 8.3 \text{ ms}$	3 500 3 800	A
$I^2t$	Limiting load integral half sine pulse, $V_R = 0 \text{ V}$	$t_p = 10 \text{ ms}$ $t_p = 8.3 \text{ ms}$	61 000 58 000	A <sup>2</sup> s

Unless otherwise specified  $T_j = 125 \text{ }^\circ\text{C}$

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<b>Characteristics – Thyristor</b>		<b>Value</b>			<b>Unit</b>
		<i>min.</i>	<i>typ.</i>	<i>max.</i>	
$V_{TM}$	<b>Maximum peak on-state voltage</b> $I_{TM} = 1\ 000\ A$			<b>2.420</b>	<b>V</b>
$V_{T0}$	<b>Threshold voltage</b>			<b>1.748</b>	<b>V</b>
$r_T$	<b>Slope resistance</b> $I_{T1} = 581\ A, I_{T2} = 1\ 744\ A$			<b>0.653</b>	<b>mW</b>
$I_{DM}$	<b>Peak off-state current</b> $V_D = V_{DRM}$			<b>70</b>	<b>mA</b>
$t_{gd}$	<b>Delay time</b> $T_j = 25\ ^\circ C, V_D = 100\ V, I_{TM} = I_{TAVm}, t_r = 0.5\ \mu s, I_{GT} = 2\ A$			<b>1</b>	<b>ms</b>
$t_{gt}$	<b>Switch-on time</b> <i>the same conditions as at <math>t_{gd}</math></i>			<b>4</b>	<b>ms</b>
$t_q$	<b>Turn-off time</b> $I_T = I_{TAVm}, di_T/dt = -50\ A/ms,$ $V_D = 0.67\ V_{DRM}, dv_D/dt = 50\ V/ms$	<b>group of <math>t_q</math></b>		<b>32</b>	<b>ms</b>
		<b>5STR 04T2032</b> <b>5STR 04T1832</b>			
		<b>5STR 04T2040</b> <b>5STR 04T1840</b>		<b>40</b>	
$I_H$	<b>Holding current</b>	$T_j = 25\ ^\circ C$ $T_j = 125\ ^\circ C$		<b>500</b>	<b>mA</b>
$I_L$	<b>Latching current</b>	$T_j = 25\ ^\circ C$ $T_j = 125\ ^\circ C$		<b>5 000</b>	<b>mA</b>
$V_{GT}$	<b>Gate trigger voltage</b> $V_D = 12\ V, I_T = 4\ A$	$T_j = -40\ ^\circ C$ $T_j = +25\ ^\circ C$ $T_j = +125\ ^\circ C$	<b>0.25</b>	<b>4.5</b> <b>2.5</b> <b>2.0</b>	<b>V</b>
$I_{GT}$	<b>Gate trigger current</b> $V_D = 12\ V, I_T = 4\ A$	$T_j = -40\ ^\circ C$ $T_j = +25\ ^\circ C$ $T_j = +125\ ^\circ C$	<b>10</b>	<b>1000</b> <b>400</b> <b>250</b>	<b>mA</b>

Unless otherwise specified  $T_j = 125\ ^\circ C$

<b>Characteristics – Diode</b>		<b>Value</b>			<b>Unit</b>
		<i>min.</i>	<i>typ.</i>	<i>max.</i>	
$V_{FM}$	<b>Maximum forward voltage</b> $I_{FM} = 1\,000\text{ A}$			<b>2.170</b>	<b>V</b>
$V_{T0}$	<b>Threshold voltage</b> $I_{F1} = 454\text{ A}, I_{F2} = 1\,362\text{ A}$			<b>1.419</b>	<b>V</b>
$r_T$	<b>Forward slope resistance</b>			<b>0.714</b>	<b>mW</b>
$Q_{rr}$	<b>Reverse recovery charge</b> $I_{FM} = I_{TAVm}, di_F/dt = -50\text{ A/ms}, V_D = 100\text{ V}$		<b>250</b>		<b>mC</b>
$I_{rrM}$	<b>Maximum reverse recovery current</b> <i>the same conditions as at <math>Q_{rr}</math></i>		<b>150</b>		<b>A</b>
$t_{rr}$	<b>Reverse recovery time</b> <i>the same conditions as at <math>Q_{rr}</math></i>		<b>4</b>		<b>μs</b>

Unless otherwise specified  $T_j = 125\text{ °C}$

<b>Thermal Parameters - Thyristor</b>		<b>Value</b>	<b>Unit</b>
$R_{thjc}$	<b>Thermal resistance junction to case</b> <i>double side cooling</i>	<b>55</b>	<b>K/kW</b>
	<i>anode side cooling</i>	<b>91</b>	
	<i>cathode side cooling</i>	<b>140</b>	
$R_{thch}$	<b>Thermal resistance case to heatsink</b> <i>double side cooling</i>	<b>10</b>	<b>K/kW</b>
	<i>single side cooling</i>	<b>20</b>	

<b>Thermal Parameters - Diode</b>		<b>Value</b>	<b>Unit</b>
$R_{thjc}$	<b>Thermal resistance junction to case</b> <i>double side cooling</i>	<b>88</b>	<b>K/kW</b>
	<i>anode side cooling</i>	<b>190</b>	
	<i>cathode side cooling</i>	<b>165</b>	

### Transient Thermal Impedance - Thyristor

**Correction for periodic waveforms - Thyristor**

180° sine:	add 7.4 K/kW
180° rectangular:	add 8.4 K/kW
120° rectangular:	add 13.8 K/kW
60° rectangular:	add 23.8 K/kW

**Analytical function for transient thermal impedance**

$$Z_{thjc} = \sum_{i=1}^5 R_i (1 - \exp(-t/t_i))$$

Conditions:  
 $F_m = 10 \pm 2$  kN, Double side cooled

$i$	1	2	3	4	5
$t_i$ (s)	1.62	0.111	0.0236	0.00322	0.307e-3
$R_i$ (K/kW)	3.77	36.70	9.64	3.54	1.38

*Fig. 2 Dependence transient thermal impedance junction to case on square pulse - Thyristor*

### Diode

**Correction for periodic waveforms - Diode**

180° sine:	add 10.7 K/kW
180° rectangular:	add 11.1 K/kW
120° rectangular:	add 18.2 K/kW
60° rectangular:	add 31.9 K/kW

$i$	1	2	3	4	5
$t_i$ (s)	0.401	0.108	0.0267	0.0034	0.584e-3
$R_i$ (K/kW)	23.00	41.00	17.20	3.47	2.50

*Fig. 3 Dependence transient thermal impedance junction to case on square pulse - Diode*

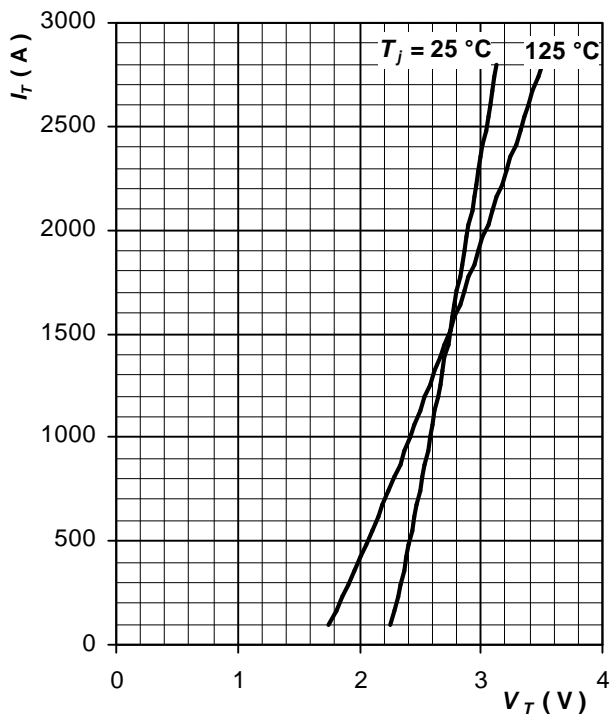


Fig. 4 Maximum on-state characteristics

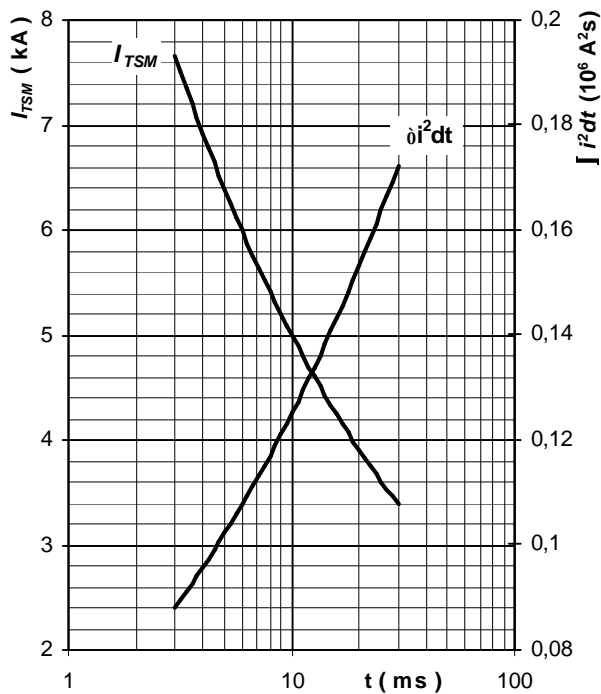


Fig. 5 Surge on-state current vs. pulse length, half sine wave, single pulse,  $V_R = 0 \text{ V}$ ,  $T_j = T_{jmax}$

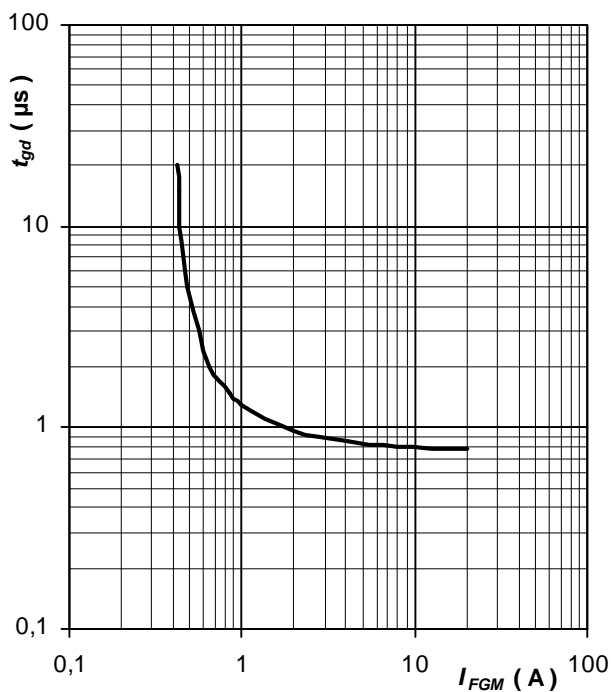


Fig. 6 Delay time vs. forward gate current,  $T_j = 25\text{ °C}$ ,  $V_D = 100 \text{ V}$ ,  $I_{TM} = I_{TAVm}$ ,  $t_r \leq 0.5 \mu\text{s}$ ,  $t_p = 1 \text{ ms}$

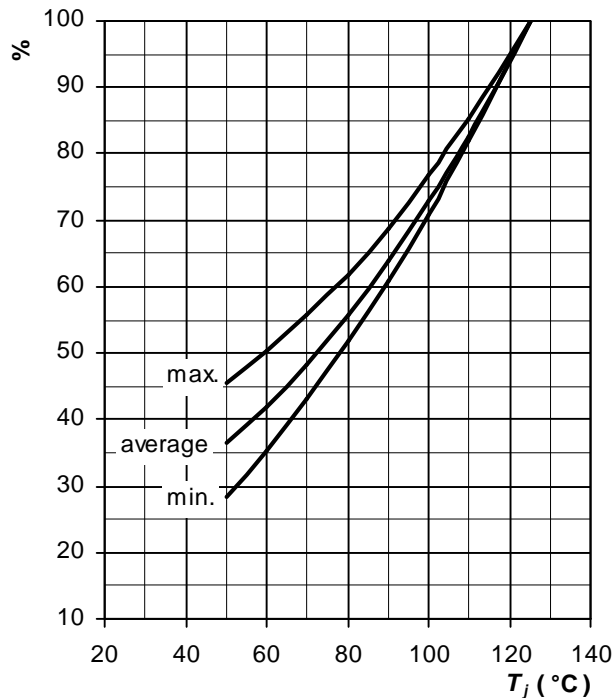


Fig. 7 Relative value of turn-off time vs. junction temperature

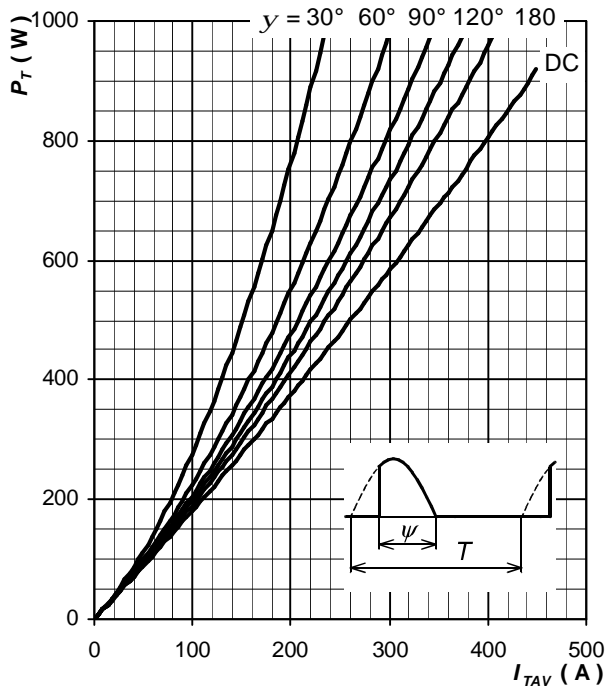


Fig. 8 On-state power loss vs. average on-state current, sine waveform,  $f = 50 \text{ Hz}$ ,  $T = 1/f$

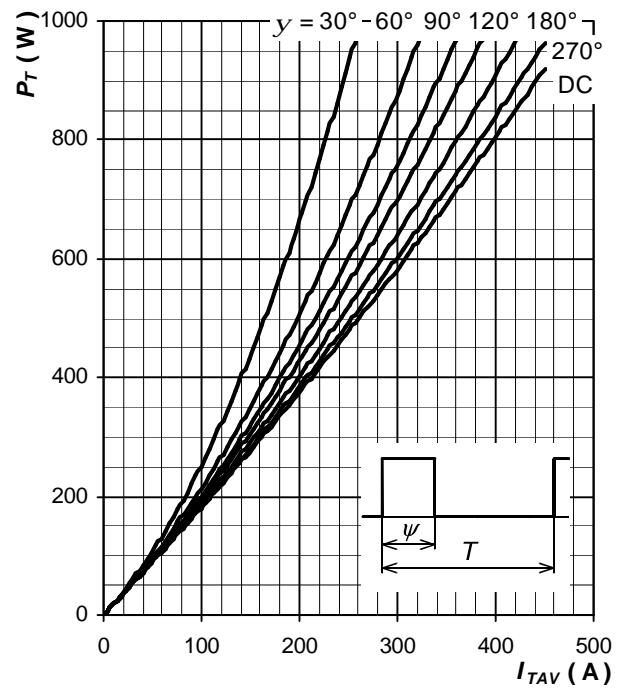


Fig. 9 On-state power loss vs. average on-state current, square waveform,  $f = 50 \text{ Hz}$ ,  $T = 1/f$

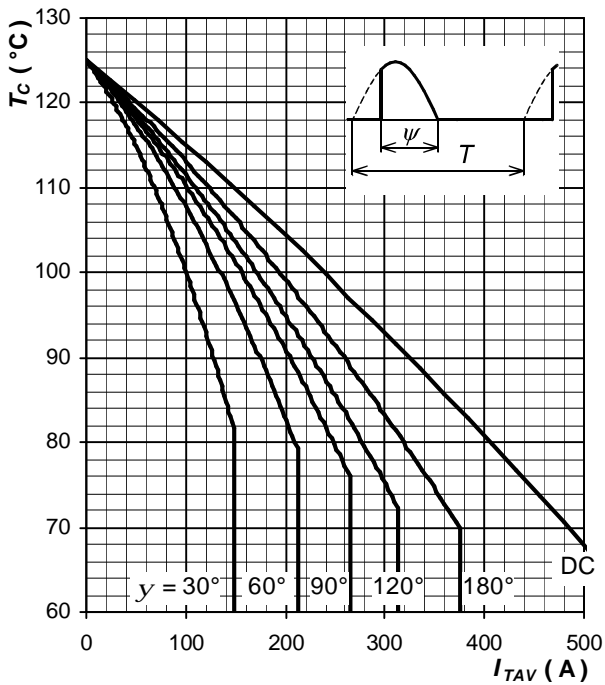


Fig. 10 Max. case temperature vs. aver. on-state current, sine waveform,  $f = 50 \text{ Hz}$ ,  $T = 1/f$

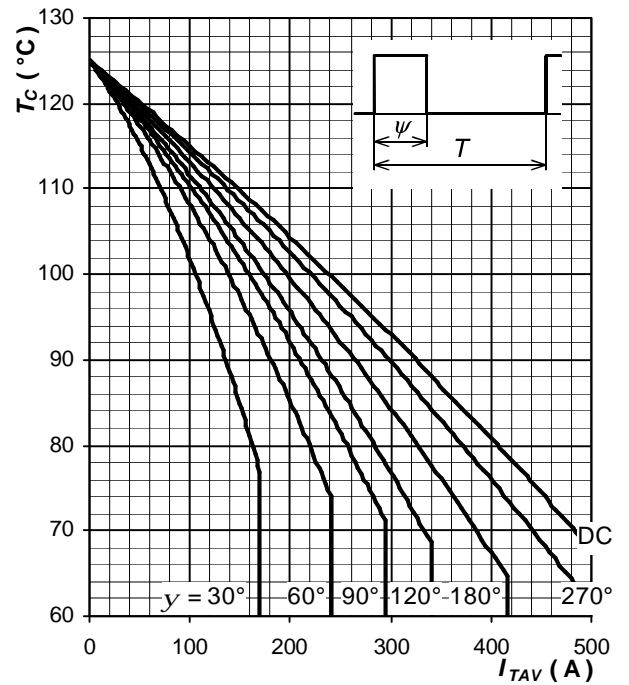


Fig. 11 Max. case temperature vs. aver. on-state current, square waveform,  $f = 50 \text{ Hz}$ ,  $T = 1/f$

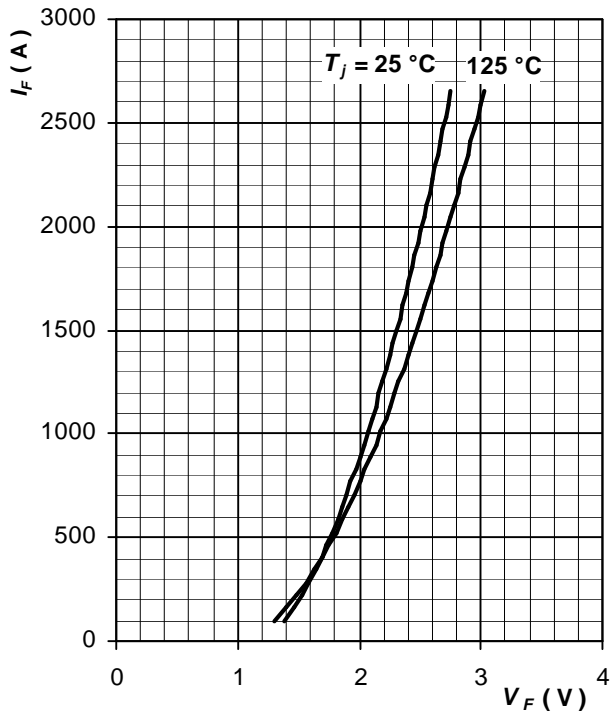


Fig. 12 Maximum forward voltage drop characteristics of the diode

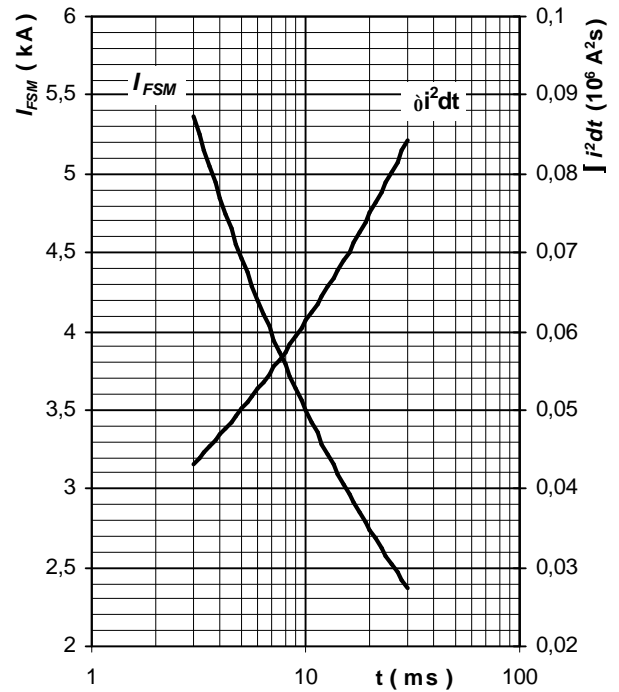


Fig. 13 Surge on-state current vs. pulse length of the diode. Half sine wave, single pulse,  $V_R = 0 \text{ V}$ ,  $T_j = T_{jmax}$

Notes: