

HD29412

Dual Differential Line Drivers With 3 State Output

HITACHI

ADE-205-581 (Z)
1st. Edition
Dec. 2000

Description

The HD29412 features dual differential line drivers with three state outputs designed to satisfy the requirements of EIA-422. Each driver has an output control is low, the associated outputs are in a high impedance state. This permits many devices to be connected together on the same transmission line for party line applications.

Function Table

Input				Output	
A	B	C	D	Y	Z
H	H	H	H	H	L
H	L	H	H	L	H
L	H	H	H	L	H
L	L	H	H	L	H
X	X	L	X	Z	Z
X	X	X	L	Z	Z

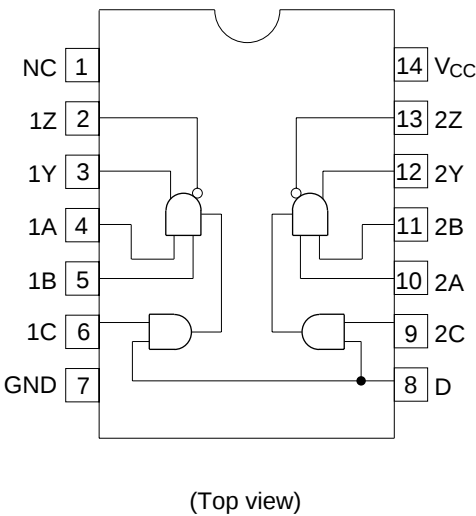
H : High level

L : Low level

X : Irrelevant

Z : High impedance

Pin Arrangement



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit
Supply Voltage	V_{CC}^{*1}	7	V
Input Voltage	V_{IN}	5.5	V
Power Dissipation	P_T	1150	mW
Operating Temperature	T_{opr}	0 to + 70	°C
Storage Temperature	T_{stg}	-65 to + 150	°C

- Notes: 1. The values is defined as of ground terminal.
2. The absolute maximum ratings are values which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

Recommended Operating Conditions

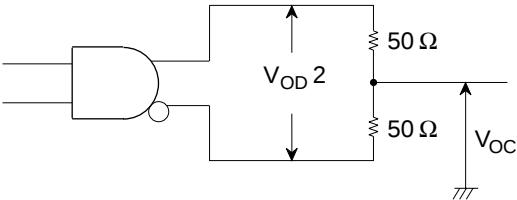
Item	Symbol	Min	Typ	Max	Unit
Supply Voltage	V_{CC}	4.75	5.00	5.25	V
Output Current	I_{OH}	—	—	−40	mA
	I_{OL}	—	—	40	mA
Operating Temperature	T_{opr}	0	—	70	°C

Electrical Characteristics (Ta = 0 to +70°C)

Item	Symbol	Min	Typ*1	Max	Unit	Conditions
Input Voltage	V _{IH}	2	—		V	
	V _{IL}	—	—	0.8	V	
Input Clamp Voltage	V _{IK}	—	—	−1.5	V	V _{CC} = 4.75 V, I _I = −12 mA
Output Voltage	V _{OH}	2.5	—	—	V	V _{CC} = 4.75 V, V _{IL} = 0.8 V V _{IH} = 2 V, I _{OH} = −40 mA
	V _{OL}	—	—	0.5	V	V _{CC} = 4.75 V, V _{IL} = 0.8 V V _{IH} = 2 V, I _{OL} = 40 mA
Output Clamp Voltage	V _{OK}	—	—	−1.5	V	V _{CC} = 5.25 V, I _O = −40 mA
Differential Output Voltage	V _{OD1}	—	—	2 V _{OD2}	V	V _{CC} = 5.25 V, I _O = 0 mA
	V _{OD2}	2	—	—	V	V _{CC} = 4.75 V R _L = 100 Ω
Change In Magitude of Differential Output Voltage*1	V _{OD}	—	—	0.4	V	V _{CC} = 4.75 V
Common Mode Output Voltage*3	V _{OC}	—	—	3	V	V _{CC} = 5.25 V
		—	3	—	V	V _{CC} = 4.75 V
Magitude of Common Mode Output Voltage*2	Δ V _{OC}	—	—	0.4	V	V _{CC} = 4.75 V or 5.25 V
Output Current With Power Off	I _O	—	—	100	μA	V _{CC} = 0 V _O = 6 V
		—	—	−100		V _O = −0.25 V
		—	—	±100		V _O = −0.25 V to 6 V
Off State (HighImpedance) Output Current	I _{OZ}	—	—	±10	μA	V _{CC} = 5.25 V Ta = 25°C, V _O = 0 to V _{CC}
		—	—	−20		Output Ta = 70°C V _O = 0 V
		—	—	±20		Control V _O = 0.4 V
		—	—	±20		= 0.8 V V _O = 2.4 V
		—	—	20		V _O = V _{CC}
Input Current (A, B, C Input)	I _I	—	—	1	mA	V _{CC} = 5.25 V, V _I = 5.5 V
	I _{IH}	—	—	40	μA	V _{CC} = 5.25 V, V _I = 2.4 V
	I _{IL}	—	—	−1.6	mA	V _{CC} = 5.25 V, V _I = 0.4 V
Input Current (D Input)	I _I	—	—	2	mA	V _{CC} = 5.25 V, V _I = 5.5 V
	I _{IH}	—	—	80	μA	V _{CC} = 5.25 V, V _I = 2.4 V
	I _{IL}	—	—	−1.8	mA	V _{CC} = 5.25 V, V _I = 0.4 V
Short Circuit Output*4	I _{OS}	−40	—	−150	mA	V _{CC} = 5.25 V
Supply Current (All Input GND)	I _{CC}	—	31	65	mA	V _{CC} = 5.25 V, Ta = 25°C

Notes: 1. All typical values are at V_{CC} = 5.0 V, Ta = 25°C2. Δ|V_{OD}| and Δ|V_{OC}| denote the change of V_{OD} and V_{OC} in absolute values, respectively. Voltage generated when input level is changed from high to low.

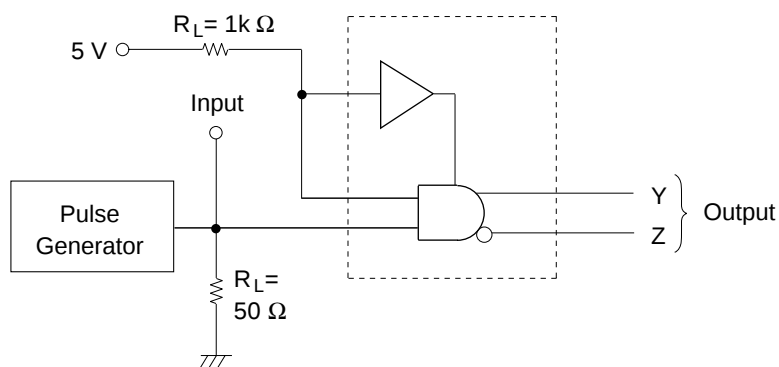
- 3. Voc is the average of two output voltages referenced to GND, and is identical to output offset voltage (V_{os}) in EIA standard RS-422.
- 4. Not more than one output should be shorted at a time and duration of the short circuit should not exceed one second.



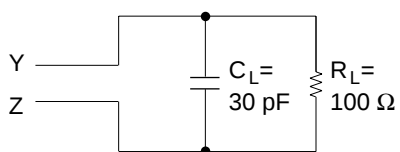
Switching Characteristics ($V_{cc} = 5\text{ V}$, $T_a = 25^{\circ}\text{C}$)

Item	Symbol	Test Circuit	Min	Typ	Max	Unit	Conditions
Propagation Delay Time	t_{PLH}	1 Connection A	—	16	25	ns	$C_L = 30\text{ pF}$, $R_L = 100\text{ }\Omega$
	t_{PHL}		11	—	20		
	t_{PLH}	1 Connection B	—	13	20		$C_L = 15\text{ pF}$
	t_{PHL}		9	—	15		
Transition Time	t_{TLH}	1 Connection A	—	4	20	ns	$C_L = 30\text{ pF}$, $R_L = 100\text{ }\Omega$
	t_{THL}		4	—	20		
Output Enable Time	t_{ZH}	2	—	7	20	ns	$C_L = 30\text{ pF}$, $R_L = 180\text{ }\Omega$
	t_{ZL}	3	—	14	40		$C_L = 30\text{ pF}$, $R_L = 250\text{ }\Omega$
Output Disable Time	t_{HZ}	2	—	10	30	ns	$C_L = 30\text{ pF}$, $R_L = 180\text{ }\Omega$
	t_{LZ}	3	—	17	35		$C_L = 30\text{ pF}$, $R_L = 250\text{ }\Omega$
Over Shoot Coefficient		2, Connection C	—	—	10	%	$R_L = 100\text{ }\Omega$

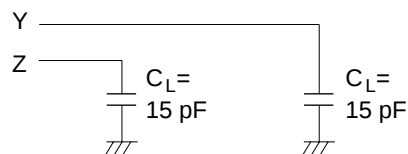
1. Switching Time Test Circuit



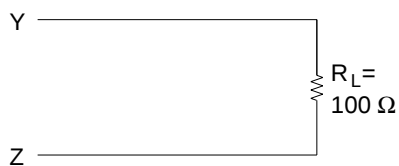
Connection A

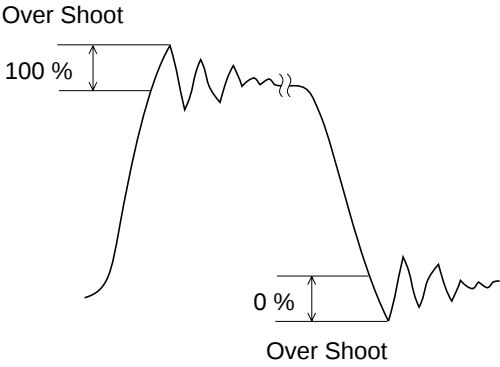
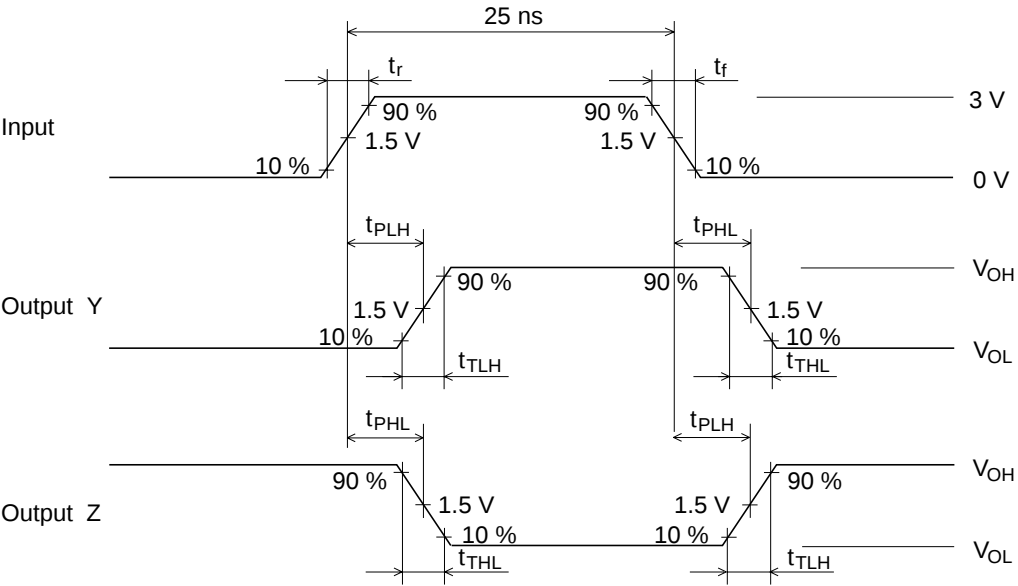


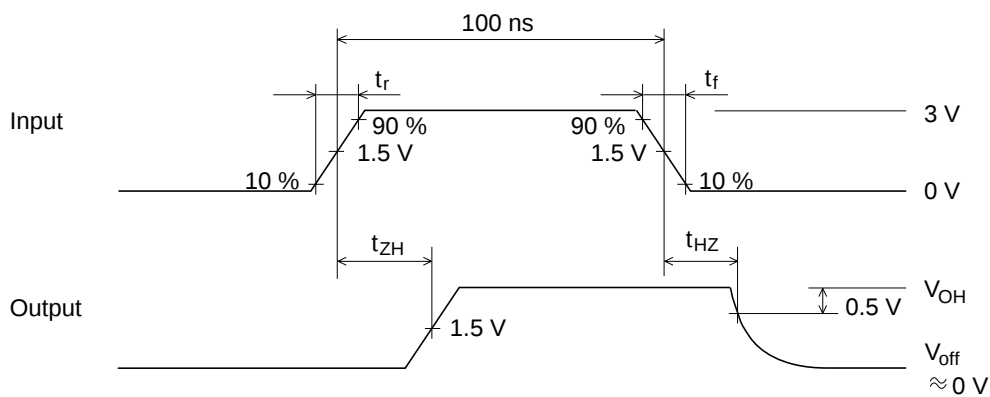
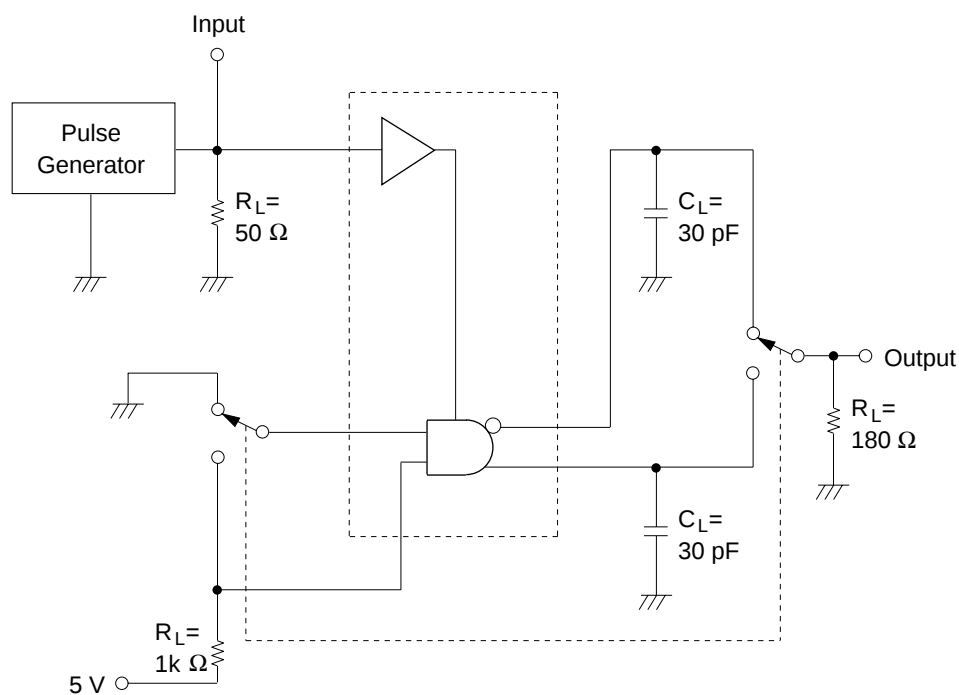
Connection B



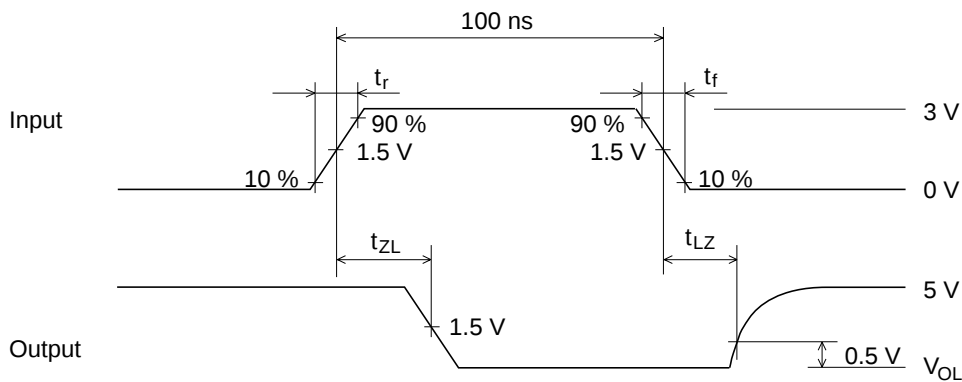
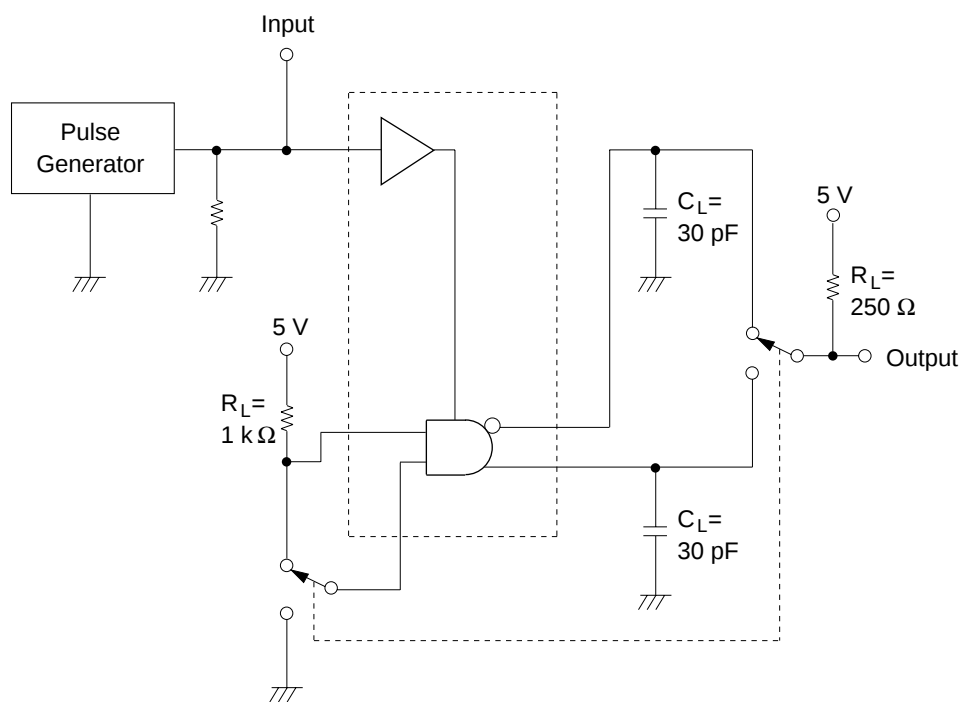
Connection C





2. t_{ZH} , t_{HZ} 

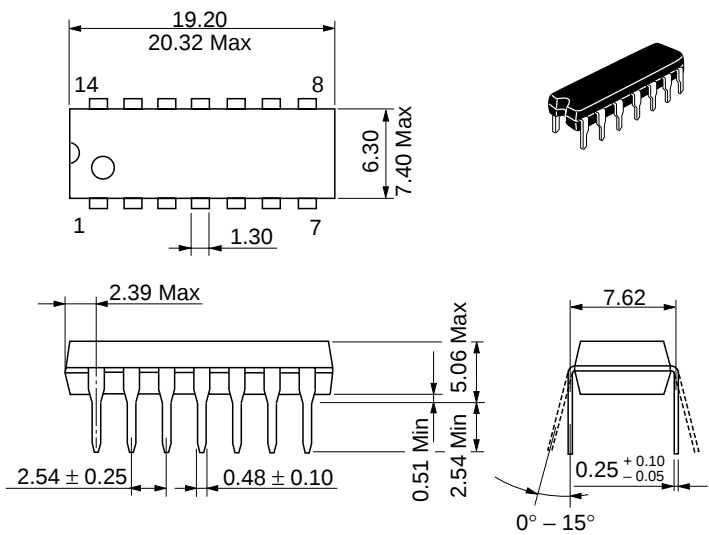
3. t_{ZL} , t_{LZ}



- Notes:
1. The pulse generator has the following characteristics: PRR = 500 KHz, $Z_{OUT} = 50 \Omega$.
 2. C_L includes probe and jig capacitance.

Package Dimensions

Unit: mm



Hitachi Code	DP-14
JEDEC	Conforms
EIAJ	Conforms
Mass (reference value)	0.97 g

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