

PELP Series

5X7X1.6mm / PECL SMD Oscillator (Low Phase Jitter)

Lead-Free
RoHS Compliant

CALIBER
Electronics Inc.

Features

- Tight Symmetry (45-55%)
- Complimentary Output

Typical Applications

- 10G-Bit, Ethernet, MAN, SONET
- WLAN/WiMax, xDSL and Fiber Channel

PART NUMBERING GUIDE

Environmental/Mechanical Specifications on page F5

PESM 00 HM 33 R T - 155.520MHz	
Package PELP = 5X7X1.6mm, PECL Oscillator, High Frequency	Pin One Connection T = Tri State Enable High N = No Connect
Inclusive Stability 00 = 100ppm 50 = 50ppm	Output Symmetry R = 40/60% S = 45/55%
Operating Temperature Range HM = 0°C to 70°C GM = -10° to 70°C FM = -20° to 70°C CQ = -40°C to 85°C (50ppm ONLY)	Voltage 18 = 1.8V 25 = 2.5V

ELECTRICAL SPECIFICATIONS

Revision: 2005-B

Frequency Range	150.000MHz to 160.000MHz
Frequency STANDARD	155.520MHz / 156.250MHz
Storage Temperature Range	-55°C to 125°C
Supply Voltage	2.5Vdc, 3.3Vdc
Supply Current	75mA Maximum
Frequency Tolerance / Stability	Inclusive of Operating Temperature Range, Supply Voltage and Load ±100ppm, ±50ppm
Rise / Fall Time	1.0 nSec Maximum (20% to 80% of Amplitude)
Duty Cycle	50 ±10% (Standard) 50±5% (Optional)
Load Drive Capability	50 ohms (Vcc to +2.0V)
Aging (@ 25°C)	±3ppm Maximum
Start Up Time	3mSeconds Maximum
RMS Clock Jitter (Integrated 12kHz-20MHz) / 150MHz ≤ FO ≤ 160MHz	0.1 @ 3.3Vdc and 2.5Vdc

MECHANICAL DIMENSIONS

Marking Guide on page F3-F4

Pin	Connection
1	Enable / Disable
2	NC
3	Ground
4	Output
5	C - Output
6	Vcc

