

Microcomputer Development Support System

Host System

■ TDS68DVLP

TDS68DVLP is a host system for TLCS-68000 family, mounted with 32-bit microprocessor 68020, that enables simultaneous supporting of up to 8 users under VME bus specifications. This TDS68DVLP is provided with 16.67MHz 68020, co-processor for floating decimal point operation, and 16K-byte cache memory while its operating system is equipped with UNIX™ (SYSTEM V/68™).

Hardware is composed of module groups and auxiliary memory devices. These module groups consist of 2M-byte RAM module (expandable), serial I/O module, and disk control module. On the other hand, auxiliary memory devices comprise 70M-byte hard disk, 655K-byte floppy disk, and storing tape cartridge.

As for software, mounted is UNIX™ (SYSTEM V/68™) operating system. SYSTEM V/68™ is provided with C language, FORTRAN77, Pascal, Assembler, and Linker. Further, to improve development efficiency of application systems, SLD (source level debugger) of C language is available.
Note: UNIX is a tradename of AT&T Inc.
SYSTEM V/68 is a tradename of Motorola, Inc. U.S.A.

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32/64bit RISC (Reduced Instruction Set Computer) & Bus Chip Set

RISC Processor

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Group Name	Product Name	Package	Function	Data Sheet	Status
32 Bit RISC	TC86R3081EY-40	PGA84	IDT R3081™ functional compatible • R3000A + I-Cache+D-Cache+R/W Buffer • I-Cache 8 or 16K Byte • D-Cache 8 or 4K Byte • 4 deep R/W Buffer Power management mode. (Added feature to IDT R3081) 3.3V 33MHz operation	Available	ES 94/Jul.
64 Bit RISC	TC85R4000PC-50	PGA179	64-bit RISC Processor Compatible with MTI R4000PC™ • IU+FPU+ I-Cache+D-Cache • I-Cache 8K Byte • D-Cache 8K Byte	Available	CS Now
	TC85R4000SC-50*	PGA447	64-bit RISC Processor Compatible with MTI R4000SC • IU+FPU+I-Cache+D-Cache • I-Cache 8K Byte • D-Cache 8K Byte • Secondary Cache Controller	Available	CS Now
	TC85R4400PC-50	PGA179	64-bit RISC Processor Compatible with MTI R4400PC™ • IU+FPU+ I-Cache+D-Cache • I-Cache 16K Byte • D-Cache 16K Byte	Available	ES Now
	TC86R4400PC-67/75				
	TC85R4400SC-50	PGA447	64-bit RISC Processor Compatible with MTI R4400SC • IU+FPU+ I-Cache+D-Cache • I-Cache -16K Byte • D-Cache -16K Byte • Secondary Cache Controller	Available	ES Now
	TC86R4400SC-67/75			Available	ES Now
	TC85R4400MC-50	PGA447	64-bit RISC Processor Compatible with MTI R4400MC • IU+FPU+ I-Cache+D-Cache • I-Cache 16K Byte • D-Cache 16K Byte • Secondary Cache Controller • Multiprocessor capability	Available	ES Now
	TC86R4400MC-67/75			Available	ES Now
	TC86R4600-100	PGA179 CQFP208	64-bit RISC Processor • MIPS Architecture • I-Cache 16K Byte • D-Cache 16K Byte • Operation Voltage 3.3V • 100MHz (Internal), 50MHz (External)	Available	ES 94/Jun.

* TC85R4000SC-50 contains tested multiprocessor functionality.
R3000A, R4000, R4400 are trade marks of MIPS Technology Inc.
R3081 is a trade mark of Integrated Device Technology, Inc.

Bus Chip Set

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Group Name	Parts Name	Package	Function Summary	Technical Data	Product Status
486 Bus Bridge chip set	TC86R4021F	QFP160-P-2828A	I/O Controller (i-ctrl) Support for 3.3V processors and 5.0V system interface	18 Feb. '94 (Preliminary)	2Q '94 ES
	TC86R4022F	QFP160-P-2828A	Cache Controller (c-ctrl) Support for 3.3V processors and 5.0V system interface	18 Feb. '94 (Preliminary)	2Q '94 ES
PCI Bus Bridge chip set	TC86R4031F	—	Data Path Chip (DPC) Support for 3.3V processors and 5.0V PCI Use 2 chips for a system	—	Under development
	TC86R4032F	—	Address Control Chip (ACC) Memory Control Cache Control PCI Control Support for 3.3V processors and 5.0V PCI Use 1 chip for a system	—	Under development