

# ICs/LSIs for VCR, Camera

## Others for Video

Category	Type No.	Operating Voltage (V)	Process	Functions	Package	
						No.
Converter	AN3122	6.2	Bipolar	RF converter circuit	16-DIP(b)	B39
	AN3126/SB	4.5 ~ 5.5		RF modulator circuit	14-DIP(a) USO-16D	B36 B69
	AN3129S	4.5 ~ 5.5		RF modulator circuit	SO-14D	B66
	AN3131	9		RF converter circuit	14-ZIP	B29
A/D converter	MN6550B/S	5	CMOS	Conversion rate 15MSPS, Resolution 7 bit	22-DIP/SO-22D	L13 L37
	MN65531AS	5		Resolution 6 bit, Conversion rate 20MSPS	SO-22D	L37
	MN65543S	5		Resolution 8 bit, Conversion rate 15MSPS	SO-24D	L38
	MN655431SH	4.8/3.5		Resolution 8 bit, Conversion rate 15MSPS	SSOP-24D	L39
	△MN6576H	4.8/3.3		Resolution 9 bit, Conversion rate 20MSPS	32-QFH	L42
	AN8130K/FBP	$V_{CC} = 5$ $V_{EE} = -5$	Bi-CMOS	High-speed low power consumption, 10 bit A/D converter	42-SDIP 64-QFH(e)	B59 B102
	AN6857N	-5, 2	Bipolar	High-speed 8 bit A/D converter	40-DIC	B50
D/A converter	MN65523A/S	5	CMOS	Resolution 6 bit, Conversion rate 20MSPS	16-DIP(c)/SO-22D	L9 L37
	MN6556A/S	5		Resolution 8 bit, Conversion rate 20MSPS	22-DIP/SO-22D	L13 L37
	MN6557A/S	5		Resolution 10 bit, Conversion rate 30MSPS	22-DIP/SO-22D	L13 L37
	MN6559S	5		Resolution 6 bit × 3, Conversion rate 20MSPS	SO-28D	L40
	MN6570F	5		Resolution 8 bit × 3, Conversion rate 35MSPS	44-QFP	L46
	MN6570TF	5		Resolution 8 bit × 3, Conversion rate 40MSPS	44-QFP	L46
	MN6570EF	5		Resolution 8 bit × 3, Conversion rate 40MSPS	44-QFP	L46
	MN65701FHP	5		Resolution 8 bit × 3, Conversion rate 35MSPS	48-QFH	L47
	MN657011H	3.5		Resolution 8 bit × 3, Conversion rate 20MSPS	48-QFH	L47
	△MN65702H	4.8/3.3		Resolution 8 bit × 3, Conversion rate 20MSPS	48-QFH	L47
	MN6571K	5		Resolution 8 bit × 3, Conversion rate 31.5MSPS, Interpolation filter built-in	28-SDIP	L20
	AN8140K/S	4.75 ~ 5.25	Bi-CMOS	High-speed low power consumption, 10 bit D/A converter	24-DIP/SO-24D	B55 B74
AN8145FBP	5	Bi-CMOS	High-speed low power consumption, 10 bit D/A converter (3 ch)	64-QFH(c)	B102	
A/D · D/A on-chip	AN8124K/SC	4.75 ~ 5.25	Bi-CMOS	High-speed 8 bit A/D · D/A converter	30-SDIP/VSO-32D	B58 B84
Others	MN31061	5	CMOS	Clock multiplier (2fsc → 8fsc)	8-DIP	L1
	MN3108S	5		Clock multiplier (fsc → 6fsc)	SO-8D	L29
	AN6873N/NS	-50 ~ -15	Bipolar	Fluorescent display tube drive circuit (8 circuits)	18-DIP/SO-18D	B42 B70
	AN8360NK	8 ~ 19		Battery charge control circuit	24-SDIP	B55
	HH8360	-	H-IC	Battery charge control circuit	13-SIL	B28
	MN6790/S	4.5 ~ 5.5	CMOS	Clock generator	14-DIP(b)/SO-14D	L31

△ Preliminary

## For LD

Category	Type No.	Operating Voltage (V)	Process	Functions	Package	
						No.
For multi-laser player	MN66262	3.4 ~ 5.5	CMOS	Digital sound signal processing (For bilingual)	64-QFP(b)	L52
	MN88101	4.5 ~ 5.5		Digital TBC	84-QFP	L59
	MN73045PXH	4.5 ~ 5.5		Video memory controller	124-QFP	L63
	MN73534PXE	4.75 ~ 5.25		Disk system controller	64-SDIP	L24
	MN53060PXG	4.5 ~ 5.25		Digital Y/C separation	100-QFP(a)	L61
	AN2661NK	4.5 ~ 5.5	Bipolar	Video signal processing circuit for multi-laser player	30-SDIP	B58
	AN2662K	± 4.5 ~ ± 5.5		Time axis correction control circuit for multi-laser player	30-SDIP	B58
	AN2663K/S	4.75 ~ 5.5		Video signal processing circuit	24-SDIP/SO-24D	B55 B74
	AN3891FBP	4.5 ~ 5.5		Spindle motor drive circuit for multi-laser player	42-QFH	B89

(Package Symbol) SIP = Single-In-Line Plastic Package, DIP = Dual-In-Line Plastic Package, SDIP = Shrunk Dual-In-Line Plastic Package,

QFP = Quad Flat Package, QFH = Quad Flat L-Leaded High Package,

SO = Small Outline Package (PANA FLAT PACKAGE)