

CD54HC4067/3A
CD54HCT4067/3A

HARRIS SEMICOND SECTOR

T-51-12

T-43-21

Switching Speed (Limits with black dots (•) are tested 100%.)

SWITCHING CHARACTERISTICS ($C_L = 50$ pF, Input $t_r, t_f = 6$ ns)

CHARACTERISTIC	SYMBOL	V_{CC} V	25°C				-55°C to +125°C				UNITS	
			HC		HCT		54HC		54HCT			
			Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.		
Propagation Delay Switch In to Out	t_{PLH} t_{PHL}	2	—	75	—	—	—	—	110	—	—	ns
		4.5	—	15•	—	15•	—	22•	—	22•		
		6	—	13	—	—	—	19	—	—		
Switch Turn-On \bar{E} to Out	t_{PZL}	2	—	275	—	—	—	—	415	—	—	
		4.5	—	55•	—	60•	—	83•	—	90•		
		6	—	47	—	—	—	71	—	—		
Sn to Out	t_{PZH}	2	—	300	—	—	—	—	450	—	—	
		4.5	—	60•	—	60•	—	90•	—	90•		
		6	—	51	—	—	—	76	—	—		
Switch Turn-Off \bar{E} to Out	t_{PLZ}	2	—	275	—	—	—	—	415	—	—	
		4.5	—	55•	—	55•	—	83•	—	83•		
		6	—	47	—	—	—	71	—	—		
Sn to Out	t_{PHZ}	2	—	290	—	—	—	—	435	—	—	
		4.5	—	58•	—	58•	—	87•	—	87•		
		6	—	49	—	—	—	74	—	—		
Input Capacitance	C_i	—	—	10	—	10	—	10	—	10	pF	

Burn-In Test-Circuit Connections (Use Static II for /3A burn-in and Dynamic for Life Test.)

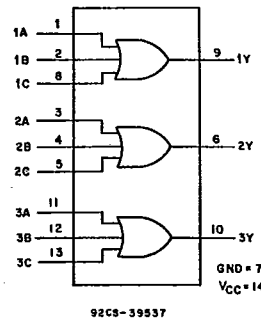
Static	STATIC BURN-IN I			STATIC BURN-IN II		
	OPEN	GROUND	V_{CC} (6V)	OPEN	GROUND	V_{CC} (6V)
CD54HC/HCT4067	1	2-23	24	1	12	2-11,13-24
Dynamic	OPEN	GROUND	$1/2 V_{CC}$ (3V)	V_{CC} (6V)	OSCILLATOR	
CD54HC/HCT4067	—	12,15	1	24	50 kHz	25 kHz
					2-9,16-23	(10,11,13,14) ¹

NOTE: Each pin except V_{CC} and Gnd will have a resistor of 2k-47k ohms.
¹Pin 10 is at 14 kHz, pin 11 is at 7 kHz, Pin 13 is at 1.7 kHz, and pin14 is at 3.5 kHz.

CD54HC4075/3A
CD54HCT4075/3A

Triple 3-Input OR Gate

The RCA-CD54HC4075 and CD54HCT4075 logic gates utilize silicon-gate CMOS technology to achieve operating speeds similar to LSTTL gates with the low power consumption of standard CMOS integrated circuits. All devices have the ability to drive 10 LSTTL loads. The CD54HCT logic family is functionally as well as pin compatible with the standard 54LS logic family.



FUNCTIONAL DIAGRAM

Package Specifications
See Section 11, Fig. 10

CD54HC4075/3A
CD54HCT4075/3A

T-43-21

Static Electrical Characteristics (Limits with black dots (•) are tested 100%)

CHARACTERISTICS		TEST CONDITIONS							LIMITS		UNITS	
		HC/HCT				V _{IN}		MIN.				MAX.
		V _{DD}	V _O	I _O	V _{CC} or GND	HC V _{IL} or V _{IH}	HCT V _{IL} or V _{IH}					
Quiescent Device Current I _{CC}	25°C	6	—	—	6, 0	—	—	—	2•	μA		
	-55°C	6	—	—	6, 0	—	—	—	40•			
	+125°C	6	—	—	6, 0	—	—	—	40•			

The complete static electrical test specification consists of the above by-type static tests combined with the standard static tests in the beginning of this section.



HCT INPUT LOADING TABLE

INPUT	UNIT LOAD*
All	1.6

*Unit load is ΔI_{CC} limit specified in Static Characteristics Chart, e.g., 360 μA max. @ 25°C.

Switching Speed (Limits with black dots (•) are tested 100%)

SWITCHING CHARACTERISTICS (C_L = 50 pF, Input t_r, t_f = 6 ns)

CHARACTERISTIC	SYMBOL	V _{CC} V	25°C				-55°C to +125°C				UNITS
			HC		HCT		54HC		54HCT		
			Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	
Propagation Delay Input to Output	t _{PLH}	2	—	100	—	—	—	150	—	—	ns
	t _{PHL}	4.5	—	20•	—	24•	—	30•	—	36•	
Transition Times	t _{TLH}	6	—	17	—	—	—	26	—	—	
	t _{THL}	2	—	75	—	—	—	110	—	—	
	t _{TLH}	4.5	—	15	—	15	—	22	—	22	
Input Capacitance	C _i	6	—	13	—	—	—	19	—	—	pF
		—	—	10	—	10	—	10	—	10	

Burn-In Test-Circuit Connections (Use Static II for /3A burn-in and Dynamic for Life Test.)

Static	STATIC BURN-IN I			STATIC BURN-IN II		
	OPEN	GROUND	V _{CC} (6V)	OPEN	GROUND	V _{CC} (6V)
CD54HC/HCT4075	6,9,10	1-5,7,8,11-13	14	6,9,10	7	1-5,8,11-14
Dynamic	OPEN	GROUND	1/2 V _{CC} (3V)	V _{CC} (6V)	OSCILLATOR	
	CD54HC/HCT4075	—	7	6,9,10	14	50 kHz 25 kHz
						1-5,8,11-13 —

NOTE: Each pin except V_{CC} and Gnd will have a resistor of 2k-47k ohms.