

*Product Preview*  
**Dual Differential AND/NAND**

The MC100LVEL03 is a dual differential AND/NAND gate. The MC100EL03 is pin and functionally equivalent to the MC100LVEL03 but is specified for operation at the standard 100E ECL voltage supply. The  $V_{BB}$  pin is provided for single-ended input applications. When used the  $V_{BB}$  pin should be bypassed to ground via a  $0.01\mu\text{F}$  capacitor.

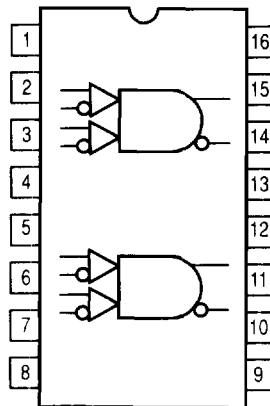
The differential inputs have special circuitry which ensures device stability under open input conditions. When both differential inputs are left open the D input will pull down to  $V_{EE}$  and the  $\bar{D}$  input will bias around  $V_{CC}/2$ . This situation will then force a LOW on the Q output.

- 16-Lead SOIC Packaging
- 400ps Typical Propagation Delays
- Supports Both Standard and Low Voltage 100K ECL

**MC100LVEL03**  
**MC100EL03**



**Logic Diagram: 16-Lead SOIC**  
 (Top View, Pinout TBD)



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