



040707

HS-C²MOS™ INTEGRATED CIRCUITS

PRODUCT PREVIEW

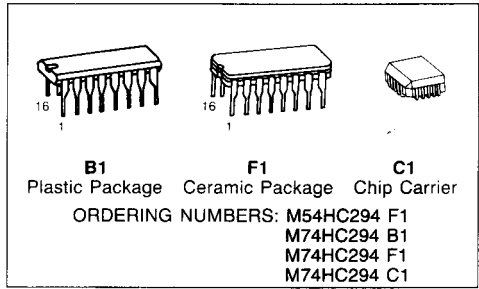
PROGRAMMABLE FREQUENCY DIVIDER/ DIGITAL TIMER

GENERAL DESCRIPTION

The M54/74HC294 is a high speed CMOS PROGRAMMABLE FREQUENCY DIVIDER/DIGITAL TIMER fabricated in Silicon Gate C²CMOS Technology. It has the same high speed performance of LSTTL combined with true CMOS low power consumption.

This device has an active-low clear input to initialize the state of all flip-flops. To facilitate incoming inspection, a test point (TP) is provided. This test point is not intended to drive system loads. It has two clock inputs; either one may be used for clock gating. (See the truth table below.)

All inputs are equipped with protection circuits against static discharge or transient excess voltage.



FEATURES

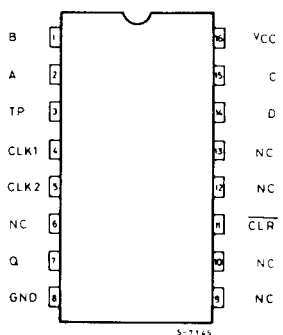
- Low Power Dissipation
 $I_{CC} = 4 \mu A$ (Max.) at $T_A = 25^\circ C$
- High Noise Immunity
 $V_{NIH} = V_{NIL} = 28\% V_{CC}$ (Min.)
- Output Drive Capability
10 LSTTL Loads
- Symmetrical Output Impedance
 $|I_{OH}| = I_{OL} = 4 mA$ (Min.)
- Balanced Propagation Delays
 $t_{PLH} = t_{PHL}$
- Wide Operating Voltage Range
 $V_{CC} (opr) = 2V$ to $6V$
- Pin and Function compatible with 54/74LS294

TRUTH TABLE

*: Don't care

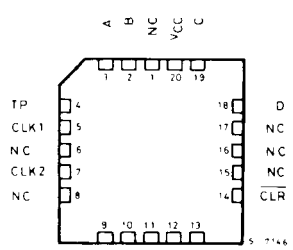
CLEAR	CLK1	CLK2	Q OUTPUT MODE
L	*	*	Cleared to L
H		L	Count
H		L	No Count
H	L		Count
H	L		No Count
H	H	*	Inhibit
H	*	H	Inhibit

PIN CONNECTIONS (top view)



Dual in line

CHIP CARRIER



NC = No Internal Connection