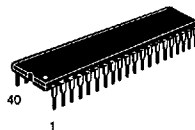


MOTOROLA
SEMICONDUCTOR ■ **MOTOROLA SC (TELECOM)**
TECHNICAL DATA
Product Preview
**Time Slot Interchange
Circuit**

The MC145601 time slot interchange circuit (TSIC) is a CMOS IC designed for switching pulse code modulation (PCM) voice or data, under microprocessor control, in a digital exchange or central office. It connects any of 256 incoming PCM channels to any of 256 outgoing PCM channels.

- 5 V Supply
- 8 × 32 Channel Input
- 8 × 32 Channel Output
- 256 Port Non-blocking Digital Switching Matrix
- Building Block for Digital PABX
- Expandable to Larger Capacity Block
- 32 Serial Channels Per Frame
- Typical Bit Rate: 2.048 Mbps
- Typical Synchronization Rate: 8 kHz
- Interface to MC88XXX Family Microprocessors
- 8 Instructions Available
- 40-Pin Dual-In-Line Package

**NOT RECOMMENDED
FOR NEW DESIGN**

MC145601


P SUFFIX
PLASTIC
CASE 711

PIN ASSIGNMENT

| | | | |
|-----------------|----|----|-----------------|
| OC4 | 1 | 40 | RESET |
| OC3 | 2 | 39 | CLK |
| OC2 | 3 | 38 | SYNC |
| OC1 | 4 | 37 | Tx7 |
| OCO | 5 | 36 | Tx6 |
| Rx7 | 6 | 35 | Tx5 |
| Rx6 | 7 | 34 | Tx4 |
| Rx5 | 8 | 33 | V _{DD} |
| Rx4 | 9 | 32 | Tx3 |
| V _{SS} | 10 | 31 | Tx2 |
| Rx3 | 11 | 30 | Tx1 |
| Rx2 | 12 | 29 | Tx0 |
| Rx1 | 13 | 28 | D7 |
| Rx0 | 14 | 27 | D6 |
| READY | 15 | 26 | D5 |
| DTACK | 16 | 25 | D4 |
| RS1 | 17 | 24 | D3 |
| RS0 | 18 | 23 | D2 |
| R \bar{W} | 19 | 22 | D1 |
| CS | 20 | 21 | D0 |