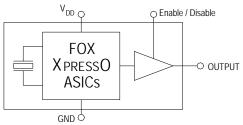


Model: FXU-HC73 SERIES

HCMOS 7 x 5mm 3.3V Oscillator Freq: 0.016 MHz to 167MHz

Features

- ULTRA Low Jitter
- Low Cost
- XPRESS Delivery
- Frequency Resolution to six decimal places
- Stabilities to ± 20 PPM
- -20 to +70°C or -40 to +85°C operating temperatures
- Tri-State Enable / Disable Feature
- Industry Standard Package, Footprint & Pin-Out
- Fully RoHS compliant
- Gold over Nickel Termination Finish
- Serial ID with Comprehensive Traceability



For more information -- Click on the drawing

Description

The Fox XPRESSO-ULTRA Crystal Oscillator is a breakthrough in configurable Frequency Control Solutions. XPRESSO-ULTRA utilizes a family of proprietary ASICs, designed and developed by Fox, with a key focus on noise reduction technologies.

The 4th order Delta Sigma Modulator reduces noise to the levels that are comparable to traditional Bulk Quartz and SAW oscillators. The ASICs family has the ability to select the output type and supply voltage.

With the XPRESSO-ULTRA lead-time, low cost, low noise, wide frequency range, excellent ambient performance, XPRESSO-ULTRA is an excellent choice over the conventional technologies.

Finished XPRESSO-ULTRA parts are 100% final tested.





Rev. 7/21/2015



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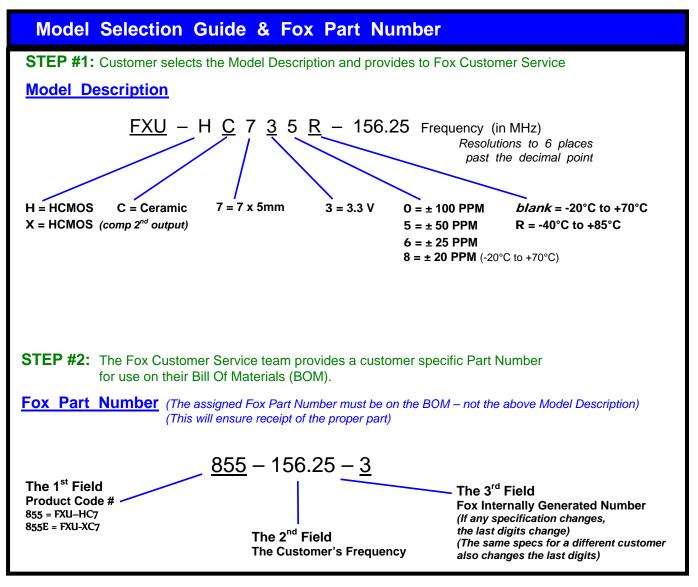
Applications

- ANY application requiring a high performance HCMOS oscillator
- SONET
- Ethernet
- Storage Area Network
- Broadband Access
- Microprocessors / DSP / FPGA
- Industrial Controllers
- Test and Measurement Equipment

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This example, FXU-HC735R-156.25 = HCMOS Output, Ceramic, 7 x5mm Package, 3.3V, ±50 PPM Stability, -40 to +85°C Temperature Range, at 156.25 MHz





	Electrical Characteristics			
Parameters	Symbol	Condition	Maximum Value (unless otherwise noted)	
Frequency Range	Fo		0.016 MHz to 167 MHz	
Frequency Stability ¹		0.016 MHz to 167 MHz	100, 50, 25, 20 ² PPM	
Temperature Range	Т _о Т _{stg}	Standard operating Optional operating Storage	-20°C to +70°C -40°C to +85°C -55°C to +125°C	
Supply Voltage	V _{DD}	Standard	3.3 V ± 5%	
Input Current (@ Standard Load)	I _{DD}	0.016 to 62.5 MHZ 62.5+ to 167 MHz	98 mA 108 mA	
Output Load		Standard	15 pF	
Start-Up Time	Ts		10 mS	
Output Enable / Disable Time			100 nS	
Moisture Sensitivity Level	MSL	JEDEC J-STD-20	1	
Termination Finish			Au	

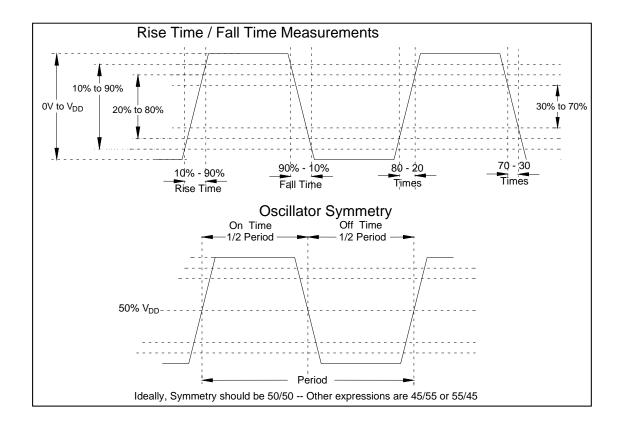
Note 1 – Stability is inclusive of 25°C tolerance, operating temperature range, input voltage change, load change, aging, shock and vibration. Note 2 –. ±20 PPM stability -20°C to +70°C only.

Absolute Maximum Ratings (Useful life may be impaired. For user guidelines only, not tested)				
Parameters	Symbol	Condition	Maximum Value (unless otherwise noted)	
Input Voltage	V _{DD}		-0.5V to +5.0V	
Operating Temperature	T _{AMAX}		–55°C to +105°C	
Storage Temperature	T _{STG}		–55°C to +125°C	
Junction Temperature			125°C	
ESD Sensitivity	HBM	Human Body Model	1 kV	





Output Wave Characteristics			
Parameters	Symbol	Condition	Maximum Value (unless otherwise noted)
Low Output Voltage	V _{OL}	0.016 MHz to 167 MHz	10%V _{DD}
High Output Voltage	V _{OH}	0.016 MHz to 167 MHz	90%V _{DD} Minimum
Output Symmetry @ 50% V _{P-P} Level (See Drawing Below)		0.016 to to 167 MHz	45% ~ 55%
Output Enable Note1 (PIN # 1) Voltage	VIH		≥ 70% V _{DD}
Output Disable ^{Note1} (PIN # 1) Voltage	VIL		≤ 30% V _{DD}
Cycle Rise Time (10%~90%V _{DD} - See Drawing Below)	T _R	0.016 MHz to 167 MHz	2.5 nS
Cycle Fall Time (90%~10%V _{DD} - See Drawing Below)	T _F	0.016 MHz to 167 MHz	2.5 nS

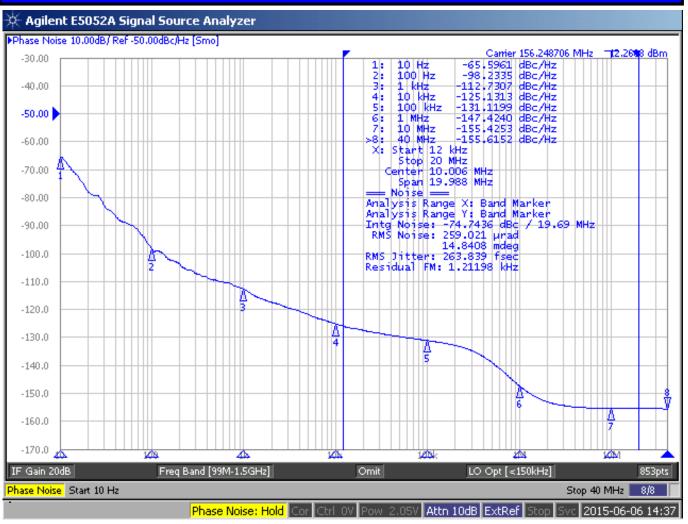






FXU-HC73 Series

Phase Noise (typical measurements at 156.25 MHz)



Jitter is frequency dependent. Below are typical values at 156.25 MHz.

HCMOS Phase Jitter & Time Interval Error (TIE)			
Frequency	Phase Jitter (pS) (12kHz to 20MHz)	TIE (pS) (sigma of jitter distribution)	
156.25 MHz	0.26	1.7	

Phase Jitteris integrated from Agilent 5052A Signal Noise Analyzer; measured directly into 50 ohm input; $V_{DD} = 3.3V$.TIEwas measured on LeCroy LC684 Digital Storage Scope, directly into 50 ohm input, with Amherst M1 software; $V_{DD} = 3.3V$.Per MJSQ spec(Methodologies for Jitter and Signal Quality specifications)

HCMOS Ra	HCMOS Random & Deterministic Jitter Composition		
Frequency	Random (Rj)	Deterministic (Dj) (pS P-P)	Total Jitter (Tj) (14 x Rj) + Dj
156.25 MHz	0.6	6.1	14.8

<u>**Rj and Dj**</u>, measured on LeCroy SDA-6000A Digital Storage Scope, directly into 50 ohm input, with Amherst M1 software. *Per MJSQ spec (Methodologies for Jitter and Signal Quality specifications)*





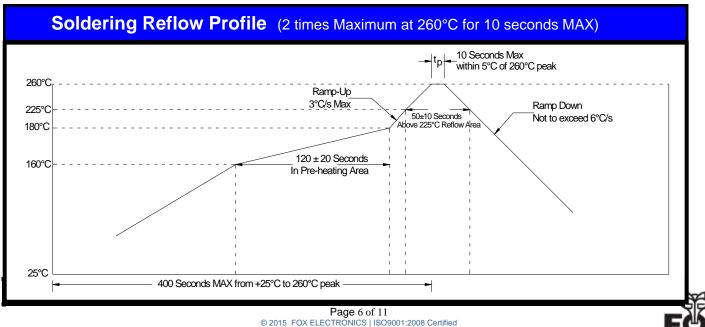
FXU-HC73 Series

			mended Circuit
Pin #	Name	Туре	Function
1	E / D 1	Logic	Enable / Disable Control of Output $(0 = Disabled)$
2	GND	Ground	Electrical Ground for VDD
3	Output	Output	HCMOS Oscillator Output
4	VDD 2	Power	Power Supply Source Voltage
Test Points	N. C.	Hi Z	No Connection (Factory Use ONLY)
 NOTES: ¹ Includes pull-up resistor to V_{DD} to provide output when the pin (1) is No Connect. ² Installation should include a 0.1µF bypass capacitor placed between V_{DD} (Pin 4) and GND (Pin 2) to minimize power supply line noise. 			
$E / D 1 4 V_{DD} 0.1 uF 0.1 $			

N. C. TP TP N. C. GND 2 3 Output E/D #1 #4 NC GND #2 #3 OUT HCMOS LOAD (15pF) HCMOS LOAD

Terminations as viewed from the Top NOTE: XPRESSO-Ultra HCMOS XOs are designed to fit on Industry Standard, 4 pad layouts

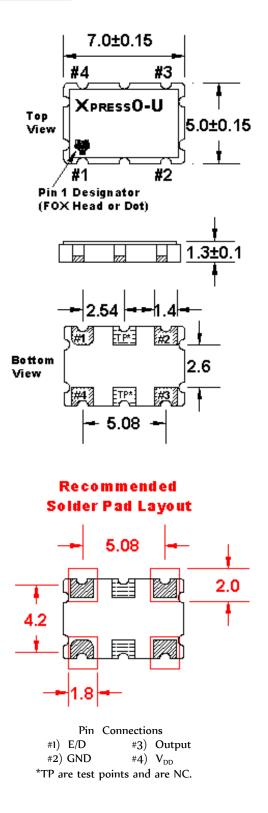
Enable / Disable Control		
Pin # 1 (state)	Output (Pin #3)	
OPEN (No Connection)	ACTIVE Output	
"1" Level V _{IH} ≥ 70% V _{DD}	ACTIVE Output	
"0" Level $V_{IL} \le 30\% V_{DD}$	High Impedance	



An Integrated Device Technology, Inc. company



FXU-HC73 Series



Actual part marking is depicted.

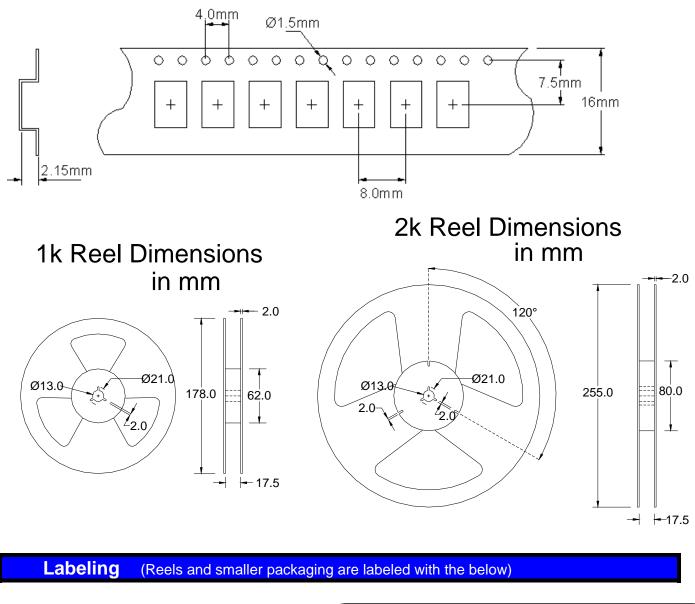
See **Traceability** (pg. 9) for more information

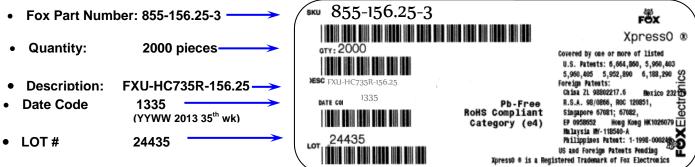
Drawing is for reference to critical specifications defined by size measurements. Certain non-critical visual attributes, such as side castellations, reference pin shape, etc. may vary





Tape and Reel Dimensions









Traceability – LOT Number & Serial Identification

LOT Number

The LOT Number has direct ties to the customer purchase order. The LOT Number is stored internally in non-volatile memory. XPRESSO-ULTRA parts are packaged in Cut Tape or Tape and Reel and placed in an ESD bag. The package label containing the Lot number is affixed to the ESD bag, and in the case of Tape and Reel it is attached to the reel as well

It is recommended that the XPRESSO-ULTRA parts remain in this ESD bag during storage for protection and identification.

If the parts become separated from the label showing the LOT Number, it can be retrieved from inside one of the parts, and the information that can be obtained is listed below:

- Customer Purchase Order Number
- Internal Fox Sales Order Number
- Dates that the XPRESSO-ULTRA part was shipped from the factory
- The assigned customer part number
- The specification that the part was designed for

Serial Identification

The Serial ID is the individualized information about the configuration of that particular XPRESSO-ULTRA part. The Serial ID is unique for each and every XPRESSO-ULTRA part, and can be read by special Fox equipment.

With the Serial ID, the below information can be obtained about that individual, XPRESSO-ULTRA part:

- Equipment that the XPRESSO-ULTRA part was configured on
- Raw material used to configure the XPRESSO-ULTRA part
- Traceability of the raw material back to the foundries manufacturing lot
- Date and Time that the part was configured
- Any optimized electrical parameters based on customer specifications
- Electrical testing of the actual completed part
- Human resource that was monitoring the configuration of the part

Fox has equipment placed at key Fox locations World Wide to read the Lot Identification and Serial Number of any XPRESSO-ULTRA part produced and can then obtain the information from above within 24 hours





Mechanical Testing

Parameter	Test Method
Mechanical Shock	MIL-STD-202 Method 213 Condition C
Mechanical Vibration	MIL-STD-202 Method 204 5g's for 20 minutes 12 cycles of each 3 orientations: X, Y, Z
High Temperature Operating Life (HTOL)	Under Power @ 125°C for 1000 Hours
Hermetic Seal	MIL-STD-202F: Method 112D Test condition C He pressure: 4 ±1 kgf / cm ² 2 Hour soak





XpressO-ULTRA Home

XpressO-ULTRA XOs

XpressO Brochure

Patent Numbers: US 6,664,860, US 5,960,403, US 5,952,890; US 5,960,405; US 6,188,290; Foreign Patents: R.S.A. 98/0866, R.O.C. 120851; Singapore 67081, 67082; EP 0958652 China ZL 98802217.6, Malaysia MY-118540-A, Philippines 1-1998-000245, Hong Kong #HK1026079, Mexico #232179 US and Foreign Patents Pending XpressO® Fox Electronics

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The above specifications, having been carefully prepared and checked, is believed to be accurate at the time of publication; however, no responsibility is assumed by Fox Electronics for inaccuracies.

