

Introduces

## M310x Series LVPECL/LVDS/CML VCXO

Featuring **QiK Chip™** Technology

### Features:

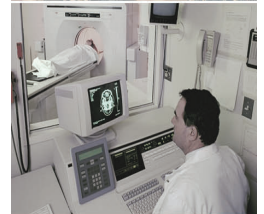
- Superior Jitter Performance (less than 0.25 ps RMS, 12 kHz - 20 MHz)
- Frequencies from 150 MHz to 1.4 GHz
- Designed for a short 2 week cycle time

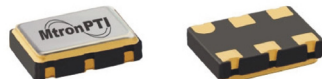
### Applications:

- Telecommunications such as SONET / SDH / DWDM / FEC / SERDES / OC-3 thru OC-192
- Wireless base stations / WLAN / Gigabit Ethernet
- Avionic flight controls and military communications

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## Applications Note:

The MtronPTI M310x series of voltage controlled crystal oscillators, featuring *QiK Chip™* technology, provides for extremely low jitter of 0.50 ps RMS, typical at 622.08 MHz. For applications requiring low jitter, frequencies from 150 MHz to 1.4 GHz are available. LVPECL, LVDS, or CML compatible outputs, as well as operating voltage of 1.8 V, 2.5 V, and 3.3 V are also options on the M310x.

The M310x is available with a standard APR of  $\pm 50$  ppm and  $\pm 100$  ppm, over the industrial operating temperature range of  $-40^{\circ}\text{C}$  to  $+85^{\circ}\text{C}$ . The M310x achieves this level of performance by utilizing an AT-cut crystal. An enable/disable function is also an available option on the M310x. An internal 0.01  $\mu\text{F}$  by-pass capacitor also assures optimum noise suppression on the supply voltage pad.

The superior integrated jitter performance of 0.50 pS RMS, typical at 622.08 MHz, makes the M310x suitable for 10 Gig-E, broadband networks, network switches, SONET, SDH, SERDES, DWDM, FEC, WLAN, and OC-3 thru OC-192 systems. The M310x is available in a six-pad or nine-pad, 5x7x1.9 mm, leadless, ceramic, surface mount package (see page 4, N package drawing) that is RoHS and  $+260^{\circ}\text{C}$  reflow compatible, (no PCB traces should be located directly under the 5x7 product). The M310x oscillators are backward compatible to many of the existing products in the industry from Vectron, Epson, and others.

For superior performance in a high frequency clock oscillator, the M310x is a logical choice for designers. The unique design architecture allows the M310x fast turn around on engineering design samples, as well as production quantities in 2 weeks or less.

## M310x Series LVPECL/LVDS/CML Voltage Controlled Crystal Oscillator – 3.3/2.5/1.8 Volt – 5x7 mm

### Product Features

- Featuring **QiK Chip™** Technology
- From order to ship in 2 weeks
- Superior Jitter Performance (less than 0.25 ps RMS, 12 kHz - 20 MHz)
- APR from  $\pm 50$  to  $\pm 300$ ppm over industrial temperature range
- SAW replacement performance
- Frequencies from 150 MHz to 1.4 GHz

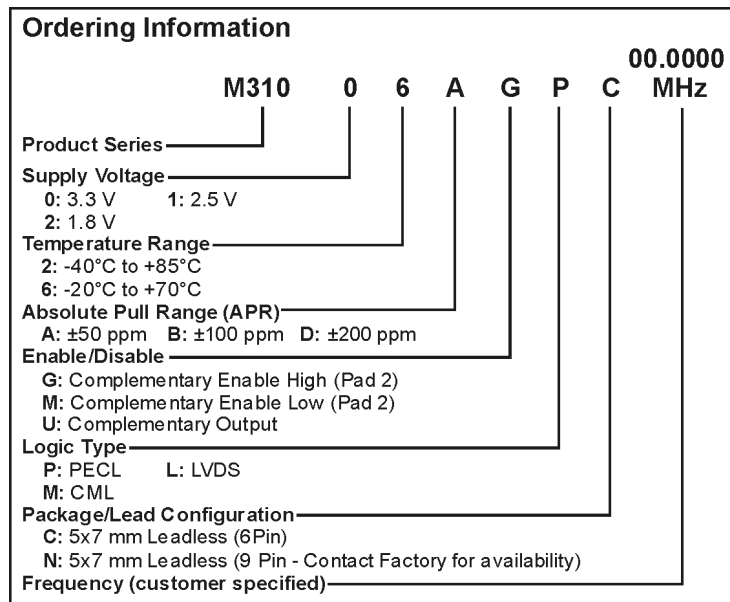
### Product Description

The M310x series of VCXO's is designed with a hermetically sealed high precision AT cut quartz crystal, combined with our QiK Chip™ technology. This combination provides an industry setting 0.35 ps RMS jitter performance and excellent Phase Noise for your demanding circuit. The M310x is available in LVPECL, LVDS, or CML output and can be built to a variety of power requirements, 3.3, 2.5, and 1.8V. Tight thermal stability performance, broad frequency range, in a small 5x7 mm package, and the ability to build and provide product in approximately 2 weeks, gives the designer a quick, solid foundation to build a solution with.

### Product Applications

- Telecommunications such as SONET / SDH / DWDM / FEC / SERDES / OC-3 thru OC-192
- Wireless base stations / WLAN / Gigabit Ethernet
- xDSL, Network Communications
- Avionic Flight Controls
- Military Communications
- Clock and Data Recovery
- Low Jitter Clock Generation

### Product Ordering Information



M3100Sxxx, M3101Sxxx & M3102Sxxx - Contact factory for datasheets.

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# M310x Series LVPECL/LVDS/CML Voltage Controlled Crystal Oscillator – 3.3/2.5/1.8 Volt – 5x7 mm

## Performance Characteristics

PARAMETER	Symbol	Min.	Typ.	Max.	Units	Condition/Notes	
Frequency Range	F	150		1400	MHz	See Note 1	
Operating Temperature	T <sub>A</sub>	(See ordering information)					
Storage Temperature	T <sub>s</sub>	-55		+125	°C		
Frequency Stability	ΔF/F		±25		ppm		
Aging 1st Year Thereafter (per year)		-3 -1		+3 +1	ppm ppm		
Pullability/APR		(See ordering information)					See Note 2
Gain Transfer Function			90 135 180		ppm/V ppm/V ppm/V	For ±50 ppm APR For ±100 ppm APR For ±200 ppm APR	
Control Voltage	V <sub>c</sub>	0.18 0.25 0.30	0.90 1.25 1.65	1.62 2.25 3.0	V V V	@ 1.8V V <sub>cc</sub> @ 2.5V V <sub>cc</sub> @ 3.3V V <sub>cc</sub>	
Linearity			1	5	%	Positive Monotonic	
Modulation Bandwidth	f <sub>m</sub>	10			KHz	-3 dB bandwidth	
Input Impedance	Z <sub>in</sub>	500k	1M		Ohms	@ DC	
Supply Voltage	V <sub>cc</sub>	1.71 2.375 3.135	1.8 2.5 3.3	1.89 2.625 3.465	V V V	LVDS/CML	
Input Current	I <sub>cc</sub>			125	mA	LVPECL/LVDS/CML	
Load		50 Ohms to (V <sub>cc</sub> - 2) V <sub>dc</sub> 100 Ohm differential load					See Note 3 LVPECL Waveform LVDS/CML Waveform
Symmetry (Duty Cycle)		45		55	%	LVPECL: V <sub>dd</sub> -1.3 V LVDS: 1.25 V	
Output Skew			20 15 20		ps ps ps	LVPECL CML LVDS	
Differential Voltage	V <sub>od</sub>	250	350	450	mV	LVDS	
	V <sub>od</sub>	0.7	0.95	1.20	V <sub>pp</sub>	CML	
Common Mode Output Voltage	V <sub>cm</sub>		1.2		V	LVDS	
Logic "1" Level	V <sub>oh</sub>	V <sub>cc</sub> - 1.02			V	LVPECL	
Logic "0" Level	V <sub>ol</sub>			V <sub>cc</sub> - 1.63	V	LVPECL	
Rise/Fall Time	T <sub>r</sub> /T <sub>f</sub>			0.35	ns		
Enable Function		80% V <sub>cc</sub> min or N/C: Output active 0.5V max: Output disables to high-Z					Output Option G
		0.5V max or N/C: Output active 80% V <sub>cc</sub> min: Output disables to high-Z					Output Option M
Start up Time				10	ms		
Phase Jitter @ 622.08 MHz	φ <sub>J</sub>		0.25		ps RMS	Integrated 12 kHz – 20 MHz	
Environmental	Mechanical Shock	Per MIL-STD-202, Method 213, Condition C					
	Vibration	Per MIL-STD-202, Method 201 & 204					
	Max Soldering Conditions	See solder profile, Figure 1					
	Hermeticity	Per MIL-STD-202, Method 112 (1 x 10 <sup>-8</sup> atm cc/s of helium)					
	Solderability	Per MIL-STD-883, Method 203					

Note 1: Contact factory for standard frequency availability over 945 MHz.

Note 2: APR specification is inclusive of initial tolerance, deviation over temperature, shock, vibration, supply voltage, and aging for one year at 50°C mean ambient temperature.

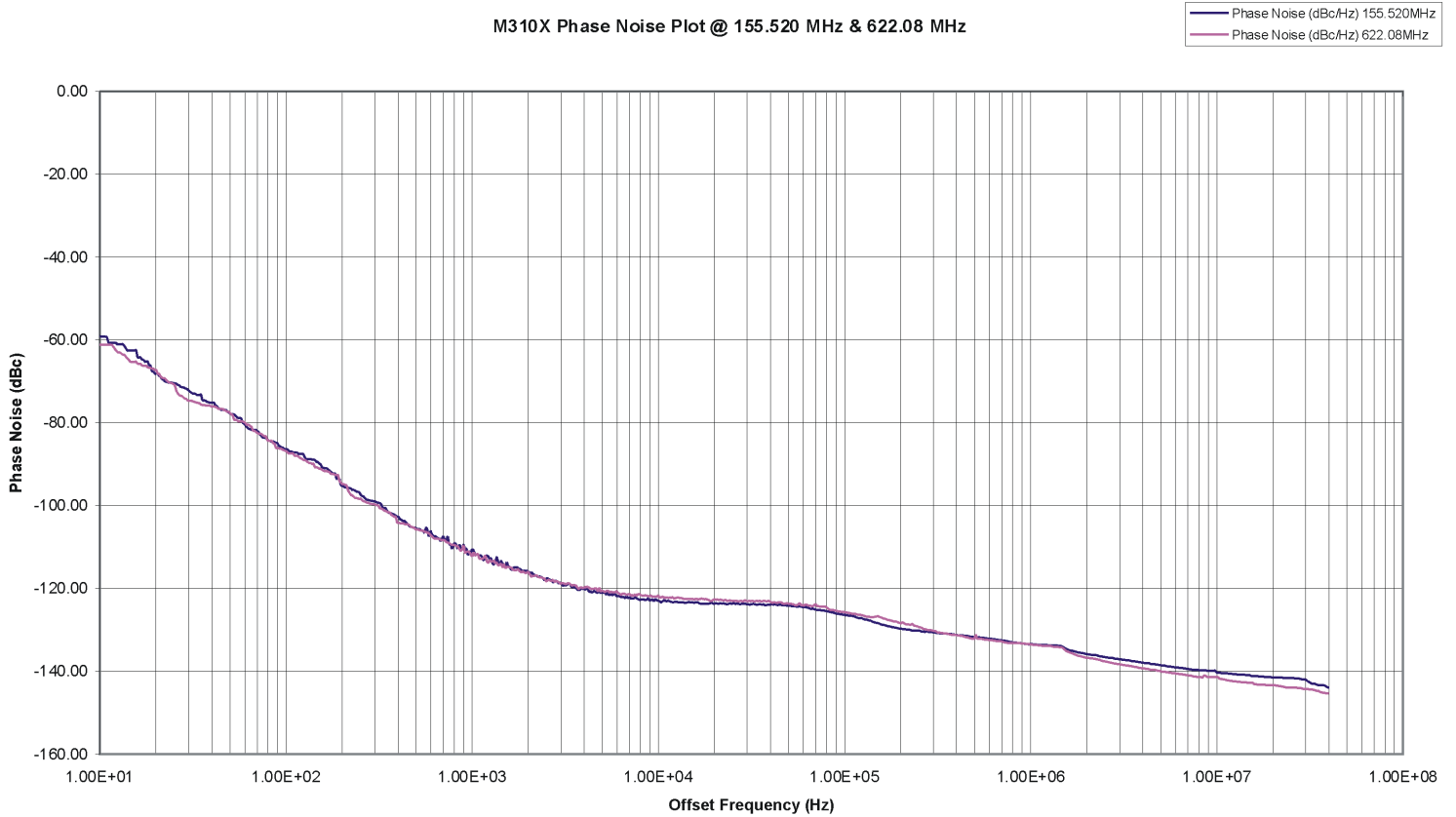
Note 3: See Load Circuit Diagram in this Datasheet. Consult factory with nonstandard output load requirements.

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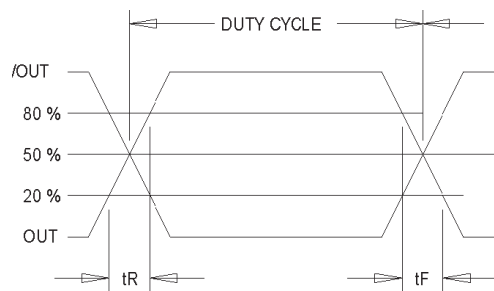
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# M310x Series LVPECL/LVDS/CML Voltage Controlled Crystal Oscillator – 3.3/2.5/1.8 Volt – 5x7 mm

## Phase Noise Plot



## Output Waveform



**Output Waveform: LVDS/CML/PECL**

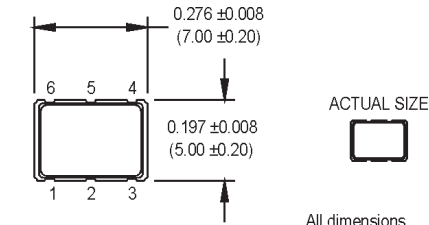
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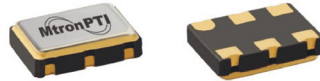
# M310x Series LVPECL/LVDS/CML Voltage Controlled Crystal Oscillator – 3.3/2.5/1.8 Volt – 5x7 mm

## Product Dimension & Pinout Information

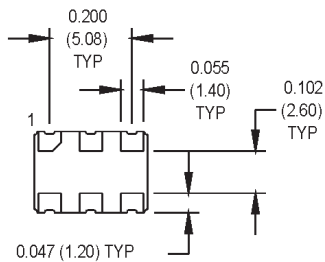
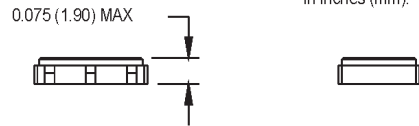
### 6 Pad Standard Option



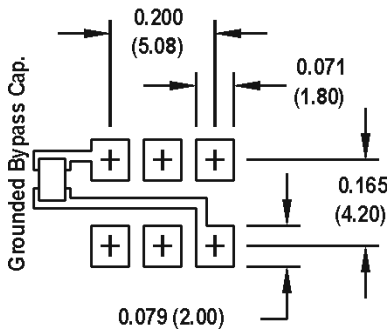
ACTUAL SIZE  
All dimensions  
in inches (mm).



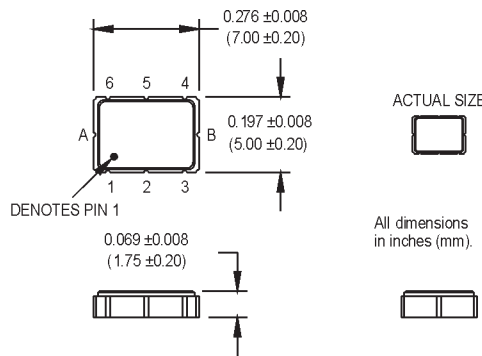
- Pad1: Voltage Control
- Pad2: Enable/Disable (or N/C)
- Pad3: Ground
- Pad4: Output Q (PECL, LVDS, CML)
- Pad5: Output  $\bar{Q}$  (PECL, LVDS, CML)
- Pad6: Vcc



#### SUGGESTED SOLDER PAD LAYOUT

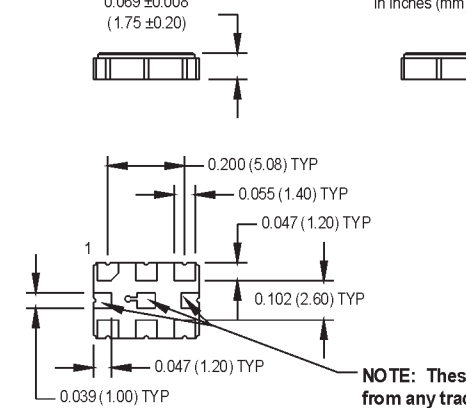


### 9 Pad Option



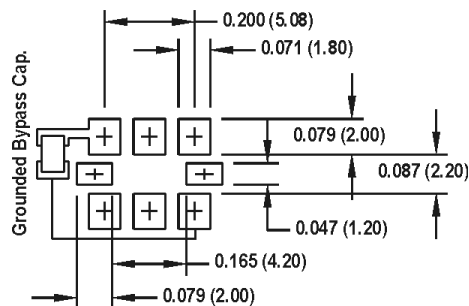
ACTUAL SIZE  
All dimensions  
in inches (mm).

- Pad1: Voltage Control
- Pad2: Enable/Disable (or N/C)
- Pad3: Ground
- Pad4: Output Q (PECL, LVDS, CML)
- Pad5: Output  $\bar{Q}$  (PECL, LVDS, CML)
- Pad6: Vcc
- PadA: Do not connect!
- PadB: Do not connect!
- PadC: Do not connect!



**NOTE:** These 3 pads must be isolated from any traces or vias appearing beneath this port.

#### SUGGESTED SOLDER PAD LAYOUT



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## M310x Series LVPECL/LVDS/CML Voltage Controlled Crystal Oscillator – 3.3/2.5/1.8 Volt – 5x7 mm

### Handling Information

Although protection circuitry has been designed into the M310x oscillator, proper precautions should be taken to avoid exposure to electrostatic discharge (ESD) during handling and mounting. MtronPTI utilizes a human-body model (HBM) and a charged-device model (CDM) for ESD-susceptibility testing and protection design evaluation. ESD voltage thresholds are dependent on the circuit parameters used to define the mode. Although no industry-wide standard has been adopted for the CDM, a standard HBM (resistance = 1500  $\Omega$ , capacitance = 100 pF) is widely used and therefore can be used for comparison purposes. The HBM ESD threshold presented here was obtained using these circuit parameters.

Model	ESD Threshold, Minimum	Unit
Human Body	1500*	V
Charged Device	1500*	V

\* MIL-STD-883D, Method 3015, Class 1



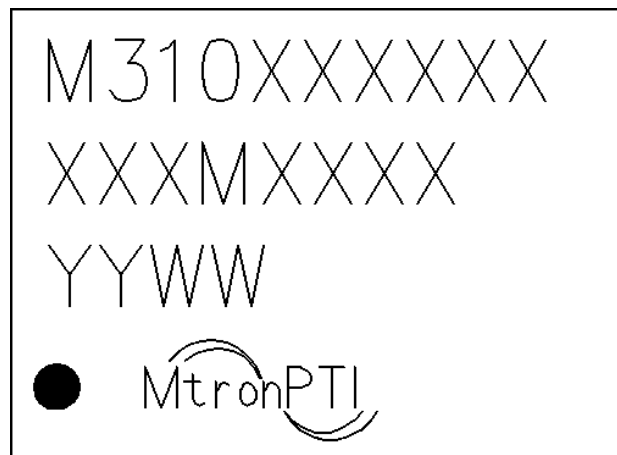
ATTENTION  
Static Sensitive  
Devices  
Handle only at  
Static Safe Work  
Stations

### Quality Parameters

Environmental Specifications/Qualification Testing Performed on the M310 VCXO		
Test	Test Method	Test Condition
Electrical Characteristics	Internal Specification	Per Specification
Frequency vs. Temperature	Internal Specification	Per Specification
Mechanical Shock	MIL-STD-202, Method 213, C	100 g's
Vibration	MIL-STD-202, Method 201-204	10 g's from 10-2000 Hz
Thermal Cycle	MIL-STD-883, Method 1010, B	-55 Deg. C to +125 Deg. C, 15 minute Dwell, 10 cycles
Aging	Internal Specification	168 Hours at 105 Degrees C
Gross Leak	MIL-STD-202, Method 112	30 Second Immersion
Fine Leak	MIL-STD-202, Method 112	Must meet $1 \times 10^{-5}$
Solderability	MIL-STD-883, Method 2003	8 Hour Steam Age – Must Exhibit 95% coverage
Resistance to Solvents	MIL-STD-883, Method 2015	Three 1 minute soaks
Terminal Pull	MIL-STD-883, Method 2004, A	2 Pounds
Lead Bend	MIL-STD-883, Method 2004, B1	1 Bending Cycle
Physical Dimensions	MIL-STD-883, Method 2016	Per Specification
Internal Visual	Internal Specification	Per Internal Specification

### Part Marking Guide

- Line 1: Model Number
- Line 2: Frequency
- Line 3: Date Code
- Line 4: Pin 1 Indicator / MtronPTI



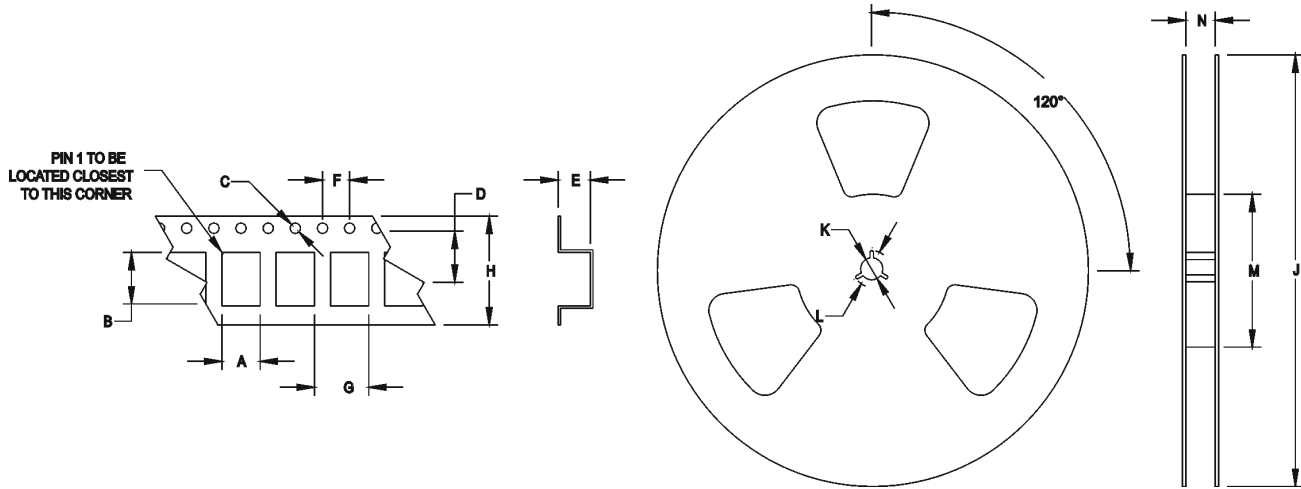
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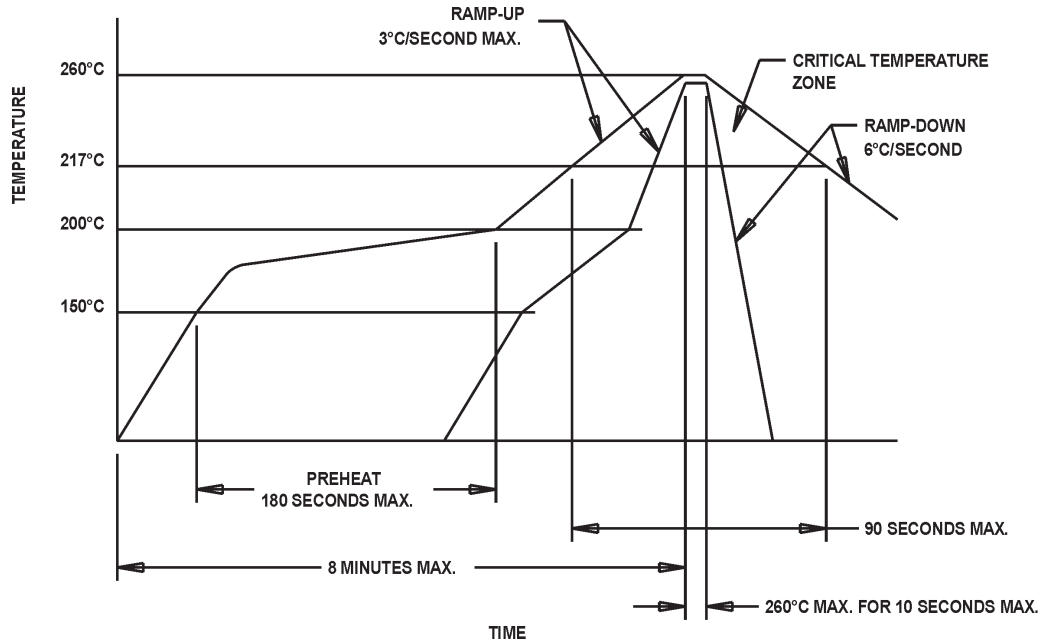
### Tape & Reel Specifications

(all measurements are in mm)	A	B	C	D	E	F	G	H	I	J	K	L
M310x	6.51	9.29	1.5	7.5	2.8	4	8/12	16	180-330	13	21	60-100



Standard Tape and Reel: 100 parts per reel

### Maximum Soldering Conditions



### Solder Conditions

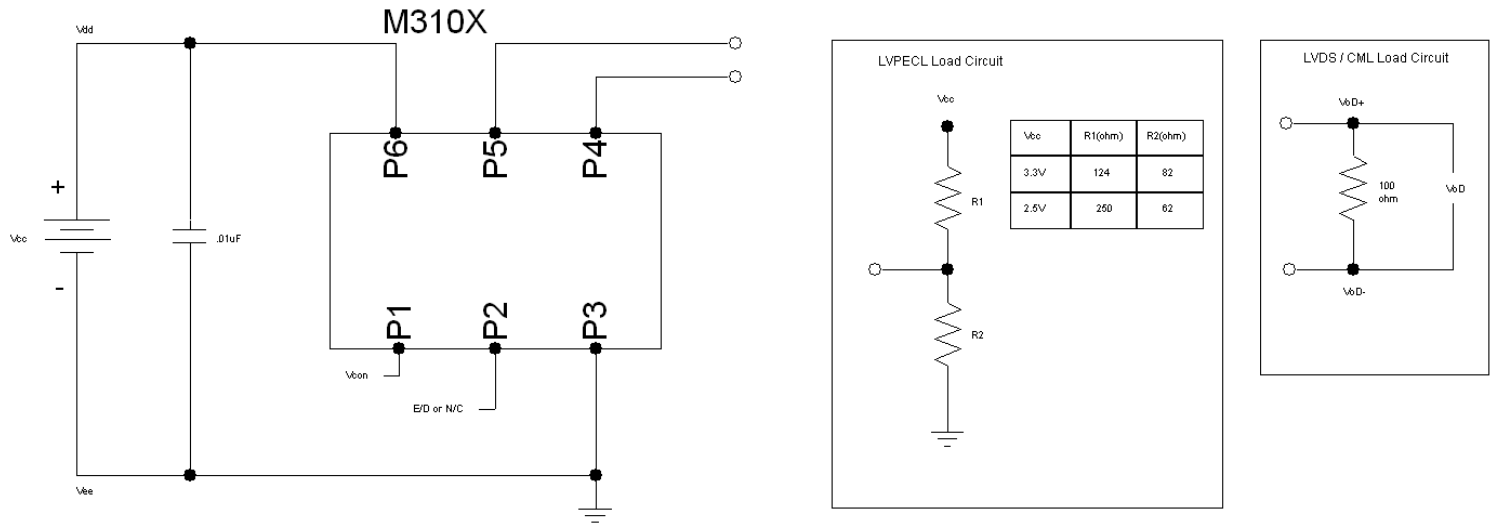
Note: Exceeding these limits may damage the device.

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## M310x Series LVPECL/LVDS/CML Voltage Controlled Crystal Oscillator – 3.3/2.5/1.8 Volt – 5x7 mm

### Typical Test Circuit & Load Circuit Diagrams



### Product Revision Table

Date	Revision	PCN Number	Details of Revision
7/20/07	A	10118	IC Revision to improve phase noise and electrical performance

For custom products or additional specifications contact our sales team at  
**800.762.8800 (toll free) or 605.665.9321**

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