

Marketing Bulletin

DATE: September 23rd, 2004
TO: Affected Customers
FROM: Mark Stoner
RE: Product Termination

To all concerned parties,

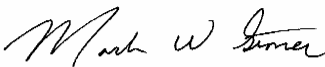
This bulletin is to notify all customers of the termination of the following Ecliptek series effective September 23rd, 2004:

Series	Description	Recommended Replacement
ELF11	5V 32.768kHz 14 pin DIP Oscillator	None

Because of the circumstances surrounding this termination, there will be no end-of-life policy exercised. The series will be terminated with no purchasing or lifetime buy window available. There will be no alternate product replacement recommended or available.

All of us at Ecliptek Corporation apologize for any inconvenience this may have caused and can assure you we are taking measures to insure this will not happen again in the future. If there are any questions pertaining to this bulletin, please feel free to contact me. Thank you for your cooperation.

Best Regards,



Mark W. Stoner
Director of Marketing
Ecliptek Corporation

STANDARD SPECIFICATIONS

Frequency Range:	32.768kHz
Frequency Tolerance/Stability:	(All Values Inclusive of Operating Temp. Range, Supply Voltage, and Load)
00	±100ppm Max.
45	±50ppm Max.
Operating Temperature Range	0°C to +70°C
Storage Temperature Range	-30°C to +80°C
Supply Voltage	5.0Vdc ±5%
Input Current	200µA Maximum (Measured at 5.5Vdc)
Output Voltage Logic High	2.4Vdc Min. w/TTL Load, V _{DD} -0.5Vdc Min. w/CMOS Load
Output Voltage Logic Low	0.4Vdc Max. w/TTL Load, 0.5Vdc Max. w/CMOS Load
Rise/Fall Time	1µSec (0.4Vdc to 2.4Vdc w/TTL Load, 20% to 80% of waveform w/CMOS Load)
Duty Cycle (at 25°C)	50% ±10% (@ 1.4Vdc w/10TTL Load or 50pF CMOS Load)
T	50% ±5% (@ 1.4Vdc thru 50MHz w/10TTL Load or 50pF HCMOS Load)
Load Drive Capability	1TTL Load or CMOS Load of 15pF Maximum
Aging @ 25°C	±5ppm/year

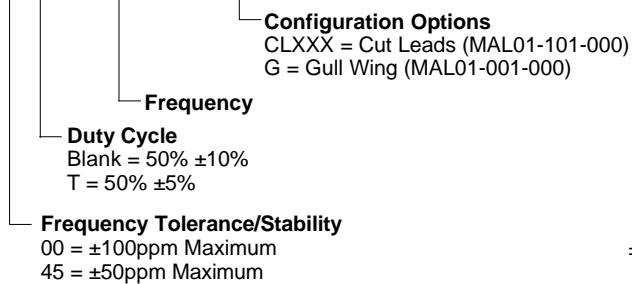
ENVIRONMENTAL & MECHANICAL

Fine Leak Test:	MIL-STD-883, Method 1014, Condition A	Solderability:	MIL-STD-883, Method 2002
Gross Leak Test:	MIL-STD-883, Method 1014, Condition C	Temperature Cycling:	MIL-STD-883, Method 1010
Mechanical Shock:	MIL-STD-202, Method 213, Condition C	Resistance to Soldering Heat:	MIL-STD-202, Method 210
Vibration:	MIL-STD-883, Method 2007, Condition A	Resistance to Solvents:	MIL-STD-202, Method 215
Lead Integrity:	MIL-STD-883, Method 2004		

OBSOLETE

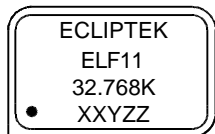
PART NUMBERING GUIDE

ELF11 00 T - 32.768K - CL175

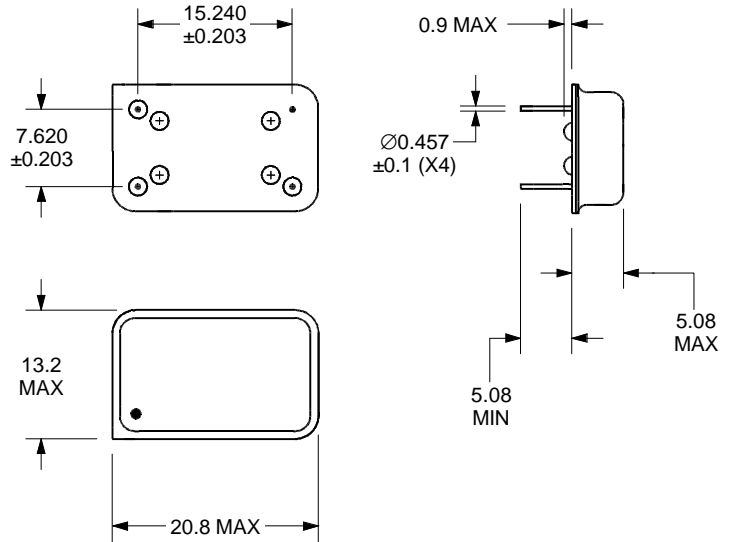


MARKING GUIDE

- (Line #1) **ECLIPTEK**
- (Line #2) **ELF11**
- (Line #3) **32.768K**
- (Line #4) **XX Y ZZ**
 - Week of Year
 - Last Digit of Year
 - Ecliptek Manufacturing Code (TEN02-001-000)



NOTE: Pin 1 shall be marked with a black dot.
 Marking shall conform to conditions listed in TQC41-001-000.



ALL DIMENSIONS
IN MILLIMETERS

PIN	CONNECTION
1	No Connect
7	Ground/Case Ground
8	Output
14	Supply Voltage

SPECIFICATION CONTROL DRAWING

		Drawing Number CSC01-030-000	
Title FULL SIZE LOW FREQUENCY CMOS OSCILLATOR			
Revision C		Effectivity Date 08-27-03	
ECN Number 8677		PAGE 1 OF 1	
Approved By	Date	Released By	Date