



MOTOROLA

MC54/74HC181

Product Preview

4-BIT ARITHMETIC LOGIC UNIT

The MC54/74HC181 is identical in pinout to the LS181. The device inputs are compatible with standard CMOS outputs; with pullup resistors, they are compatible with LSTTL outputs.

This device can perform all 16 logic operations and a variety of arithmetic operations on two 4-Bit words.

- Low Power Consumption Characteristic of CMOS Devices
- Output Drive Capability: 10 LSTTL Loads Minimum
- Operating Speeds Similar to LSTTL
- Wide Operating Voltage Range: 2 to 6 Volts
- Low Input Current: 1 μ A Maximum
- Low Quiescent Current: 80 μ A Maximum (74HC Series)
- High Noise Immunity Characteristic of CMOS Devices
- Diode Protection on All Inputs

HIGH-PERFORMANCE

CMOS

LOW-POWER COMPLEMENTARY MOS
SILICON-GATE

4-BIT ARITHMETIC LOGIC UNIT



J SUFFIX
CERAMIC PACKAGE
CASE 758



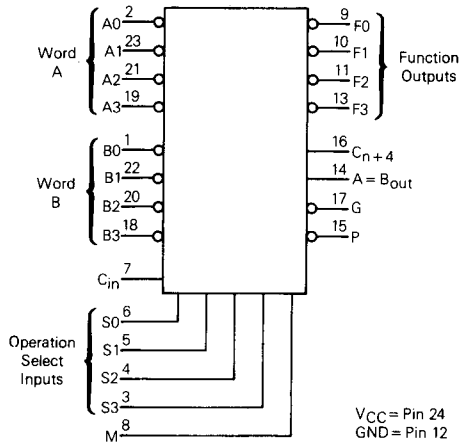
N SUFFIX
PLASTIC PACKAGE
CASE 724

ORDERING INFORMATION

54 Series: -55°C to +125°C
MC54HCXXXJ (Ceramic Package Only)

74 Series: -40°C to +85°C
MC74HCXXXN (Plastic Package)
MC74HCXXXJ (Ceramic Package)

BLOCK DIAGRAM



PIN ASSIGNMENT

