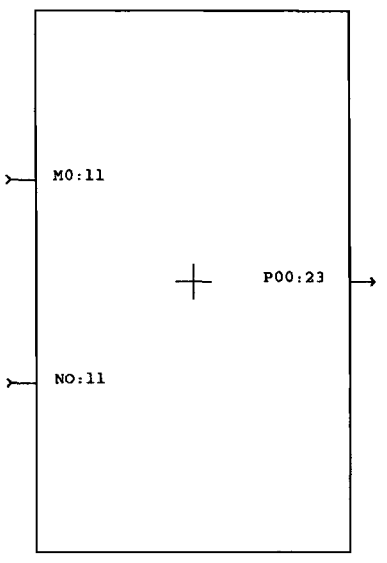
GFB2010A 12 X 12 2'S COMPLEMENT MULTIPLIER

GENERAL DESCRIPTION:

THE GFB2010A MEGAFUNCTION IS A 12 X 12 2'S COMPLEMENT MULTIPLIER WHICH GENERATES A 24-BIT PRODUCT. BY USING A MODIFIED BOOTH ALGORITHM, THIS MEGAFUNCTION GIVES A REASONABLE SPEED AND GATE COUNT.

PIN DIAGRAM:



- GATES USED = 1102
- AREA USED = 1343 GATE LOCATIONS
- LL7000 SERIES COMPATIBLE
- LSA2000 SERIES COMPATIBLE

INPUT LOADING: (LOADING IN TRANSISTOR PAIRS)

M0:11 = 3 N0:11 = 2

OUTPUT DRIVE: (DRIVE IN (#P,#N))

P00:23 = (2,2)

NDL SYNTAX:

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Z(P00,P01,P02,P03,P04,P05,P06,P07,P08,P09,P010,P011,P012,P013,P014,P015,P016,
  P017,P018,P019,P020,P021,P022,P023)

=GFB2010ABOOK (M0,M1,M2,M3,M4,M5,M6,M7,M8,M9,M10,M11,N0,N1,N2,N3,N4,N5,N6,N7,
  N8,N9,N10,N11)$
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AC CHARACTERISTICS*:

FROM	TO	5K TYPE. DELAY (NS)	7K TYP. DELAY (NS)
ANY M	ANY P	77	47
ANY N	ANY P	77	47

*ASSUMING OUTPUT LOADING OF 3