

**POWER MOSFET IN HERMETIC
ISOLATED TO257AB PACKAGE**
SM8F13* SM8F23***
SM8F33*** SM8F43*****

These devices offer the latest ruggedized MOSFET transistor die mounted in isolated and hermetically sealed metal packages. The standard MOSFET characteristics of very low on-state resistance and high transconductance are maintained. This product range features all of the proven advantages of MOSFET transistors such as excellent switching capability, low drive currents along with voltage control. SEMTECH power MOSFET's are ideally suited for applications such as switching power supplies, motor controls, choppers, audio amplifiers and high energy pulse circuits.

FEATURES

Fast Switching
Low Drive Current
Ease of Paralleling
Excellent Temperature Stability
Available with High Reliability Screening

**QUICK
REFERENCE DATA**

- $V_{DS} = 100V-500V$
- $I_D = 14A$
- $R_{DS(ON)} = 0.18\Omega$

ABSOLUTE MAXIMUM RATINGS (@ 25°C unless otherwise specified)

Parameter	Symbol	SM8F131	SM8F231	SM8F331	SM8F431	Units
Drain-Source Voltage	V_{DS}	100	200	400	500	V
Drain-Gate Voltage	V_{DGR}	100	200	400	500	V
Continuous Drain Current	$I_D @ T_C=25^\circ C$	14	9.0	5.5	4.5	A
Continuous Drain Current	$I_D @ T_C=100^\circ C$	9.0	6.0	3.5	3.0	A
Pulsed Drain Current (1)	I_{DM}	56	36	22	18	A
Max. Power Dissipation	$P_D @ T_C=25^\circ C$	50	50	50	50	W
Gate-Source Voltage	V_{GS}	20	20	20	20	V
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55 to 150	-55 to 150	-55 to 150	-55 to 150	°C

(1) Pulse Test: Pulsewidth = 300 μ s; Duty Cycle < 2%

SM8F13* SM8F23*****
SM8F33* SM8F43*****

**POWER MOSFET IN HERMETIC
 ISOLATED TO257AB PACKAGE**

ELECTRICAL CHARACTERISTICS (@ 25°C unless otherwise specified)

Symbol	Parameter	Device	Min.	Typ.	Max.	Units	Test Conditions
BV _{DSS}	Drain-Source Breakdown Voltage	SM8F431	500	-	-	V	V _{GS} =0V, I _D =1.0mA
		SM8F331	400	-	-		
		SM8F231	200	-	-		
		SM8F131	100	-	-		
V _{GS(th)}	Gate Threshold Voltage	all	2.0	-	4.0	V	V _D =V _{GS} , I _D =250µA
I _{GSS}	Gate-Source Leakage	all	-	-	100	nA	V _{GS} =20V
I _{DSS}	Zero Gate Voltage Drain Current	all	-	-	50	µA	V _D =0.8 x Rated V _D , V _{GS} =0V
R _{DS(on)}	Static Drain-Source On-State Resistance (1)	SM8F431	-	1.3	1.5	Ω	V _{GS} =10V, I _D =2.5A V _{GS} =10V, I _D =3.0A V _{GS} =10V, I _D =5.0A V _{GS} =10V, I _D =8.0A
		SM8F331	-	0.80	1.0		
		SM8F231	-	0.25	0.40		
		SM8F131	-	0.14	0.19		
g _{fs}	Forward Transconductance (1)	SM8F431	2.5	3.25	-	S	V _D =15V, I _D =2.5A V _D =15V, I _D =3.0A V _D =15V, I _D =5.0A V _D =15V, I _D =8.0A
		SM8F331	3.0	4.0	-		
		SM8F231	3.0	4.8	-		
		SM8F131	4.0	5.5	-		
C _{iss}	Input Capacitance	SM8F431	-	600	-	pF	V _{GS} =0V, V _D =25V, f=1.0MHz
		SM8F331	-	600	-		
		SM8F231	-	600	-		
		SM8F131	-	600	-		
C _{oss}	Output Capacitance	SM8F431	-	100	-	pF	V _{GS} =0V, V _D =25V, f=1.0MHz
		SM8F331	-	150	-		
		SM8F231	-	250	-		
		SM8F131	-	300	-		
C _{rss}	Reverse Transfer Capacitance	SM8F431	-	30	-	pF	V _{GS} =0V, V _D =25V, f=1.0MHz
		SM8F331	-	40	-		
		SM8F231	-	80	-		
		SM8F131	-	100	-		
t _{d(on)}	Turn-On Delay Time	SM8F431	-	18	30	nS	V _{DD} =0.5 V _D max., I _D =I _D max @ 25°C
		SM8F331	-	18	30		
		SM8F231	-	12	30		
		SM8F131	-	15	30		
t _r	Rise Time	SM8F431	-	20	30	nS	V _{DD} =0.5 V _D max., I _D =I _D max @ 25°C
		SM8F331	-	20	35		
		SM8F231	-	18	50		
		SM8F131	-	35	75		
t _{d(off)}	Turn-Off Delay Time	SM8F431	-	42	55	nS	V _{DD} =0.5 V _D max., I _D =I _D max @ 25°C
		SM8F331	-	40	55		
		SM8F231	-	35	50		
		SM8F131	-	38	40		
t _f	Fall Time	SM8F431	-	25	30	nS	V _{DD} =0.5 V _D max., I _D =I _D max @ 25°C
		SM8F331	-	25	35		
		SM8F231	-	27	40		
		SM8F131	-	23	45		
Q _{g(on)}	On State Gate Charge	SM8F431	-	22	30	nC	V _{GS} =10V, I _D =I _D max @ 25°C, V _D =0.8 max RATING
		SM8F331	-	12	30		
		SM8F231	-	11	30		
		SM8F131	-	10	30		
Q _{gs}	Gate-Source Charge	SM8F431	-	22	16	nC	V _{GS} =10V, I _D =I _D max @ 25°C, V _D =0.8 max RATING
		SM8F331	-	18	16		
		SM8F231	-	19	15		
		SM8F131	-	18	14		
Q _{gd}	Gate-Drain Charge	SM8F431	-	11	17	nC	V _{GS} =10V, I _D =I _D max @ 25°C, V _D =0.8 max RATING
		SM8F331	-	11	18		
		SM8F231	-	10	15		
		SM8F131	-	9	13		

10

**POWER MOSFET IN HERMETIC
ISOLATED TO257AB PACKAGE**

SM8F13* SM8F23***
SM8F33*** SM8F43*****

SOURCE-DRAIN DIODE RATINGS & CHARACTERISTICS

Symbol	Parameter	Device	Min.	Typ.	Max.	Units	Test Conditions
IS	Continuous Source Current (Body Diode)	SM8F431	-	-	4.5	A	
		SM8F331	-	-	5.5		
		SM8F231	-	-	9.0		
		SM8F131	-	-	14		
ISM	Pulsed Source Current (Body Diode)	SM8F431	-	-	16	A	
		SM8F331	-	-	20		
		SM8F231	-	-	36		
		SM8F131	-	-	56		
VSD	Diode Forward Voltage (1)	SM8F431	-	-	1.6	V	T _J =25°C, I _S =I _S max., V _{GS} =0V
		SM8F331	-	-	1.6		
		SM8F231	-	-	2.0		
		SM8F131	-	-	2.5		
trr	Reverse Recovery Time	SM8F431	-	370	760	ns	T _J =25°C, I _F =I _S max., di/dt=100 A/s
		SM8F331	-	300	660		
		SM8F231	-	170	400		
		SM8F131	-	120	250		
Qrr	Reverse Recovery Charge	SM8F431	-	2.0	4.3	μC	T _J =25°C, I _F =I _S max., di/dt=100 A/s
		SM8F331	-	2.0	4.3		
		SM8F231	-	1.3	2.7		
		SM8F131	-	0.58	1.3		

(1) Pulse Test: Pulsewidth = 300μs; Duty Cycle < 2%

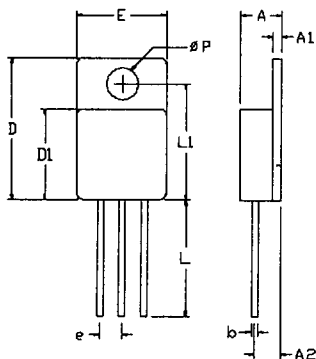
THERMAL RESISTANCE

Symbol	Parameter	Device	Max	Units
R _{θJC}	Junction to Case	All	2.5	°C/W
R _{θJA}	Junction to Ambient		80	°C/W

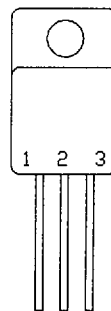
SM8F13* SM8F23*****
SM8F33* SM8F43*****

**POWER MOSFET IN HERMETIC
 ISOLATED TO257AB PACKAGE**

MECHANICAL OUTLINE AND CONFIGURATION



DIM.	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	4.83	5.33	.190	.210
A1	.635	0.89	.025	.035
A2	2.79	BSC	.110	BSC
b	.089	1.14	.035	.045
D	16.38	16.89	.645	.665
D1	10.41	10.92	.410	.430
e	2.54	BSC	.100	BSC
E	10.41	10.92	.410	.430
L	12.70	19.05	.500	.750
L1	13.21	13.72	.520	.540
P	3.56	3.81	.140	.150



STANDARD	ALTERNATE
1. Drain	1. Gate
2. Source	2. Drain
3. Gate	3. Source

ORDERING INFORMATION

The last three characters of the SEMTECH part numbering system identify the device pinout, lead bend configuration and the level of testing required. All devices are tested for hermeticity and compliance to the appropriate electrical characteristics.

Pinout

1- Standard pinout Drain Source Gate
 T- Alternate pinout Gate Drain Source

Lead Bend

S- Straight Leads
 D- 90° Bend Down (see fig 6 at the end of this section)
 C- Claw Bend Down (see fig 5 at the end of this section)

Screening

U- Unscreened
 T- Screening per MIL-S-19500 Table II
 F- Screening per MIL-S-19500/543 Table II

e.g SM8F431DF is a standard pinout SM8F431 with bent leads and screening to MIL-S-19500/543 Table II.

PACKAGE OUTLINE DRAWINGS

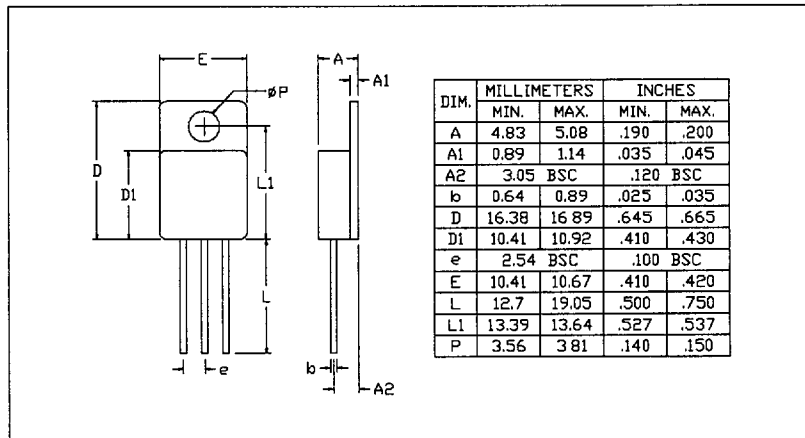


Fig.1 STRAIGHT LEAD TO257AA (S-SUFFIX)

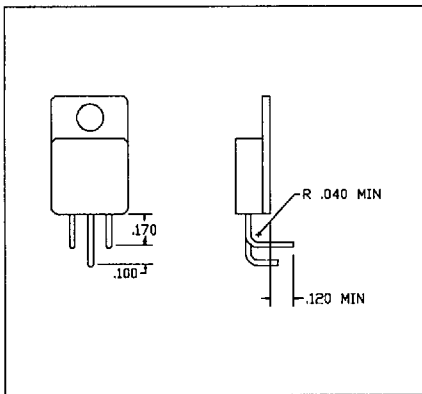


Fig.2 CLAW BEND TO257AA (C-SUFFIX)

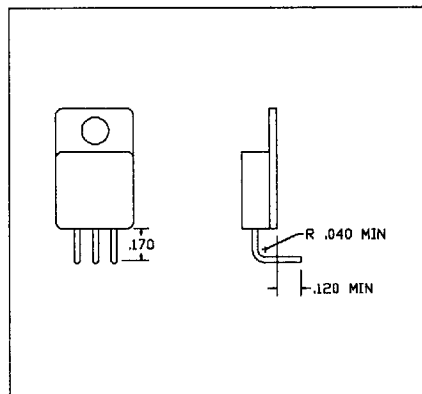


Fig.3 BENT DOWN LEAD TO257AA (D-SUFFIX)

PACKAGE OUTLINE DRAWINGS

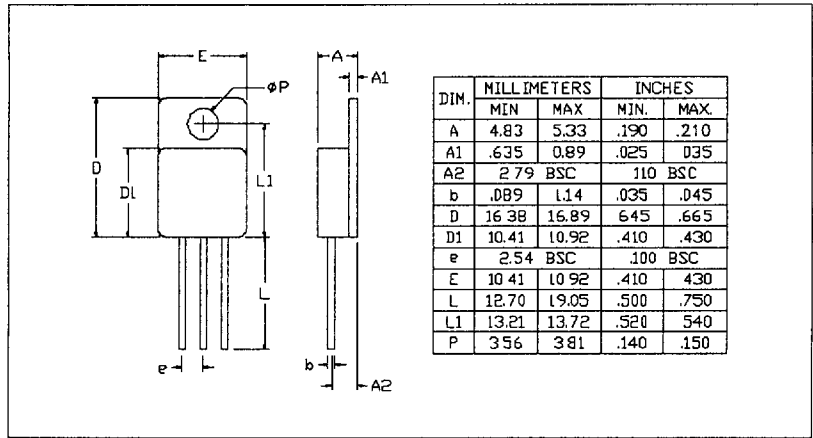


Fig.4 STRAIGHT LEAD TO257AB (S-SUFFIX)

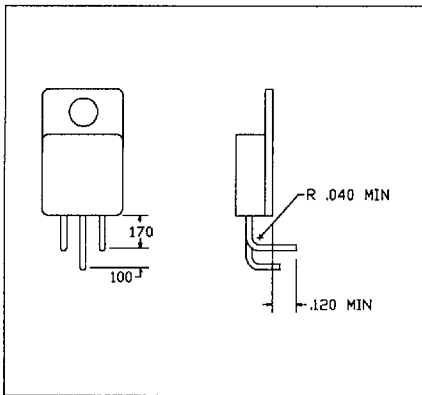


Fig.5 CLAW BEND TO257AB (C-SUFFIX)

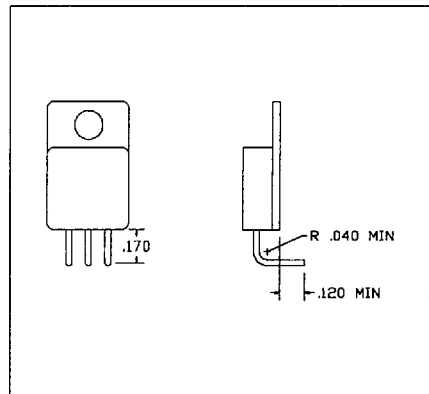


Fig.6 BENT DOWN LEAD TO257AB (D-SUFFIX)

PACKAGE OUTLINE DRAWINGS

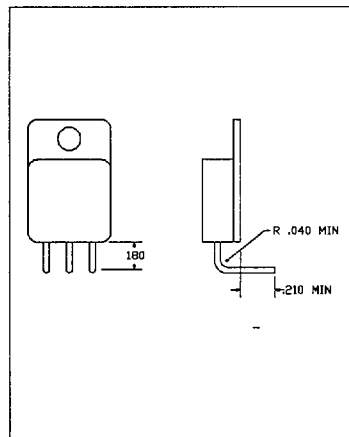
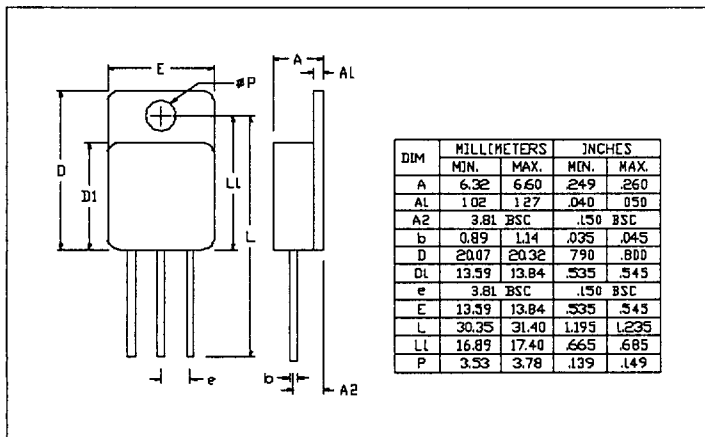


Fig.7 STRAIGHT LEAD TO254AA (S-SUFFIX)

Fig.8 BENT DOWN LEAD TO254AA (D-SUFFIX)

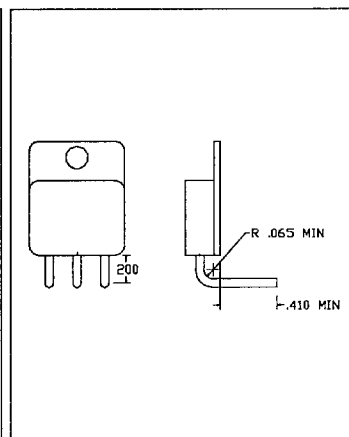
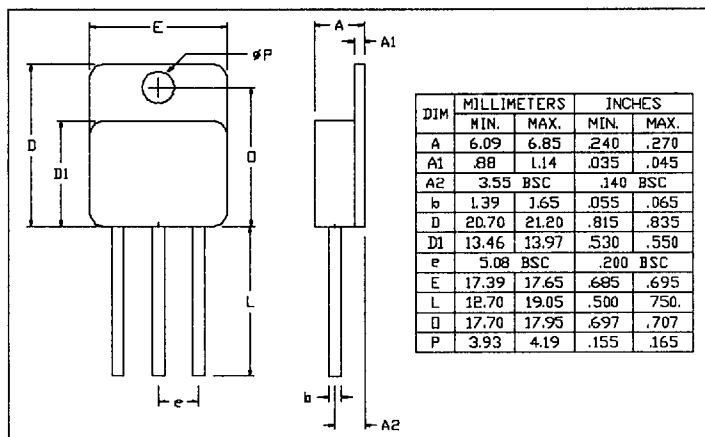


Fig.9 STRAIGHT LEAD TO258AA (S-SUFFIX)

Fig.10 BENT DOWN LEAD TO258AA (D-SUFFIX)

PACKAGE OUTLINE DRAWINGS

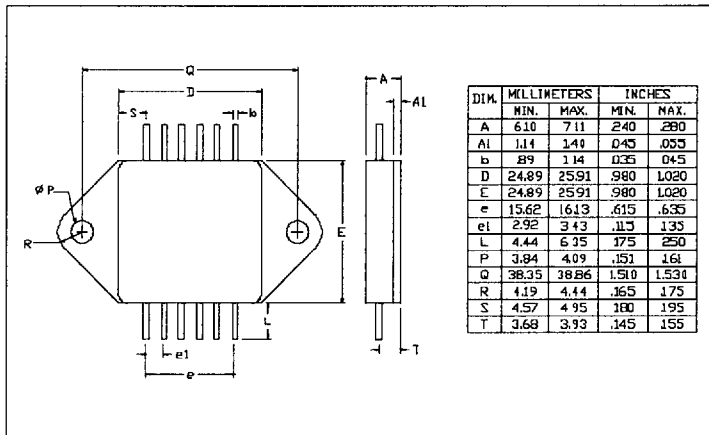
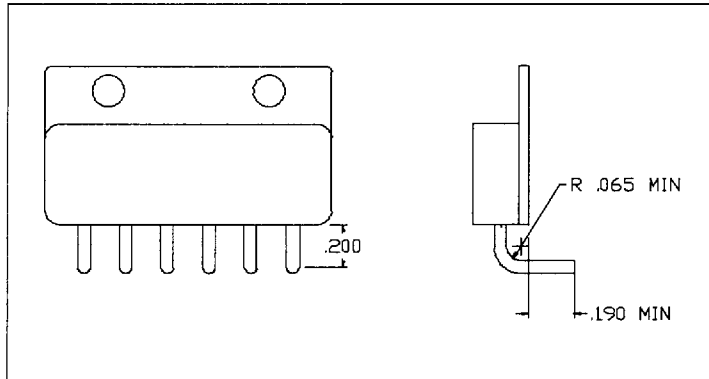
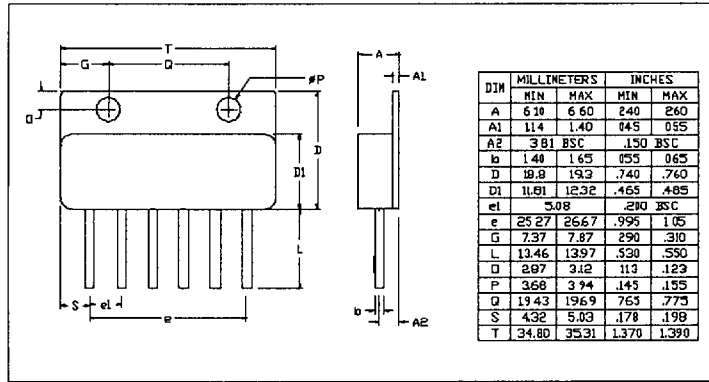


Fig.13 12 PIN "QUADPACK"