INTEGRATED CIRCUITS

DATA SHEET

PCA167x series 32 kHz watch circuits using a silver-oxide or a 3 V lithium battery

Product specification Supersedes data of September 1993 File under Integrated Circuits, IC16 1997 Apr 22





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PCA167x series

FEATURES

- 32 kHz oscillator, amplitude regulated with excellent frequency stability
- · High immunity of the oscillator to leakage currents
- · Very low current consumption; typically 150 nA
- · Stop function for accurate timing
- · Chopped motor pulses available
- · Power-on reset for fast testing
- Various test modes for testing the mechanical parts of the watch and the IC.

GENERAL DESCRIPTION

The PCA167x series devices are CMOS integrated circuits specially suited for battery-operated, quartz-crystal-controlled wrist-watches, with a bipolar stepping motor.

ORDERING INFORMATION

TYPE		PACKAGE ⁽¹⁾						
NUMBER	NAME	DESCRIPTION	VERSION					
PCA1672U	_	chip in tray	_					
PCA1673U	_	chip in tray	_					
PCA1675U	_	chip in tray	_					
PCA1676U/10	_	chip on foil	_					
PCA1677U	_	chip in tray	_					

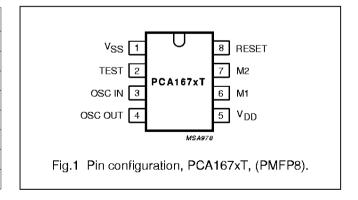
Note

1. Figure 1 and Chapter "Package outline" show details of standard package, available for large orders only. Chapter "Chip dimensions and bonding pad locations" shows exact pad locations for other delivery formats.

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PINNING

SYMBOL	PIN	DESCRIPTION
V _{SS}	1	ground (0 V)
TEST	2	test output
OSC IN	3	oscillator input
OSC OUT	4	oscillator output
V_{DD}	5	positive supply voltage
M1	6	motor 1 output
M2	7	motor 2 output
RESET	8	reset input



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FUNCTIONAL DESCRIPTION AND TESTING

Motor pulse

The motor output pulse widths (t_P) and the cycle times (t_T) are given in Chapter "Available types".

Power-on reset

For correct operation of the Power-on reset the rise time of V_{DD} from 0 V to 1.55 V should be less than 0.1 ms. All resettable flip-flops are reset. Additionally the polarity of the first motor pulse is positive: $V_{M1}-V_{M2} \geq 0 \ V.$

Customer testing and stop mode

An output frequency of 32 Hz is provided at RESET (pin 8) to be used for exact frequency measurement.

Connecting the RESET to V_{DD} stops the motor pulses leaving them in a HIGH impedance 3-state condition and a 32 Hz signal is produced at the TEST pin. A debounce circuit protects against accidental stoppages due to mechanical shock to the watch ($t_{DEB} = 14.7$ to 123.2 ms).

Connecting RESET to V_{SS} activates the test mode. The motor pulse period is 31.25 ms instead of t_T . Test and stop mode are disabled by disconnecting RESET (open-circuit).

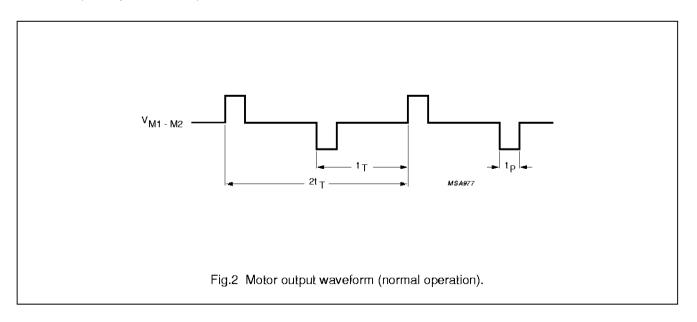
AVAILABLE TYPES

Refer to Fig.2 and to Chapters "Ordering information" and "Functional description and testing".

CHORT		DEDIOD	SPECIFICATIONS						
SHORT TYPE NUMBER	DELIVERY FORMAT ⁽¹⁾	PERIOD ^t t (s)	PULSE WIDTH t _P (ms)	DRIVE (%)	EEPROM	BATTERY EOL DETECTION	REMARKS		
1672	U	1	7.8	56	no	no	3 V Lithium		
1673	U	1	5.8	56	no	no	3 V Lithium		
1675	U	1/16	5.8	100	no	no	no oscillator		
1676	U/10	10	5.8	56	no	no	3 V Lithium		
1677	U	10	7.8	100	no	no	1.5 V		

Note

1. U = Chip in trays; U/10 = chip on foil.



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LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{DD}	supply voltage	V _{SS} = 0 V; note 1	-1.8	+6	V
VI	all input voltages		V _{SS}	V _{DD}	V
	output short-circuit duration indefinite				
T _{amb}	operating ambient temperature		-10	+60	°C
T _{stg}	storage temperature		-30	+100	°C

Note

1. Connecting the battery with reversed polarity does not destroy the circuit, but in this condition a large current flows, which will rapidly discharge the battery.

HANDLING

Inputs and outputs are protected against electrostatic discharges in normal handling. However, to be totally safe, it is advisable to take handling precautions appropriate to handling MOS devices. Advice can be found in "Data Handbook IC16, General, Handling MOS Devices".

CHARACTERISTICS

 $V_{DD}=1.55~V; V_{SS}=0~V; f_{osc}=32.768~kHz; T_{amb}=25~^{\circ}C; crystal; R_{S}=20~k\Omega; C_{1}=2~to~3~fF; C_{L}=8~to~10~pF; C_{0}=1~to~3~pF; unless otherwise specified.$

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
Supply		,			-1	
V_{DD}	supply voltage	$T_{amb} = -10 \text{ to } +60 ^{\circ}\text{C}$	1.2	1.5	3.5	V
ΔV_{DD}	supply voltage variation	transient; V _{DD} = 1.2 to 3.5 V	_	-	0.25	V
I _{DD1}	supply current	between motor pulses	_	150	250	nA
I _{DD2}	supply current	between motor pulses; V _{DD} = 3.5 V	_	200	350	nA
I _{DD3}	supply current	stop mode; pin 8 connected to V _{DD}	_	180	300	nA
I _{DD4}	supply current	stop mode; pin 8 connected to V_{DD} ; $V_{DD} = 3.5 \text{ V}$	-	300	480	nA

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SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
Motor outp	out				1	
V _{sat}	saturation voltage Σ (P + N)	$R_L = 2 \text{ k}\Omega$; $T_{amb} = -10 \text{ to } +60 \text{ °C}$	-	150	200	mV
R _{sc}	short-circuit resistance Σ (P + N)	I _{transistor} < 1 mA	_	200	300	Ω
t _T	cycle time			no	te 1	
tР	pulse width			no	te 2	
Oscillator						
V _{OSC ST}	starting voltage		1.2	_	_	V
g _m	transconductance	$V_{i(p-p)} = 50 \text{ mV}$	6	15	_	μS
t _{osc}	start-up time		_	1	_	s
Δf/f	frequency stability	$\Delta V_{DD} = 100 \text{ mV}$	_	0.05×10^{-6}	0.3×10^{-6}	
C _i	input capacitance		_	3	_	pF
Co	output capacitance		19	24	29	pF
Reset inpu	t					•
f _o	output frequency		_	32	_	Hz
ΔV _o	output voltage swing	$R = 1 M\Omega$; $C = 10 pF$	1.4	-	_	V
t _{edge}	edge time	$R = 1 \text{ M}\Omega$; $C = 10 \text{ pF}$	_	1	_	μs
I _{im}	peak input current	note 3	_	320	_	п A
I _{i(av)}	average input current		_	10	_	nA
Test mode						
t _{T1}	cycle time		_	31.25	_	ms
t _{DEB}	debounce time	RESET = V _{DD}	14.7	_	123.2	ms

Notes

- 1. Cycle time can be changed to one of the following values: 1, 5, 10, 12 or 20 s (see Chapter "Available types").
- 2. Pulse width can be varied from 2 ms to 15.7 ms in steps of 1 ms (see Chapter "Available types").
- 3. Duty factor is 1 : 32 and RESET = V_{DD} or V_{SS} .

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CHIP DIMENSIONS AND BONDING PAD LOCATIONS

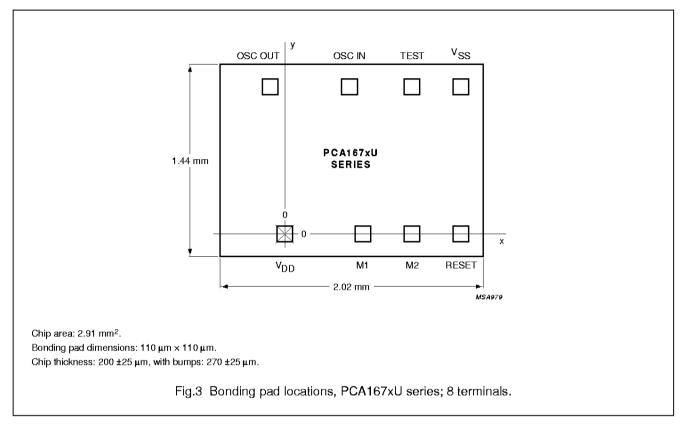


Table 1 Bonding pad locations (dimensions in μ m)

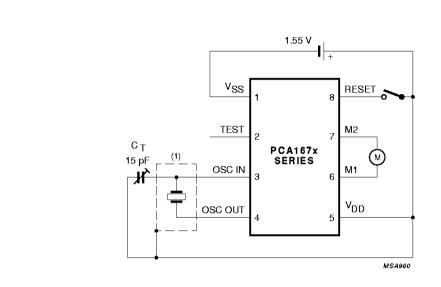
All x/y coordinates are referenced to the centre of pad (V_{DD}) , see Fig.3.

PAD	×	у
V _{SS}	1290	1100
TEST	940	1100
OSC IN	481	1100
OSC OUT	-102	1100
V_{DD}	0	0
M1	578	0
M2	936	0
RESET	1290	0
chip corner (max. value)	-497.5	-17 0

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APPLICATION INFORMATION



(1) Quartz crystal case should be connected to V_{DD} . Stray capacitance and leakage resistance from RESET, M1 or M2 to OSC IN should be less than 0.5 pF or larger than 20 $M\Omega$.

Fig.4 Typical application circuit diagram.

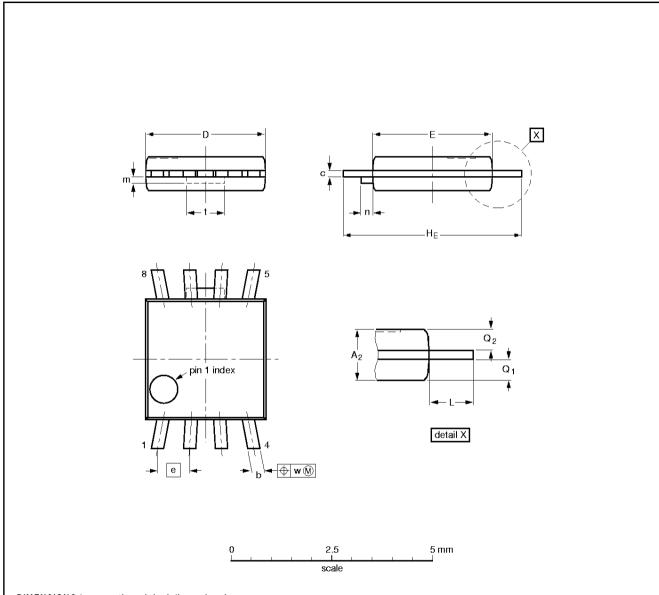
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PACKAGE OUTLINE

PMFP8: plastic micro flat package; 8 leads (straight)

SOT144-1



$\textbf{DIMENSIONS} \ (\textbf{mm} \ \textbf{are the original dimensions})$

UNIT	A ₂	b	С	D ⁽¹⁾	E ⁽¹⁾	e	H _E	L	m max.	n max.	Q ₁	Q ₂	t	w
mm	0.90 0.70	0.40 0.25	0.19 0.12	3. 1 2.9	3. 1 2.9	0.80	4.6 4.4	0.75	0.26	0.3	0.40 0.30	0.40 0.30	0.95	0.1

Note

1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.

OUTLINE		EUROPEAN	ISSUE DATE			
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
SOT144-1						-94 01 25 - 95-01-24

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SOLDERING

Introduction

There is no soldering method that is ideal for all IC packages. Wave soldering is often preferred when through-hole and surface mounted components are mixed on one printed-circuit board. However, wave soldering is not always suitable for surface mounted ICs, or for printed-circuits with high population densities. In these situations reflow soldering is often used.

This text gives a very brief insight to a complex technology. A more in-depth account of soldering ICs can be found in our "IC Package Databook" (order code 9398 652 90011).

Reflow soldering

Reflow soldering techniques are suitable for all SO packages.

Reflow soldering requires solder paste (a suspension of fine solder particles, flux and binding agent) to be applied to the printed-circuit board by screen printing, stencilling or pressure-syringe dispensing before package placement.

Several techniques exist for reflowing; for example, thermal conduction by heated belt. Dwell times vary between 50 and 300 seconds depending on heating method. Typical reflow temperatures range from 215 to 250 °C.

Preheating is necessary to dry the paste and evaporate the binding agent. Preheating duration: 45 minutes at 45 °C.

Wave soldering

Wave soldering techniques can be used for all SO packages if the following conditions are observed:

- A double-wave (a turbulent wave with high upward pressure followed by a smooth laminar wave) soldering technique should be used.
- The longitudinal axis of the package footprint must be parallel to the solder flow.
- The package footprint must incorporate solder thieves at the downstream end.

During placement and before soldering, the package must be fixed with a droplet of adhesive. The adhesive can be applied by screen printing, pin transfer or syringe dispensing. The package can be soldered after the adhesive is cured.

Maximum permissible solder temperature is 260 °C, and maximum duration of package immersion in solder is 10 seconds, if cooled to less than 150 °C within 6 seconds. Typical dwell time is 4 seconds at 250 °C.

A mildly-activated flux will eliminate the need for removal of corrosive residues in most applications.

Repairing soldered joints

Fix the component by first soldering two diagonally-opposite end leads. Use only a low voltage soldering iron (less than 24 V) applied to the flat part of the lead. Contact time must be limited to 10 seconds at up to 300 °C. When using a dedicated tool, all other leads can be soldered in one operation within 2 to 5 seconds between 270 and 320 °C.

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DEFINITIONS

Data sheet status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	

Limiting values

Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

Application information

Where application information is given, it is advisory and does not form part of the specification.

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NOTES

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Philips Semiconductors – a worldwide company

Argentina: see South America

Australia: 34 Waterloo Road, NORTH RYDE, NSW 2113.

Tel. +61 2 9805 4455. Fax. +61 2 9805 4466

Austria: Computerstr. 6, A-1101 WIEN, P.O. Box 213,

Tel. +43 1 60 101. Fax. +43 1 60 101 1210

Belarus: Hotel Minsk Business Center, Bld. 3, r. 1211, Volodarski Str. 6,

220050 MINSK, Tel. +375 172 200 733, Fax. +375 172 200 773

Beigium: see The Netherlands Brazil: see South America

Bulgaria: Philips Bulgaria Ltd., Energoproject, 15th floor,

51 James Bourchier Blvd., 1407 SOFIA Tel. +359 2 689 211, Fax. +359 2 689 102

Canada: PHILIPS SEMICONDUCTORS/COMPONENTS.

Tel. +1 800 234 7381

China/Hong Kong: 501 Hong Kong Industrial Technology Centre,

72 Tat Chee Avenue, Kowloon Tong, HONG KONG, Tel. +852 2319 7888, Fax. +852 2319 7700

Colombia: see South America

Czech Republic: see Austria

Denmark: Prags Boulevard 80, PB 1919, DK-2300 COPENHAGEN S.

Tel. +45 32 88 2636, Fax. +45 31 57 0044 Finland: Sinikalliontie 3, FIN-02630 ESPOO, Tel. +358 9 615800, Fax. +358 9 61580920

France: 4 Rue du Port-aux-Vins, BP317, 92156 SURESNES Cedex,

Tel. +33 1 40 99 6161, Fax. +33 1 40 99 6427

Germany: Hammerbrookstraße 69, D-20097 HAMBURG,

Tel. +49 40 23 53 60, Fax. +49 40 23 536 300

Greece: No. 15, 25th March Street, GR 17778 TAVROS/ATHENS,

Tel. +30 1 4894 339/239, Fax. +30 1 4814 240

Hungary: see Austria

India: Philips INDIA Ltd, Shivsagar Estate, A Block, Dr. Annie Besant Rd.

Worli, MUMBAI 400 018, Tel. +91 22 4938 541, Fax. +91 22 4938 722

Indonesia: see Singapore

Ireland: Newstead, Clonskeagh, DUBLIN 14. Tel. +353 1 7640 000, Fax. +353 1 7640 200

Israel: RAPAC Electronics, 7 Kehilat Saloniki St, PO Box 18053, TEL AVIV 61180, Tel. +972 3 645 0444, Fax. +972 3 649 1007

Italy: PHILIPS SEMICONDUCTORS, Piazza IV Novembre 3, 20124 MILANO, Tel. +39 2 6752 2531, Fax. +39 2 6752 2557

Japan: Philips Bldg 13-37, Kohnan 2-chome, Minato-ku, TOKYO 108,

Tel. +81 3 3740 5130, Fax. +81 3 3740 5077

Korea: Philips House, 260-199 Itaewon-dong, Yongsan-ku, SEOUL, Tel. +82 2 709 1412, Fax. +82 2 709 1415

Malaysia: No. 76 Jalan Universiti, 46200 PETALING JAYA, SELANGOR,

Tel. +60 3 750 5214, Fax. +60 3 757 4880

Mexico: 5900 Gateway East, Suite 200, EL PASO, TEXAS 79905, Tel. +9-5 800 234 7381

Middle East: see Italy

Netherlands: Postbus 90050, 5600 PB EINDHOVEN, Bldg. VB,

Tel. +31 40 27 82785, Fax. +31 40 27 88399

New Zealand: 2 Wagener Place, C.P.O. Box 1041, AUCKLAND, Tel. +64 9 849 4160. Fax. +64 9 849 7811

Norway: Box 1, Manglerud 0612, OSLO,

Tel. +47 22 74 8000, Fax. +47 22 74 8341

Philippines: Philips Semiconductors Philippines Inc., 106 Valero St. Salcedo Village, P.O. Box 2108 MCC, MAKATI,

Metro MANILA, Tel. +63 2 816 6380, Fax. +63 2 817 3474

Poland: Ul. Lukiska 10, PL 04-123 WARSZAWA, Tel. +48 22 612 2831, Fax. +48 22 612 2327

Portugal: see Spain Romania: see Italy

Russia: Philips Russia, Ul. Usatcheva 35A, 119048 MOSCOW,

Tel. +7 095 755 6918, Fax. +7 095 755 6919

Singapore: Lorong 1, Toa Payoh, SINGAPORE 1231,

Tel. +65 350 2538, Fax. +65 251 6500

Slovakia: see Austria Slovenia: see Italy

South Africa: S.A. PHILIPS Pty Ltd., 195-215 Main Road Martindale,

2092 JOHANNESBURG, P.O. Box 7430 Johannesburg 2000.

Tel. +27 11 470 5911, Fax. +27 11 470 5494

South America: Rua do Rocio 220, 5th floor, Suite 51, 04552-903 São Paulo, SÃO PAULO - SP, Brazil, Tel. +55 11 821 2333, Fax. +55 11 829 1849

Spain: Balmes 22, 08007 BARCELONA, Tel. +34 3 301 6312, Fax. +34 3 301 4107

Sweden: Kottbygatan 7, Akalla, S-16485 STOCKHOLM,

Tel. +46 8 632 2000, Fax. +46 8 632 2745

Switzerland: Allmendstrasse 140, CH-8027 ZÜRICH,

Tel. +41 1 488 2686, Fax. +41 1 481 7730

Talwan: Philips Semiconductors, 6F, No. 96, Chien Kuo N. Rd., Sec. 1,

TAIPEI, Taiwan Tel. +886 2 2134 2865, Fax. +886 2 2134 2874

Thailand: PHILIPS ELECTRONICS (THAILAND) Ltd.

209/2 Sanpavuth-Bangna Road Prakanong, BANGKOK 10260,

Tel. +66 2 745 4090, Fax. +66 2 398 0793

Turkey: Talatpasa Cad. No. 5, 80640 GÜLTEPE/ISTANBUL,

Tel. +90 212 279 2770, Fax. +90 212 282 6707

Ukraine: PHILIPS UKRAINE, 4 Patrice Lumumba str., Building B, Floor 7,

252042 KIEV, Tel. +380 44 264 2776, Fax. +380 44 268 0461

United Kingdom: Philips Semiconductors Ltd., 276 Bath Road, Hayes, MIDDLESEX UB3 5BX, Tel. +44 181 730 5000, Fax. +44 181 754 8421

United States: 811 East Argues Avenue, SUNNYVALE, CA 94088-3409,

Tel. +1 800 234 7381 Uruguay: see South America

Vietnam: see Singapore

Yugoslavia: PHILIPS, Trg N. Pasica 5/v, 11000 BEOGRAD,

Tel. +381 11 625 344, Fax.+381 11 635 777

For all other countries apply to: Philips Semiconductors, Marketing & Sales Communications, Building BE-p, P.O. Box 218, 5600 MD EINDHOVEN, The Netherlands, Fax. +31 40 27 24825

Internet: http://www.semiconductors.philips.com

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