

Basic Type	Description	Max. Power Supply Current (mA)	MAJOR ¹ AC CHARACTERISTICS		Temp ² Range	No. of Pins	Available Packages ³						
			Max. Clock Freq (MHz)	Max. Prop Delay (ns)			DC	PC	DM	DMB	FM	FMB	
✓Am2901	4-bit microprocessor slice	280	9.5	—	C	40	X						
		280	8.3	—	M	40			X	X			
✓2901A	High speed 4-bit slice	280 ⁴	25	—	C	40	X	*		X	X		
		280	15	—	M	40			X	X		*	*
							42						*
✓Am2902	Look-ahead carry generator	94	—	14 (C _n → C _{ntj})	C	16	X	*					
		93	—	14 (C _n → C _{ntj})	M	16			X	X	*	*	
✓Am2905	Quad two-input bus transceiver with three-state receiver	105	—	36 (DRCP → Bus)	C	24	X	*					
		105	—	40 (DRCP → Bus)	M	24			X	X	*	*	
Am2906	Quad two-input bus transceiver with parity	105	—	36 (DRCP → Bus)	C	24	X	*					
		105	—	40 (DRCP → Bus)	M	24			X	X	*	*	
Am2907	Quad bus transceiver with three-state receiver with parity	110	—	36 (DRCP → Bus)	C	20	X	*					
		110	—	40 (DRCP → Bus)	M	20			X	X	*	*	
Am2909	Microprogram sequencer	130	12.5	70 (CLK → Y ₁)	C	28	X	*					
		130	12.5	80 (CLK → Y ₁)	M	28			X	X	*	*	
Am2911	Microprogram sequencer	130	12.5	70 (CLK → Y ₁)	C	20	X	*					
		130	12.5	80 (CLK → Y ₁)	M	20			X	X	*	*	
*Am2915	Quad three-state bus transceiver with interface logic	*90	—	*32 (DRCP → Bus)	C	24	*	*					
		*90	—	*36 (DRCP → Bus)	M	24			*	*	*	*	
*Am2916	Quad three-state bus transceiver with interface logic	*110	—	*32 (DRCP → Bus)	C	24	*	*					
		*110	—	*36 (DRCP → Bus)	M	24			*	*	*	*	
*Am2917	Quad three-state bus transceiver with interface logic	*95	—	*32 (DRCP → Bus)	C	20	*	*					
		*95	—	*36 (DRCP → Bus)	M	20			*	*	*	*	
Am2918	Quad D register with standard and three-state outputs	120	75	—	C	16	X	*					
		120	75	—	M	16			X	X	*	*	

*Planned