

T-58-11-13

ECG[®] Semiconductors

ECG1903, ECG1905, ECG1907, ECG1909, ECG1917

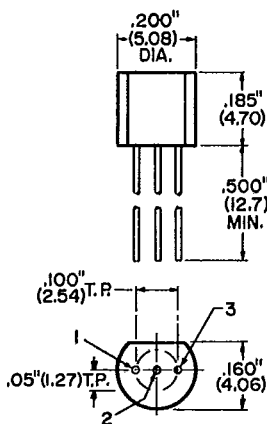
3-Terminal, Negative VR

Features

- No external components required
- Internal short-circuit current limiting
- Internal thermal overload protection

This series of 3-terminal negative voltage regulators features thermal shutdown and current limiting, making them remarkably rugged. In most applications, no external components are required for operation.

These devices are useful for on-card regulation or any other application where a regulated negative voltage at a modest current level is needed. These regulators offer substantial advantage over the common resistor/zener diode approach.

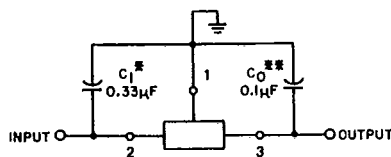
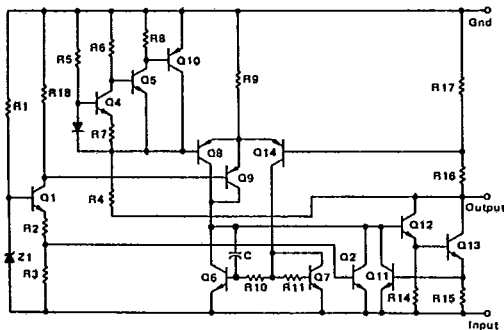


Pin 1. Ground
Pin 2. Input
Pin 3. Output

Absolute Maximum Ratings (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Rating	Unit
Input Voltage (-5, -12, -15, -18 V) (-24 V)	V _I	-35 -40	Vdc
Storage Temperature Range	T _{stg}	-55 to +150	°C
Junction Temperature Range	T _J	0 to +125	°C

Circuit Schematic



A common ground is required between the input and the output voltages. The input voltage must remain typically 2.0 V above the output voltage even during the low point on the input ripple voltage.

* = C₁ is required if regulator is located an appreciable distance from power supply filter.

** = C₀ improve stability and transient response.

ECG1903 Electrical Characteristics ($V_I = -19\text{ V}$, $I_O = 40\text{ mA}$, $C_I = 0.33\text{ }\mu\text{F}$, $C_O = 0.1\text{ }\mu\text{F}$, $T_A = +25^\circ\text{C}$)

Characteristics	Symbol	Min	Typ	Max	Unit
Output Voltage	V_O	-11.5	-12	-12.5	Vdc
Input Regulation - 14.5 Vdc $\geq V_I \geq -27\text{ Vdc}$ - 16 Vdc $\geq V_I \geq -27\text{ Vdc}$	Reg _{line}	--	--	250 200	mV
Load Regulation 1.0 mA $\leq I_O \leq 100\text{ mA}$ 1.0 mA $\leq I_O \leq 40\text{ mA}$	Reg _{load}	--	--	100 50	mV
Output Voltage - 14.5 Vdc $\geq V_I \geq -27\text{ Vdc}$, 1.0 mA $\leq I_O \leq 40\text{ mA}$ $V_I = -19\text{ Vdc}$, 1.0 mA $\leq I_O \leq 70\text{ mA}$	V_O	-11.4 -11.4	--	-12.6 -12.6	Vdc
Input Bias Current	I_{IB}	--	--	6.5	mA
Input Bias Current Change - 16 Vdc $\geq V_I \geq -27\text{ Vdc}$ 1.0 mA $\leq I_O \leq 40\text{ mA}$	ΔI_{IB}	--	--	1.5 0.1	mA
Output Noise Voltage 10 Hz $\leq f \leq 100\text{ kHz}$	V_N	--	80	--	μV
Ripple Rejection - 15 $\leq V_I \leq -25\text{ Vdc}$, $f = 120\text{ Hz}$	RR	37	42	--	dB
Input-Output Voltage Differential $I_O = 40\text{ mA}$	$ V_I - V_O $	--	1.7	--	Vdc

T-58-11-13

ECG1905 Electrical Characteristics ($V_I = -23\text{ V}$, $I_O = 40\text{ mA}$, $C_I = 0.33\text{ }\mu\text{F}$, $C_O = 0.1\text{ }\mu\text{F}$, $T_A = +25^\circ\text{C}$)

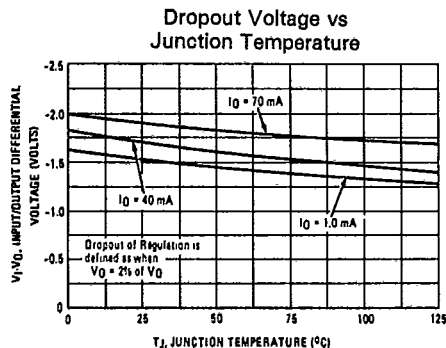
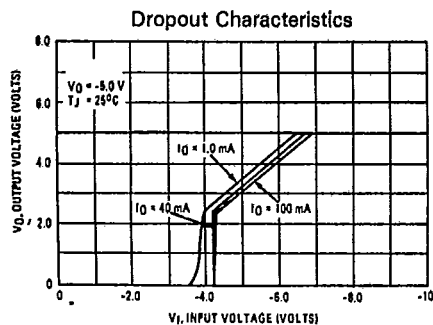
Characteristics	Symbol	Min	Typ	Max	Unit
Output Voltage	V_O	-14.4	-15	-15.6	Vdc
Input Regulation - 17.5 Vdc $\geq V_I \geq -30\text{ Vdc}$ - 20 Vdc $\geq V_I \geq -30\text{ Vdc}$	Reg _{line}	--	--	300 250	mV
Load Regulation 1.0 mA $\leq I_O \leq 100\text{ mA}$ 1.0 mA $\leq I_O \leq 40\text{ mA}$	Reg _{load}	--	--	150 75	mV
Output Voltage - 17.5 Vdc $\geq V_I \geq -30\text{ Vdc}$, 1.0 mA $\leq I_O \leq 40\text{ mA}$ $V_I = -23\text{ Vdc}$, 1.0 mA $\leq I_O \leq 70\text{ mA}$	V_O	-14.25 -14.25	--	-15.75 -15.75	Vdc
Input Bias Current	I_{IB}	--	--	6.5	mA
Input Bias Current Change - 20 Vdc $\geq V_I \geq -30\text{ Vdc}$ 1.0 mA $\leq I_O \leq 40\text{ mA}$	ΔI_{IB}	--	--	1.5 0.1	mA
Output Noise Voltage 10 Hz $\leq f \leq 100\text{ kHz}$	V_N	--	90	--	μV
Ripple Rejection - 18.5 $\leq V_I \leq -28.5\text{ Vdc}$, $f = 120\text{ Hz}$	RR	34	39	--	dB
Input-Output Voltage Differential $I_O = 40\text{ mA}$	$ V_I - V_O $	--	1.7	--	Vdc

ECG1907 Electrical Characteristics ($V_I = -27\text{ V}$, $I_O = 40\text{ mA}$, $C_I = 0.33\text{ }\mu\text{F}$, $C_O = 0.1\text{ }\mu\text{F}$, $T_A = +25^\circ\text{C}$)

T-58-11-13

Characteristics	Symbol	Min	Typ	Max	Unit
Output Voltage	V_O	-17.3	-18	-18.7	Vdc
Input Regulation -20.7 Vdc $\geq V_I \geq$ -33 Vdc -21.4 Vdc $\geq V_I \geq$ -33 Vdc -22 Vdc $\geq V_I \geq$ -33 Vdc -21 Vdc $\geq V_I \geq$ -33 Vdc	Reg _{line}	--	--	325	mV
Load Regulation 1.0 mA $\leq I_O \leq$ 100 mA 1.0 mA $\leq I_O \leq$ 40 mA	Reg _{load}	--	--	170 85	mV
Output Voltage -20.7 Vdc $\geq V_I \geq$ -33 Vdc, 1.0 mA $\leq I_O \leq$ 40 mA -21.4 Vdc $\geq V_I \geq$ -33 Vdc, 1.0 mA $\leq I_O \leq$ 40 mA $V_I = -27\text{ Vdc}$, 1.0 mA $\leq I_O \leq$ 70 mA	V_O	-17.1	--	-18.9	Vdc
Input Bias Current	I_{IB}	--	--	6.5	mA
Input Bias Current Change -21 Vdc $\geq V_I \geq$ -33 Vdc -27 Vdc $\geq V_I \geq$ -33 Vdc 1.0 mA $\leq I_O \leq$ 40 mA	ΔI_{IB}	--	--	1.5 0.1	mA
Output Noise Voltage 10 Hz $\leq f \leq$ 100 kHz	V_N	--	150	--	μV
Ripple Rejection -23 $\leq V_I \leq$ -33 Vdc, $f = 120\text{ Hz}$	RR	33	48	--	dB
Input-Output Voltage Differential $I_O = 40\text{ mA}$	$ V_I - V_O $	--	1.7	--	Vdc

Typical Characteristics ($V_O = -5.0$, $T_A = 25^\circ\text{C}$ unless otherwise noted)

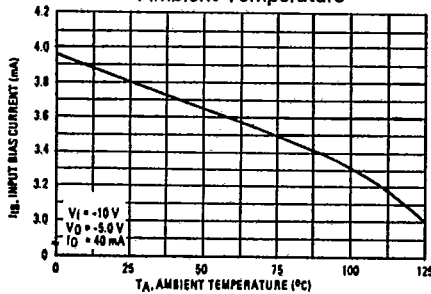


ECG1909 Electrical Characteristics ($V_I = -33$ V, $I_O = 40$ mA, $C_I = 0.33$ μ F, $C_O = 0.1$ μ F, $T_A = +25^\circ$ C)

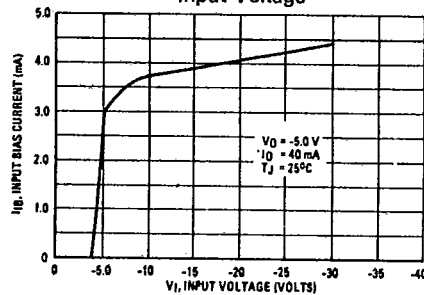
Characteristics	Symbol	Min	Typ	Max	Unit
Output Voltage	V_O	-23	-24	-25	Vdc
Input Regulation -27 Vdc $\geq V_I \geq$ -38 Vdc -27.5 Vdc $\geq V_I \geq$ -38 Vdc -28 Vdc $\geq V_I \geq$ -38 Vdc	Regline	--	--	350	mV
Load Regulation 1.0 mA $\leq I_O \leq$ 100 mA 1.0 mA $\leq I_O \leq$ 40 mA	Regload	--	--	200 100	mV
Output Voltage -27 Vdc $\geq V_I \geq$ -38 Vdc, 1.0 mA $\leq I_O \leq$ 40 mA -28 Vdc $\geq V_I \geq$ -38 Vdc, 1.0 mA $\leq I_O \leq$ 40 mA $V_I = -33$ Vdc, 1.0 mA $\leq I_O \leq$ 70 mA	V_O	-22.8 -22.8	-- --	-25.2 -25.2	Vdc
Input Bias Current	I_{IB}	--	--	6.5	mA
Input Bias Current Change -28 Vdc $\geq V_I \geq$ -38 Vdc 1.0 mA $\leq I_O \leq$ 40 mA	ΔI_{IB}	--	--	1.5 0.1	mA
Output Noise Voltage 10 Hz $\leq f \leq$ 100 kHz	V_N	--	200	--	μ V
Ripple Rejection -29 $\leq V_I \leq$ -35 Vdc, $f = 120$ Hz	RR	31	47	--	dB
Input-Output Voltage Differential $I_O = 40$ mA	$ V_I - V_O $	--	1.7	--	Vdc

T-58-11-13

Input Bias Current vs Ambient Temperature



Input Bias Current vs Input Voltage

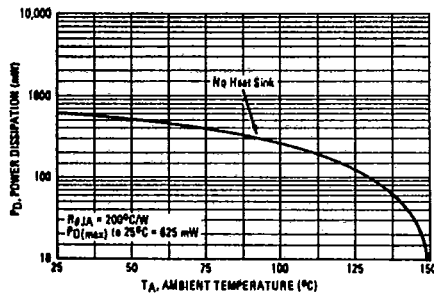


ECG1917 Electrical Characteristics ($V_I = -10$ V, $I_O = 40$ mA, $C_I = 0.33$ μ F, $C_O = 0.1$ μ F, $T_A = +25^\circ$ C)

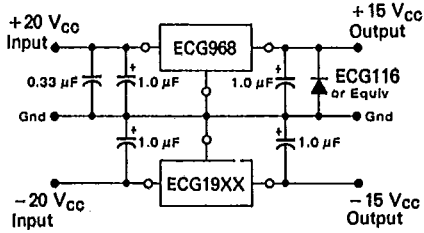
T-58-11-13

Characteristics	Symbol	Min	Typ	Max	Unit
Output Voltage	V_O	-4.8	-5.0	-5.2	Vdc
Input Regulation -7.0 Vdc $\geq V_I \geq -20$ Vdc -8.0 Vdc $\geq V_I \geq -20$ Vdc	Reg _{line}	--	--	150 100	mV
Load Regulation 1.0 mA $\leq I_O \leq 100$ mA 1.0 mA $\leq I_O \leq 40$ mA	Reg _{load}	--	--	60 30	mV
Output Voltage -7.0 Vdc $\geq V_I \geq -20$ Vdc, 1.0 mA $\leq I_O \leq 40$ mA $V_I = -10$ Vdc, 1.0 mA $\leq I_O \leq 70$ mA	V_O	-4.75 -4.75	--	-5.25 -5.25	Vdc
Input Bias Current	I_{IB}	--	--	6.0	mA
Input Bias Current Change -8.0 Vdc $\geq V_I \geq -20$ Vdc 1.0 mA $\leq I_O \leq 40$ mA	ΔI_{IB}	--	--	1.5 0.1	mA
Output Noise Voltage ($T_A = +25^\circ$ C, 10 Hz $\leq f \leq 100$ kHz)	V_N	--	40	--	μ V
Ripple Rejection (-8.0 $\geq V_I \geq 18$ Vdc, $f = 120$ kHz)	RR	41	49	--	dB
Input-Output Voltage Differential $I_O = 40$ mA	$ V_I - V_O $	--	1.7	--	Vdc

Maximum Average Power Dissipation vs Ambient Temperature

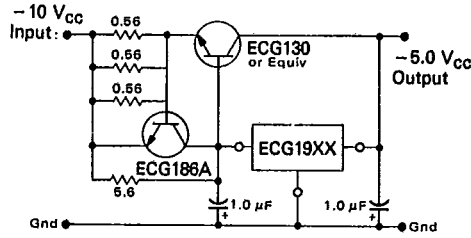


Operational Amplifier Supply
(±15 V @ 1.0 A)



The ECG968 and ECG19XX positive and negative regulators may be connected as shown to obtain a dual power supply for operational amplifiers. A clamp diode should be used at the output of the ECG968 to prevent potential latch-up problems.

Current Boost Regulator
(-5.0 V @ 4.0 A, with 5.0 A Current Limiting)



* Mounted on common heat sink

When a boost transistor is used, short-circuit currents are equal to the sum of the series pass and regulator limits, which are measured at 3.2 A and 1.8 A respectively in this case. Series pass limiting is approximately equal to $0.6 V/R_{SC}$. Operation beyond this point to the peak current capability of the ECG19XX is possible if the regulator is mounted on a heat sink; otherwise thermal shutdown will occur when the additional load current is picked up by the regulator.

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