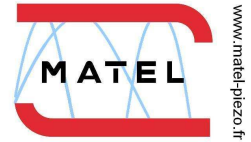


SMD Clock Oscillator CXO 7.0x5.0x1.4

XA-11 & XA-21 & XA-41 & XA-71



FEATURES
1.25 to 60 MHz LVCMOS Tri-State Enable/Disable
APPLICATIONS
Military -55°C +125°C Superior phase noise

Electrical Parameters

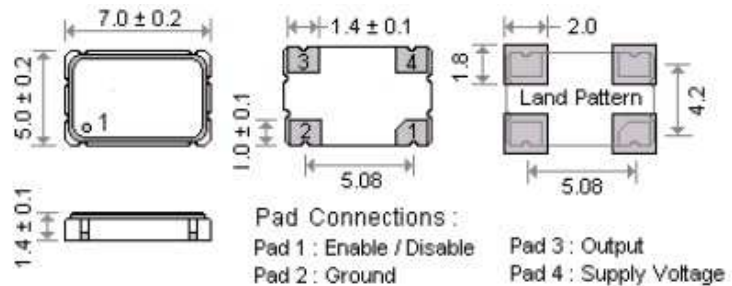
Parameters	Conditions	XA-11	XA-21	XA-41	XA-71
Frequency Range		1.25 to 60 MHz			
Frequency Stability*	All Conditions*	±50 ppm, ±100 ppm			
Storage Temperature Range		-55°C ~ +125°C			
Operating Temperature Range		-55°C ~ +125°C			
Power Supply Voltage		+5V ± 10%	+3.3V ± 10%	+2.5V ± 10%	+1.8V ± 10%
Supply Current	1.25 to 19.999 MHz	5 mA max	4 mA max	3 mA max	2 mA max
	20 MHz to 60MHz	8 mA max	6 mA max	5 mA max	4 mA max
Output Symmetry	At ½ V _{DD}	40/60% (45/55% option S)			
Rise Time/Fall Time	10% ↔ 90% of V _{DD}	10 ns max		7 ns max	
Output Voltage		90% V _{DD} min, 10% V _{DD} max			
Output Load CMOS		15 pF max			
Start-up Time		5 ms max			
Aging (first year)	25°C ± 3°C	± 2 ppm max			
Jitter RMS (12 KHz to 20 MHz)		150 fs typical at 26MHz, 3.3V			
Phase noise typical at 26MHz,3.3V		-94 dBc / Hz @ 10Hz			
		-127 dBc / Hz @ 100Hz			
		-142 dBc / Hz @ 1KHz			
		-156dBc / Hz @ 10KHz			
		-161 dBc / Hz @ 100kHz			
		-163 dBc / Hz @ 1MHz			
		-163 dBc / Hz @ 5MHz			

Temperature Range	Code	Stability*	Code	Internal Code	Internal Code	Option	Code
-55 + 125°C	I	± 100 ppm	A	0	0	45/55%	S
		± 50 ppm	C				

*Includes: 25°C calibration, operating temperature range, input voltage and load changes, ageing, shocks and vibrations (first year)

Example: XA-21IA00 10MHz

Mechanical Dimensions (mm)



SMD CXO

TYPE	XA-11 & XA-21 & XA-41 & XA-71	REVISION	03	CHECKED	PB	DATE	04/11/2016
------	-------------------------------	----------	----	---------	----	------	------------

All specifications are subject to change without notice