

	DESCRIPTION	DEVICE	RESOLUTION (BIT)	DIFFERENTIAL LINEARITY ERROR(LSB)	CONVERSION SPEED(MHz)	I/O CH	CONVERSION METHOD	V _{ref} I _{ref}	SUPPLY VOLTAGE	I/O LEVEL	PACKAGE
V I D E O	A/D	KAD0228	8	± 0.6(TYP)	30	1/	TWO-STEP CMOS	2V	5V	TTL	24SOP
	D/A	KDA0408	8	± 1/2	60	3/3	R-2R SEGMENTATION SEGMENTATION	4V	5V	TTL	42SDIP/48VQFP
		KDA0310 *KDA0308	10 8	± 1 ± 1	80 80			1.235V 1.235V	5V 5V	TTL TTL	30SOP 24SOP
D M M	AUTO RANG	KAD7001	3280cnts	± 10.08%FS(DCV)	519.7mS	1/	INTEGRATING	1.29V	3V	LCD	80QFP

* : NEW PRODUCT