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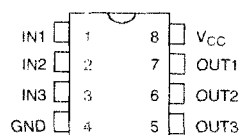
DS1033

3-in-1 Low Voltage Silicon Delay Line

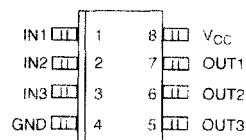
FEATURES

- All-silicon timing circuit
- Three independent buffered delays
- Initial delay tolerance ± 1.5 ns
- Stable and precise over temperature and voltage
- Leading and trailing edge precision preserves the input symmetry
- Standard 8-pin DIP, 8-pin SOIC
- Vapor phasing, IR and wave solderable
- Available in tape and reel

PIN ASSIGNMENT



DS1033M 8-PIN DIP
See Mech. Drawings
Section



DS1033Z 8-PIN SOIC (150 MIL)
See Mech. Drawings
Section

DESCRIPTION

The DS1033 series is a low-power +3.3 Volt version of the DS1035. It is characterized for operation over the range of 2.7V to 3.6V.

The DS1033 series of delay lines have three independent logic buffered delays in a single package. It is available in a standard 8-pin DIP and 150 mil, 8-pin mini-SOIC.

The device features precise leading and trailing edge accuracies. It has the inherent reliability of an all-silicon

PIN DESCRIPTION

IN1-IN3	-	Input Signals
OUT1-OUT3	-	Output Signals
NC	-	No Connection
V _{CC}	-	Supply Voltage
GND	-	Ground
(Sub)	-	Internal substrate connection, do not make any external connections to these pins

delay line solution. The DS1033's nominal tolerance is ± 1.5 ns and an additional tolerance over temperature and voltage of ± 1.0 ns for the faster delays. Detailed specifications are shown in Table 1.

Standard delay values are indicated in Table 1. Customers may contact Dallas Semiconductor at (972) 371-4348 for further information.