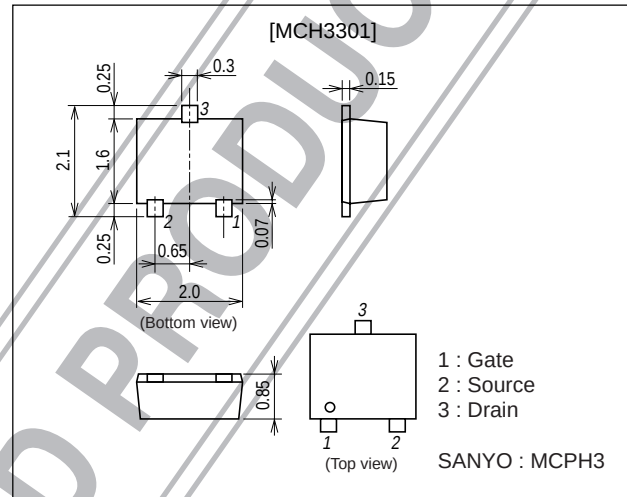


SANYO**MCH3301****Ultrahigh-Speed Switching Applications****Features**

- Low ON resistance.
- Ultrahigh-speed switching.
- 2.5V drive.

Package Dimensionsunit : mm
2167A**Specifications****Absolute Maximum Ratings** at $T_a=25^\circ\text{C}$

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V_{DS}		-20	V
Gate-to-Source Voltage	V_{GS}		± 10	V
Drain Current (DC)	I_D		-1	A
Drain Current (Pulse)	I_{DP}	$PW \leq 10\mu\text{s}$, duty cycle $\leq 1\%$	-4	A
Allowable Power Dissipation	P_D	Mounted on a ceramic board (900mm $\times$ 0.8mm)	1	W
Channel Temperature	T_{ch}		150	$^\circ\text{C}$
Storage Temperature	T_{stg}		-55 to +150	$^\circ\text{C}$

Electrical Characteristics at $T_a=25^\circ\text{C}$

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DS}$	$I_D = -1\text{mA}$, $V_{GS} = 0$	-20			V
Zero-Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -20\text{V}$, $V_{GS} = 0$			-10	μA
Gate-to-Source Leakage Current	I_{GSS}	$V_{GS} = \pm 8\text{V}$, $V_{DS} = 0$			± 10	μA
Cutoff Voltage	$V_{GS(off)}$	$V_{DS} = -10\text{V}$, $I_D = -1\text{mA}$	-0.4		-1.4	V
Forward Transfer Admittance	$ y_{fs} $	$V_{DS} = -10\text{V}$, $I_D = -500\text{mA}$	0.9	1.3		S
Static Drain-to-Source On-State Resistance	$R_{DS(on)1}$	$I_D = -500\text{mA}$, $V_{GS} = -4\text{V}$		400	520	$\text{m}\Omega$
	$R_{DS(on)2}$	$I_D = -300\text{mA}$, $V_{GS} = -2.5\text{V}$		600	840	$\text{m}\Omega$

Marking : JA

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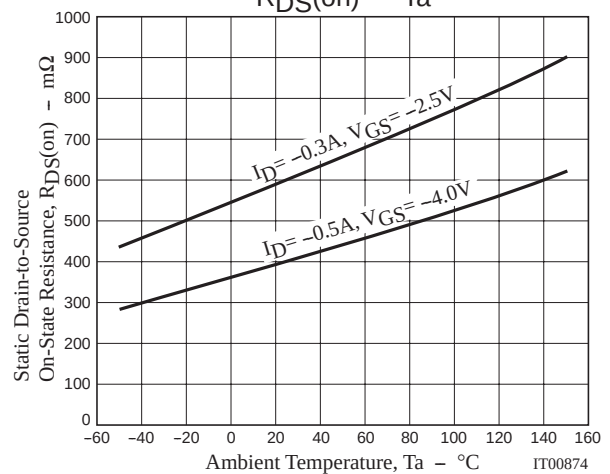
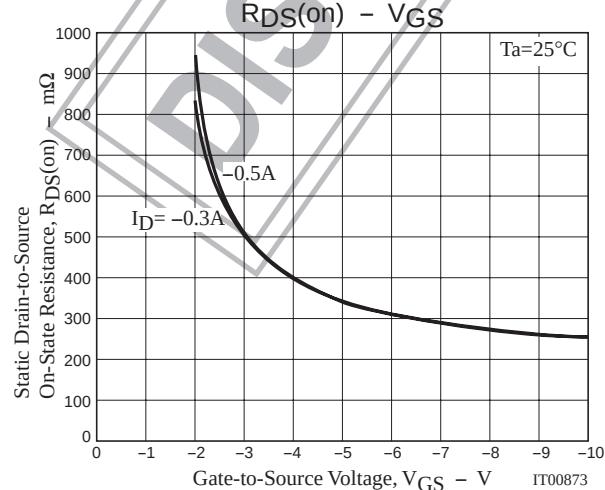
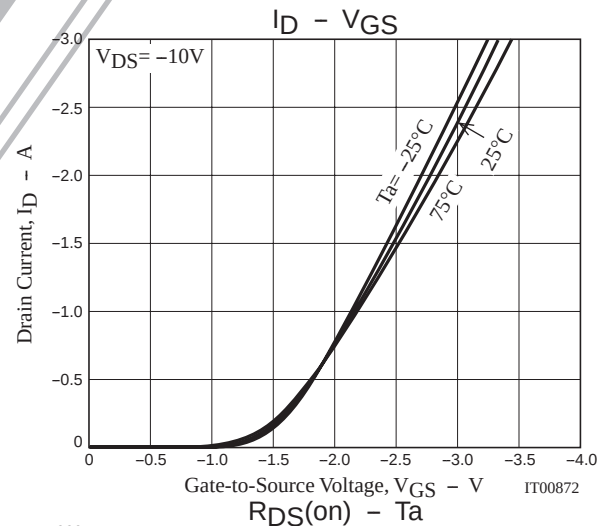
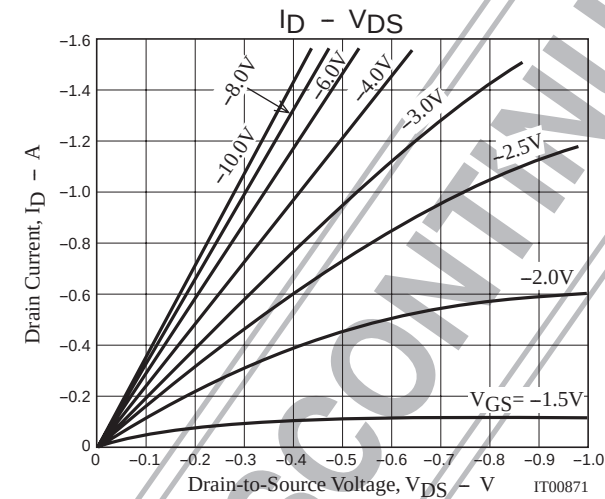
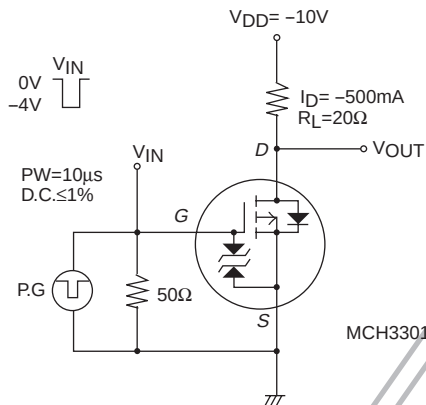
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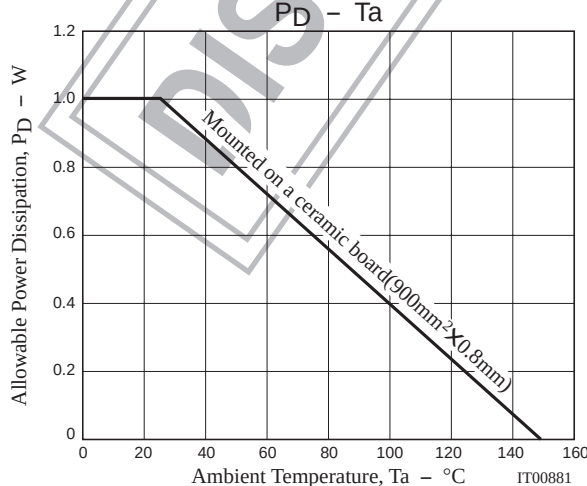
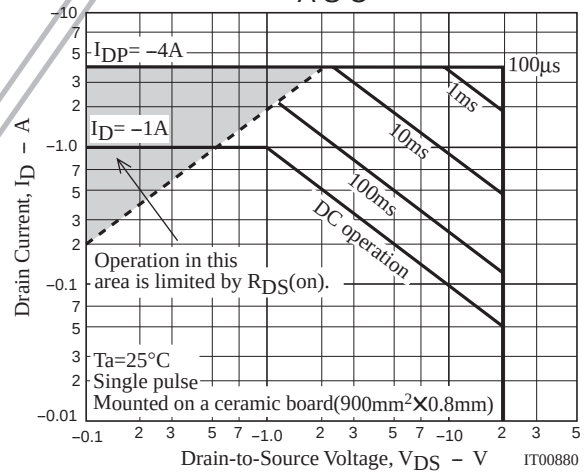
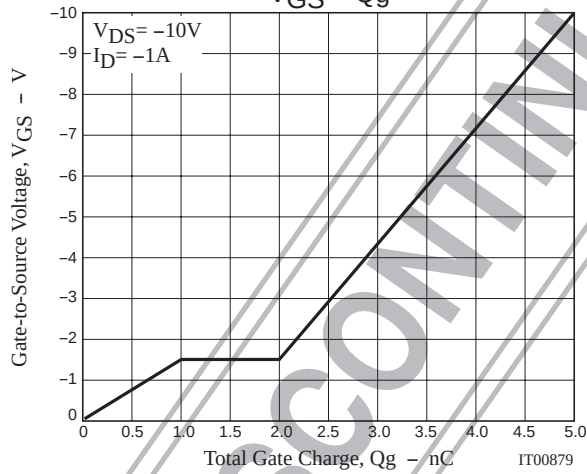
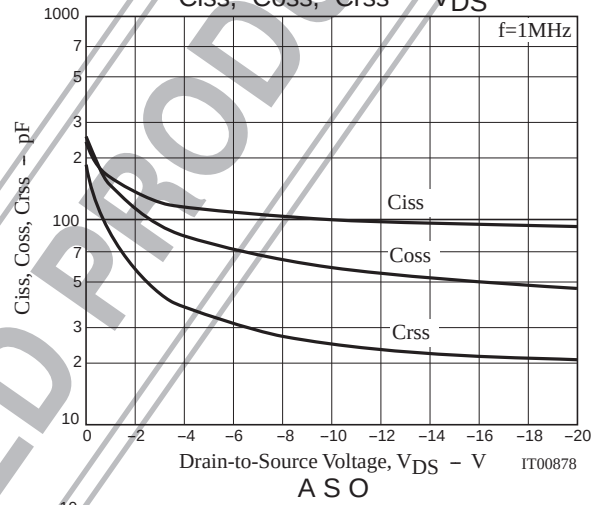
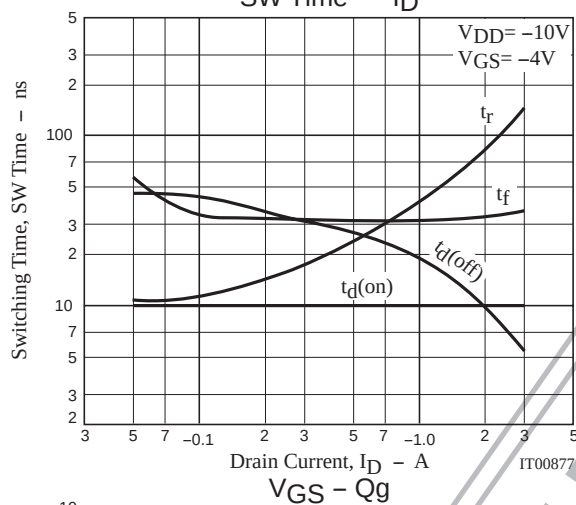
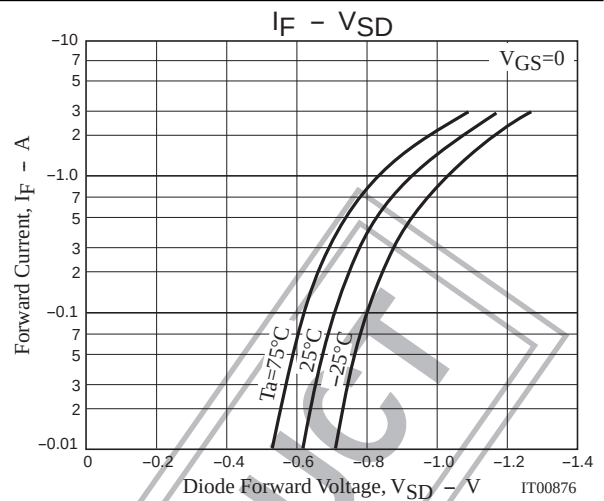
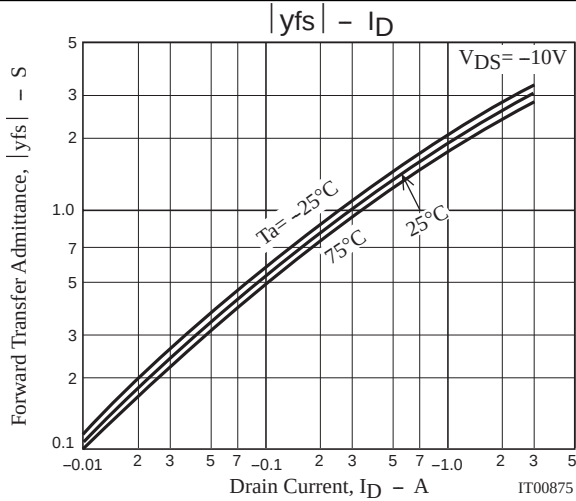
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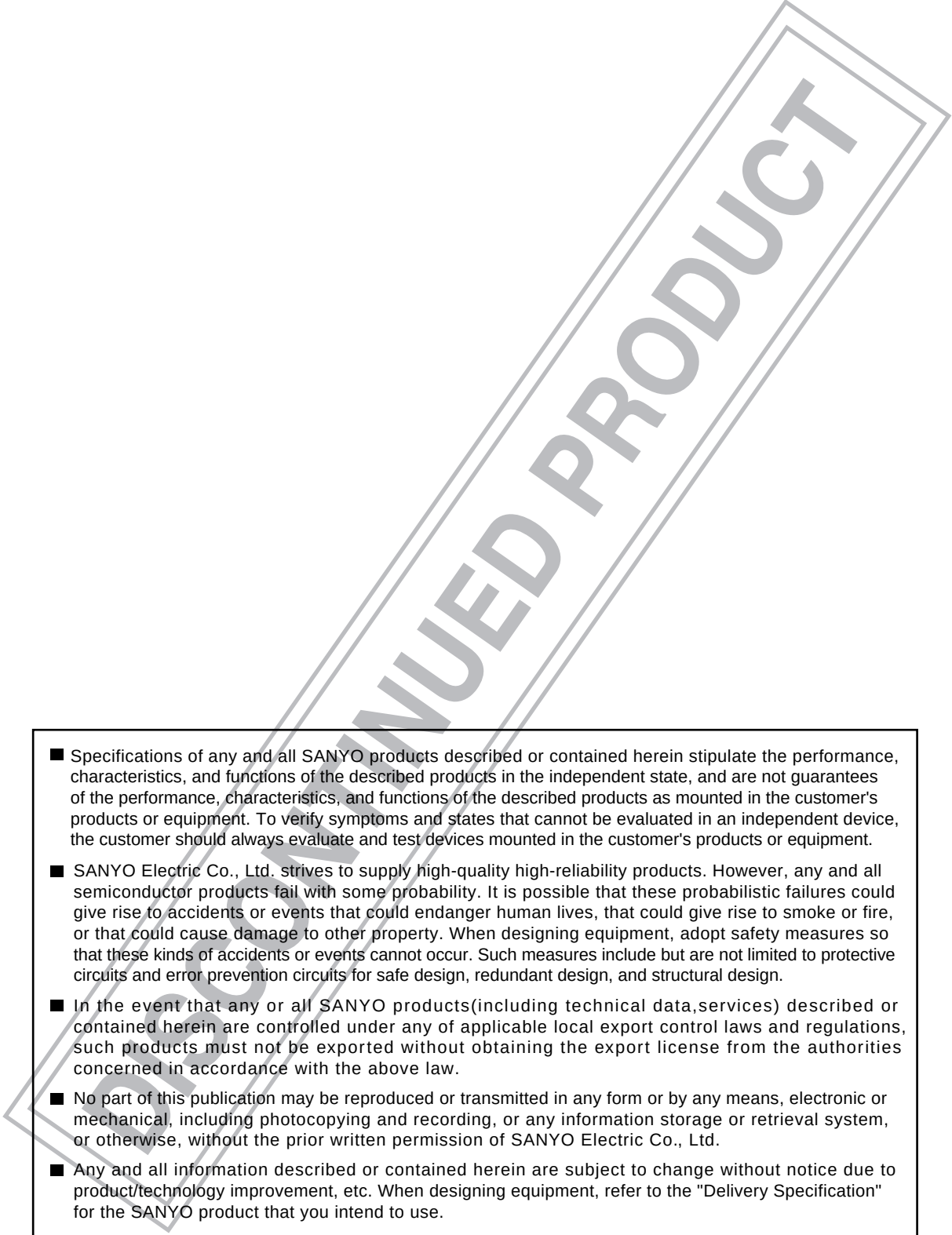
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Input Capacitance	Ciss	$V_{DS} = -10V, f = 1MHz$		100		pF
Output Capacitance	Coss	$V_{DS} = -10V, f = 1MHz$		60		pF
Reverse Transfer Capacitance	Crss	$V_{DS} = -10V, f = 1MHz$		25		pF
Turn-ON Delay Time	$t_d(on)$	See specified Test Circuit.		10		ns
Rise Time	t_r	See specified Test Circuit.		25		ns
Turn-OFF Delay Time	$t_d(off)$	See specified Test Circuit.		27		ns
Fall Time	t_f	See specified Test Circuit.		32		ns
Total Gate Charge	Qg	$V_{DS} = -10V, V_{GS} = -10V, I_D = -1A$		5		nC
Gate-to-Source Charge	Qgs	$V_{DS} = -10V, V_{GS} = -10V, I_D = -1A$		1		nC
Gate-to-Drain "Miller" Charge	Qgd	$V_{DS} = -10V, V_{GS} = -10V, I_D = -1A$		1		nC
Diode Forward Voltage	V_{SD}	$I_S = -1A, V_{GS} = 0$		-0.9	-1.5	V

Switching Time Test Circuit





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